





Power MOSFETS


DATASHEET

LM20480CLG6A

N-Channel AND P-Channel
Enhancement Mode MOSFET

 Leadpower-semiconductor Corp., Ltd

 sales@leadpower-semi.com

 (03) 6577339 FAX : (03) 6577229

 www.leadpower-semi.com

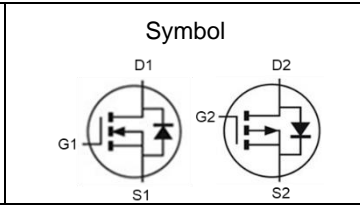
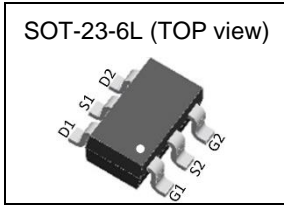


Quality Management Systems

ISO 9001:2015 Certificate

N-Channel AND P-Channel Enhancement Mode MOSFET

Pin Description



Ordering Information

Symbol	N-Channel	P-Channel	Unit
V_{DSS}	20	-20	V
$R_{DS(ON)}$	38	76	m Ω
I_D	4.2	-3	A

Feature

- Reliable and Rugged
- ROHS Compliant & Halogen-Free

Applications

- Load/Power Switching with Level Shift
- Single or Dual Cell Li-Ion Battery Supplied Devices

Ordering Information

Orderable Part Number	Package Type	Form	Shipping	Marking
LM20480CLG6A	SOT-23-6L	Tape & Reel	3000 / Tape & Reel	02□□□

Absolute Maximum Ratings (T_J=25°C Unless Otherwise Noted)

Symbol	Parameter		N-Channel	P- Channel	Unit
V_{DSS}	Drain-Source Voltage		20	-20	V
V_{GSS}	Gate-Source Voltage		±12	±12	
T_J	Maximum Junction Temperature		150	150	°C
T_{STG}	Storage Temperature Range		-55 to 150	-55 to 150	°C
$I_{DM}^{①}$	Pulse Drain Current Tested	$T_A=25^\circ\text{C}$	11	-7.5	A
I_D	Continuous Drain Current	$T_A=25^\circ\text{C}$	4.2	-3	A
		$T_A=100^\circ\text{C}$	3.4	-2.4	
P_D	Maximum Power Dissipation	$T_A=25^\circ\text{C}$	1		W
		$T_A=100^\circ\text{C}$	0.64		
I_{AS}	Avalanche Current, Single pulse	L=0.1mH	8	-10	A
$E_{AS}^{②}$	Avalanche Energy, Single pulse	L=0.1mH	3.2	5	mJ

Thermal Characteristics

Symbol	Parameter		Rating	Unit
$R_{\theta JA}^{③}$	Thermal Resistance-Junction to Ambient	Steady State	125	°C/W

Note ① : Max. current is limited by junction temperature.

Note ② : UIS tested and pulse width are limited by maximum junction temperature 150°C

Note ③ : Surface Mounted on 1in² FR-4 board with 1oz

N-Channel Electrical Characteristics (T_J=25°C Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Static Electrical Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =250uA	20	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	V _{DS} =16V, V _{GS} =0V	-	-	1	uA
V_{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250uA	0.4	0.6	1	V
I_{GSS}	Gate Leakage Current	V _{GS} =±12V, V _{DS} =0V	-	-	100	nA
R_{DS(ON)} ^④	Drain-Source On-state Resistance	V _{GS} =4.5V, I _{DS} =5A	-	30	38	mΩ
		V _{GS} =2.5V, I _{DS} =4A	-	37	48	
		V _{GS} =1.8V, I _{DS} =1A	-	43	65	
gfs	Forward Transconductance	V _{DS} =5V, I _{DS} =2.5A	-	7	-	S
Dynamic Characteristics ^⑤						
R_G	Gate Resistance	V _{GS} =0V, V _{DS} =0V, Freq.=1MHz	-	2	-	Ω
C_{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =10V, Freq.=1MHz	-	280	-	pF
C_{oss}	Output Capacitance		-	45	-	
C_{rss}	Reverse Transfer Capacitance		-	40	-	
t_{d(ON)}	Turn-on Delay Time	V _{GS} =4.5V, V _{DS} =10V, I _D =1A, R _{GEN} =6Ω	-	3.5	-	nS
t_r	Turn-on Rise Time		-	19.5	-	
t_{d(OFF)}	Turn-off Delay Time		-	22.8	-	
t_f	Turn-off Fall Time		-	24.2	-	
Q_g	Total Gate Charge	V _{GS} =2.5V, V _{DS} =10V I _D =5A	-	3.1	-	nC
Q_g	Total Gate Charge	V _{GS} =4.5V, V _{DS} =10V, I _D =5A	-	5.3	-	
Q_{gs}	Gate-Source Charge		-	0.8	-	
Q_{gd}	Gate-Drain Charge		-	1.5	-	
Source-Drain Characteristics						
V_{SD} ^④	Diode Forward Voltage	I _{SD} =2.5A, V _{GS} =0V	-	0.75	1.1	V
t_{rr}	Reverse Recovery Time	I _F =2.5A, V _R =10	-	10.8	-	nS
Q_{rr}	Reverse Recovery Charge	dI _F /dt=100A/μs	-	2.9	-	nC

Note ④ : Pulse test (pulse width≤300us, duty cycle≤2%).

Note ⑤ : Guaranteed by design, not subject to production testing.

N-Channel Typical Characteristics

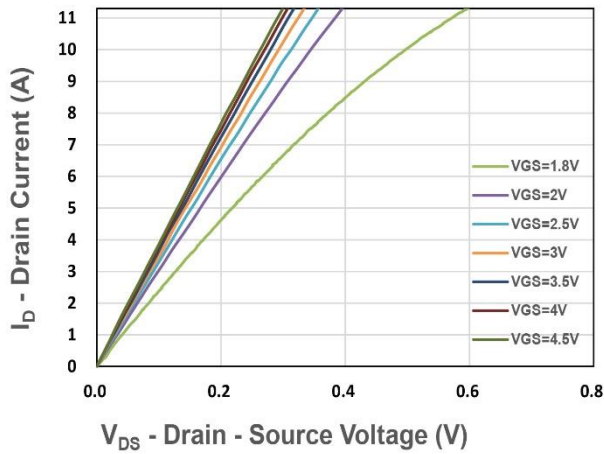


Figure 1. Output Characteristics

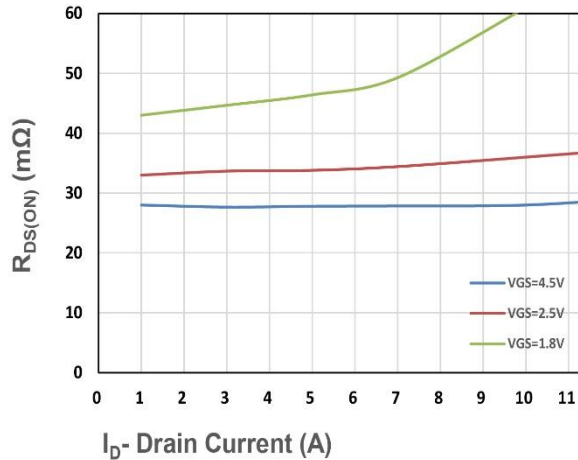


Figure 2. On-Resistance vs. I_D

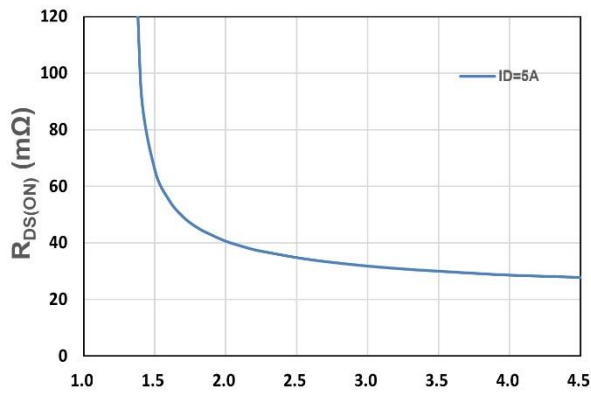


Figure 3. On-Resistance vs. V_{GS}

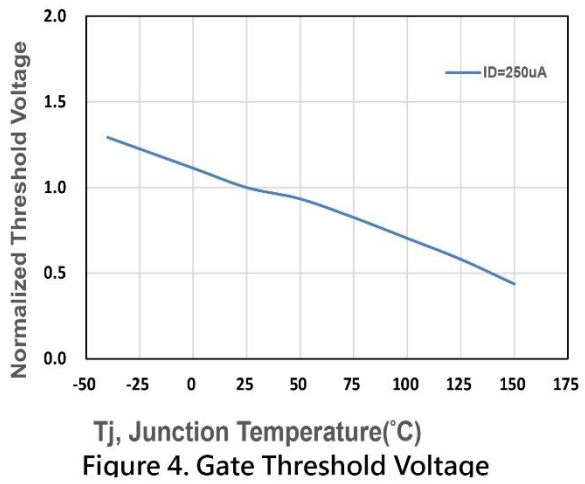


Figure 4. Gate Threshold Voltage

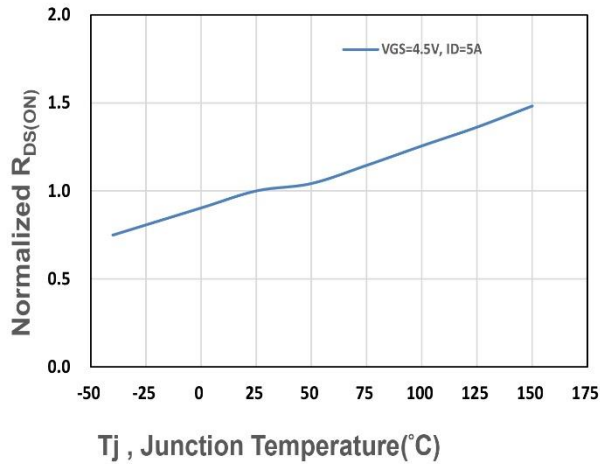


Figure 5. Drain-Source On Resistance

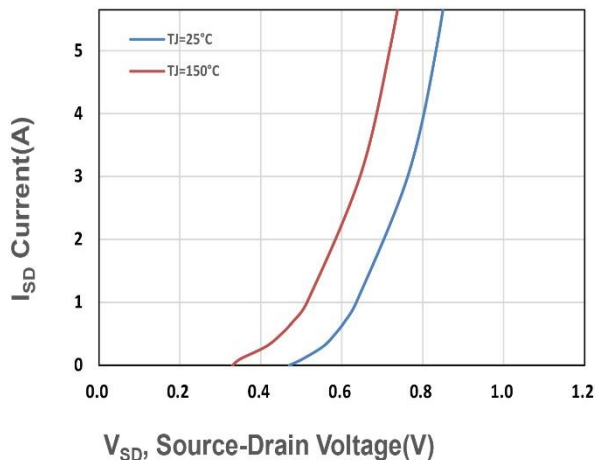
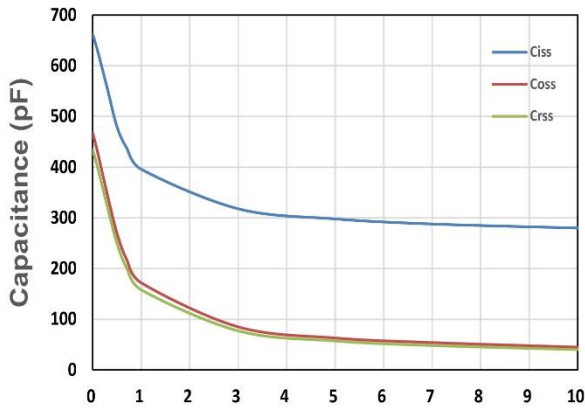
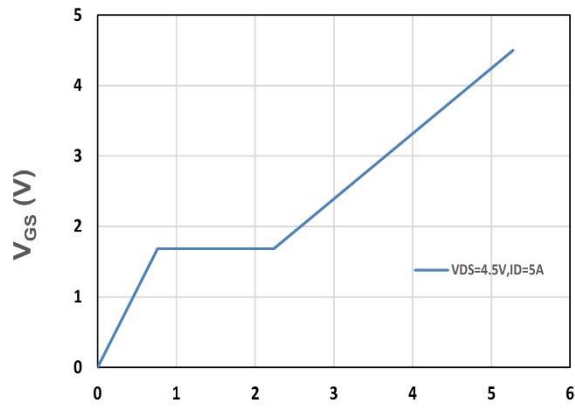


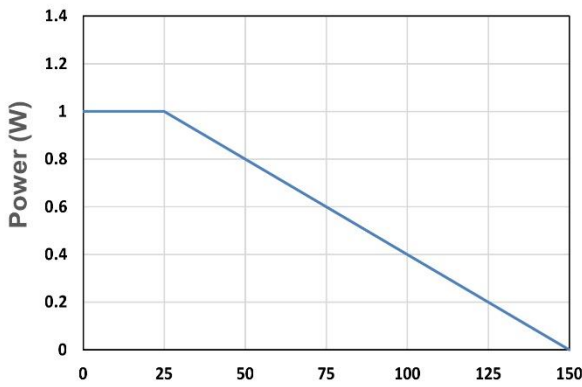
Figure 6. Source-Drain Diode Forward



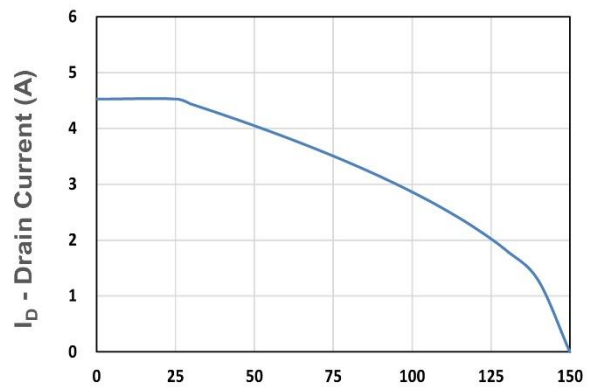
V_{DS} - Drain - Source Voltage (V)
Figure 7. Capacitance



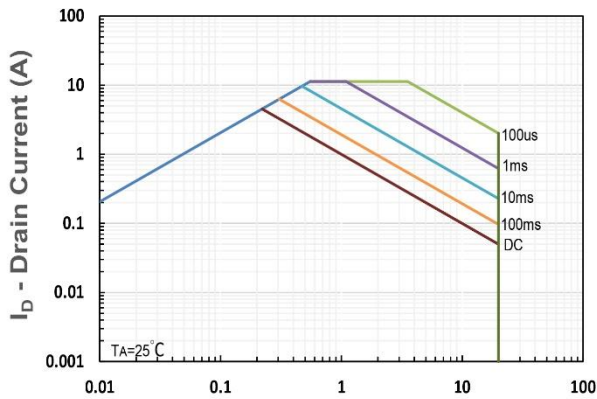
Q_g , Total Gate Charge (nC)
Figure 8. Gate Charge Characteristics



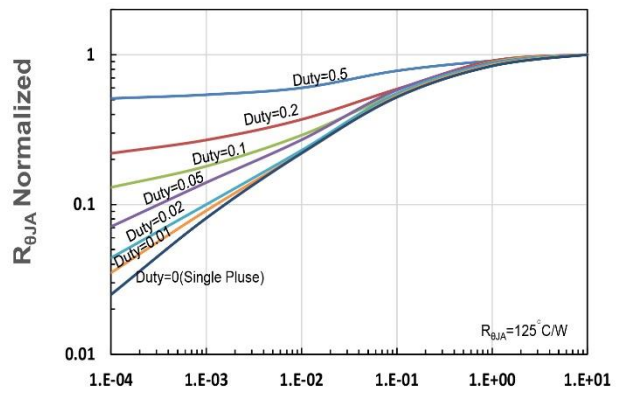
T_A - Ambient Temperature (°C)
Figure 9. Power Dissipation



T_A - Ambient Temperature (°C)
Figure 10. Drain Current



V_{DS} - Drain-Source Voltage (V)
Figure 11. Safe Operating Area

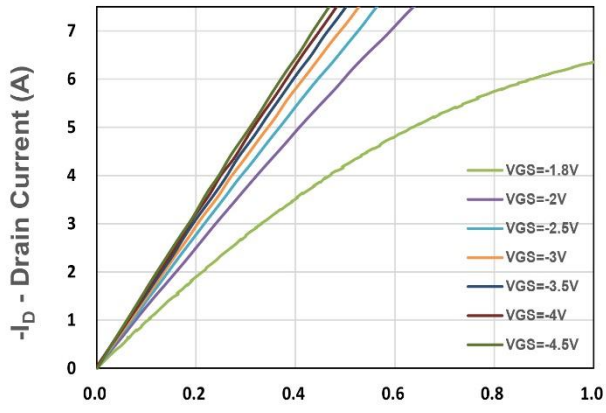


t_1 , Square Wave Pulse Duration(s)
Figure 12. $R_{\theta JA}$ Transient Thermal Impedance

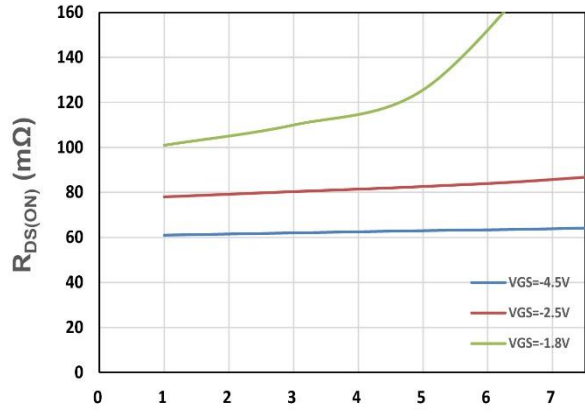
P-Channel Electrical Characteristics (T_J=25°C Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Static Electrical Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =-250uA	-20	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	V _{DS} =-16V, V _{GS} =0V	-	-	-1	uA
V_{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =-250uA	-0.45	-0.7	-1	V
I_{GSS}	Gate Leakage Current	V _{GS} =±12V, V _{DS} =0V	-	-	±100	nA
R_{DS(ON)} ^④	Drain-Source On-state Resistance	V _{GS} =-4.5V, I _{DS} =-3.3A	-	63	76	mΩ
		V _{GS} =-2.5V, I _{DS} =-2.1A	-	80	104	
		V _{GS} =-1.8V, I _{DS} =-1A	-	100	150	
gfs	Forward Transconductance	V _{DS} =-5V, I _{DS} =-1.6A	-	6.7	-	S
Dynamic Characteristics ^⑥						
R_G	Gate Resistance	V _{GS} =0V, V _{DS} =0V, Freq.=1MHz	-	6.7	-	Ω
C_{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =-10V, Freq.=1MHz	-	602	-	pF
C_{oss}	Output Capacitance		-	53	-	
C_{rss}	Reverse Transfer Capacitance		-	48	-	
t_{d(ON)}	Turn-on Delay Time	V _{GS} =-4.5V, V _{DS} =-10V, I _D =-1A, R _{GEN} =6Ω	-	5.9	-	nS
t_r	Turn-on Rise Time		-	20.1	-	
t_{d(OFF)}	Turn-off Delay Time		-	39.4	-	
t_f	Turn-off Fall Time		-	25.3	-	
Q_g	Total Gate Charge	V _{GS} =-2.5V, V _{DS} =-10V I _D =-3.3A	-	4.7	-	nC
Q_g	Total Gate Charge	V _{GS} =-4.5V, V _{DS} =-10V, I _D =-3.3A	-	8.4	-	
Q_{gs}	Gate-Source Charge		-	1.4	-	
Q_{gd}	Gate-Drain Charge		-	1.9	-	
Source-Drain Characteristics						
V_{SD} ^④	Diode Forward Voltage	I _{SD} =-1.6A, V _{GS} =0V	-	-0.85	-1.1	V
t_{rr}	Reverse Recovery Time	I _F =-1.6A, V _R =-10V	-	10.1	-	nS
Q_{rr}	Reverse Recovery Charge	dI _F /dt=100A/μs	-	4.1	-	nC

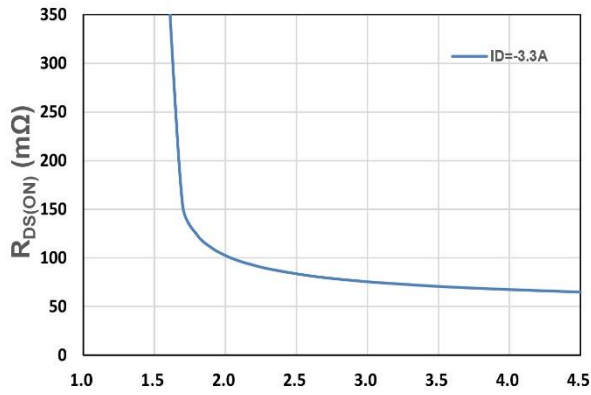
P-Channel Typical Characteristics



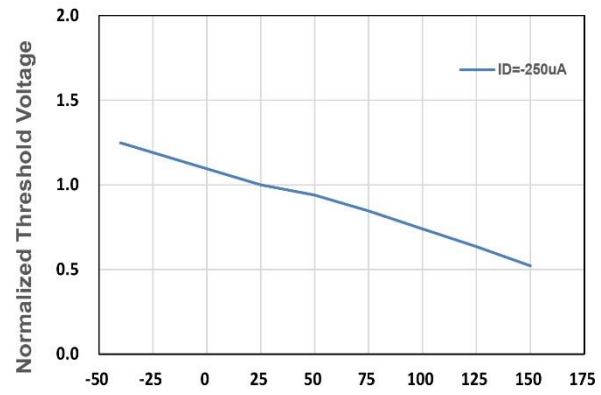
$-V_{DS}$ - Drain - Source Voltage (V)
Figure 1. Output Characteristics



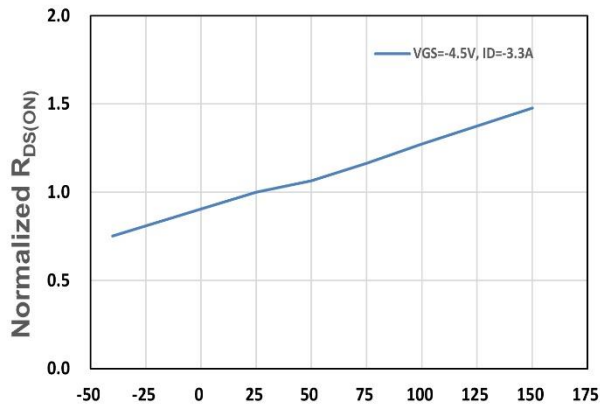
$-I_D$ - Drain Current (A)
Figure 2. On-Resistance vs. I_D



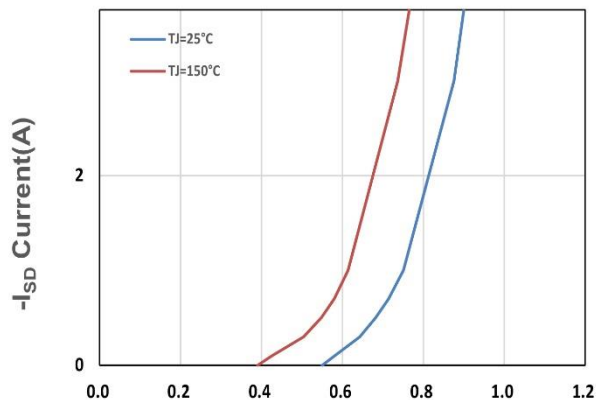
$-V_{GS}$ - Gate - Source Voltage (V)
Figure 3. On-Resistance vs. V_{GS}



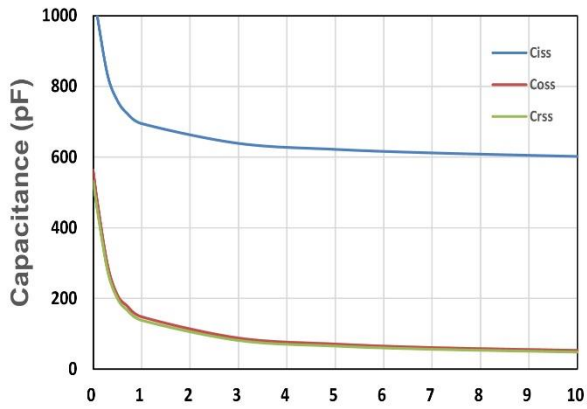
T_j , Junction Temperature(°C)
Figure 4. Gate Threshold Voltage



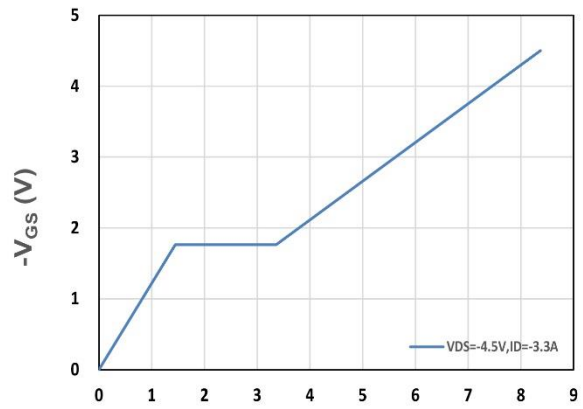
T_j , Junction Temperature(°C)
Figure 5. Drain-Source On Resistance



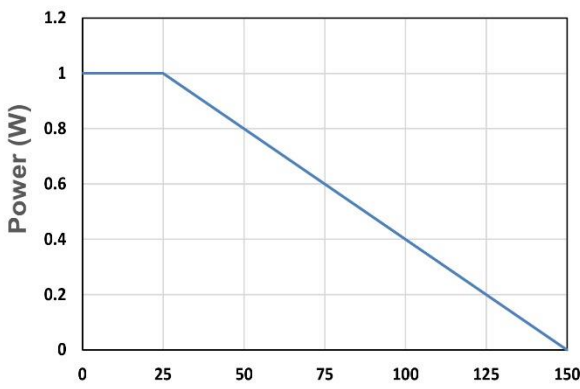
$-V_{SD}$, Source-Drain Voltage(V)
Figure 6. Source-Drain Diode Forward



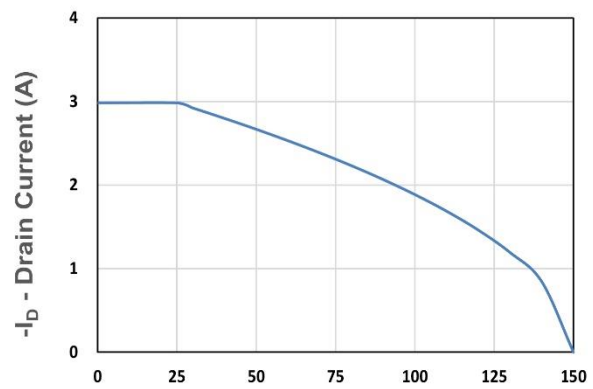
$-V_{DS}$ - Drain - Source Voltage (V)
Figure 7. Capacitance



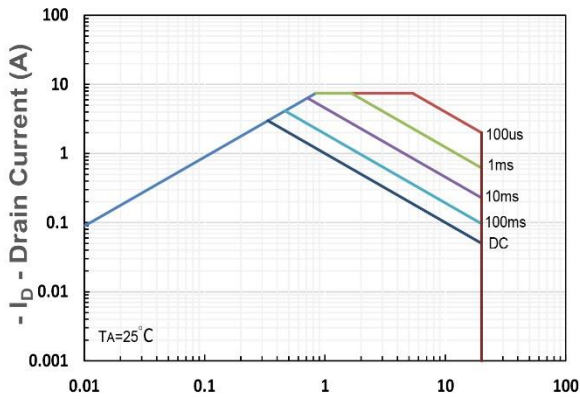
Q_g , Total Gate Charge (nC)
Figure 8. Gate Charge Characteristics



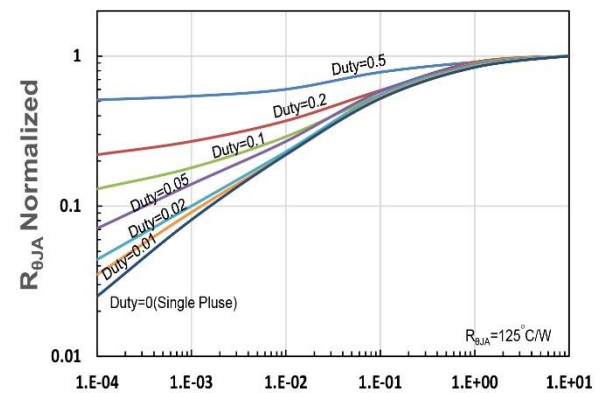
T_A - Ambient Temperature (°C)
Figure 9. Power Dissipation



T_A - Ambient Temperature (°C)
Figure 10. Drain Current



$-V_{DS}$ - Drain-Source Voltage (V)
Figure 11. Safe Operating Area



t_1 , Square Wave Pulse Duration (s)
Figure 12. $R_{\theta JA}$ Transient Thermal Impedance