Power MOSFETS

DATASHEET

LM1A090NAQ8A

N-Channel

Enhancement Mode MOSFET

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Quality Management Systems

ISO 9001:2015 Certificate

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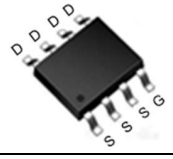
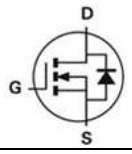
Pin Description

SOP-8L (TOP view)	Symbol
	

Ordering Information

Symbol	N-Channel	Unit
V_{DSS}	**100**	**V**
$R_{DS(ON)-Max}$	**8.4**	**mΩ**
I_D	**10.7**	**A**

Feature

- Optimized high performance of Rdson and Qg
- Reliable and Rugged
- ROHS Compliant & Halogen-Free
- 100% UIS Tested

Applications

- Motor drivers
- DC DC converter

Ordering Information

Orderable Part Number	Package Type	Form	Shipping	Marking
LM1A090NAQ8A	SOP-8L	Tape & Reel	3000 / Tape & Reel	1A090 □□□□□□

Absolute Maximum Ratings (T_J=25°C Unless Otherwise Noted)

Symbol	Parameter		N-Channel	Unit
V_{DSS}	Drain-Source Voltage		100	V
V_{GSS}	Gate-Source Voltage		±20	
T_J	Maximum Junction Temperature		150	°C
T_{STG}	Storage Temperature Range		-55 to 150	°C
I_{DM}①	Pulse Drain Current Tested	T_A=25°C	27	A
	Pulse Drain Current Tested(10us Pulse , Duty Cycle=1%)	T_A=25°C	102	A
	Pulse Drain Current Tested(10us Pulse , Duty Cycle=1%)	T_A=70°C	82	
	Pulse Drain Current Tested(10us Pulse , Duty Cycle=1%)	T_A=100°C	65	
I_D	Continuous Drain Current	T_A=25°C	10.7	A
		T_A=70°C	8.6	
P_D	Maximum Power Dissipation	T_A=25°C	1.7	W
		T_A=70°C	1.1	
I_{AS}②	Avalanche Current, Single pulse	L=0.1mH	19	A
		L=0.5mH	10	
E_{AS}②	Avalanche Energy, Single pulse	L=0.1mH	18	mJ
		L=0.5mH	25	

Thermal Characteristics

Symbol	Parameter		Rating	Unit
$R_{\theta JA}$③	Thermal Resistance-Junction to Ambient	Steady State	75	°C/W

Note ① : Max. current is limited by junction temperature

Note ② : UIS tested and pulse width are limited by maximum junction temperature 150°C

Note ③ : Surface Mounted on 1in^2 FR-4 board with 1oz.

N-Channel Electrical Characteristics (T_J=25°C Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Static Electrical Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V, I_{DS}=250uA	100	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS}=80V, V_{GS}=0V	-	-	1	uA
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS}, I_{DS}=250uA	1	2	3	V
I_{GSS}	Gate Leakage Current	V_{GS}=±20V, V_{DS}=0V	-	-	±100	nA
$R_{DS(ON)}$④	Drain-Source On-state Resistance	V_{GS}=10V, I_{DS}=12A	-	7	8.4	mΩ
		V_{GS}=4.5V, I_{DS}=8A	-	10	13	
gfs	Forward Transconductance	V_{DS}=5V, I_{DS}=12A	-	34	-	S
Dynamic Characteristics ⑤						
R_G	Gate Resistance	V_{GS}=0V,V_{DS}=0V, Freq.=1MHz	-	2	-	Ω
C_{iss}	Input Capacitance	V_{GS}=0V, V_{DS}=50V, Freq.=1MHz	-	1932	-	pF
C_{oss}	Output Capacitance		-	387	-	
C_{rss}	Reverse Transfer Capacitance		-	30	-	
$td_{(ON)}$	Turn-on Delay Time	V_{GS}=10V,V_{DS}=50V, I_D=1A,R_{GEN}=3Ω	-	10.2	-	nS
t_r	Turn-on Rise Time		-	20	-	
$t_{d(OFF)}$	Turn-off Delay Time		-	31	-	
t_f	Turn-off Fall Time		-	14	-	
Q_g	Total Gate Charge	V_{GS}=4.5V,V_{DS}=50V I_D=20A	-	22.8	-	nC
Q_g	Total Gate Charge	V_{GS}=10V,V_{DS}=50V, I_D=20A	-	41.8	-	
Qgs	Gate-Source Charge		-	5.6	-	
Qgd	Gate-Drain Charge		-	12.3	-	
Source-Drain Characteristics						
V_{SD}④	Diode Forward Voltage	I_{SD}=12A, V_{GS}=0V	-	0.8	1.1	V
t_{rr}	Reverse Recovery Time	I_F=20A, V_R=50V dIF/dt=100A/μs	-	49	-	nS
Q_{rr}	Reverse Recovery Charge		-	58.7	-	nC

Note ④：Pulse test (pulse width≤300us, duty cycle≤2%).
Note ⑤：Guaranteed by design, not subject to production testing.

N-Channel Typical Characteristics

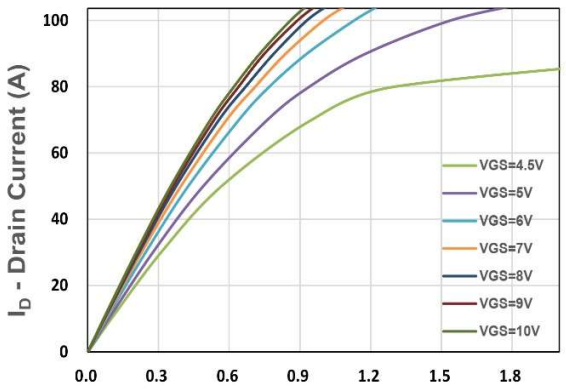


Figure 1. Output Characteristics

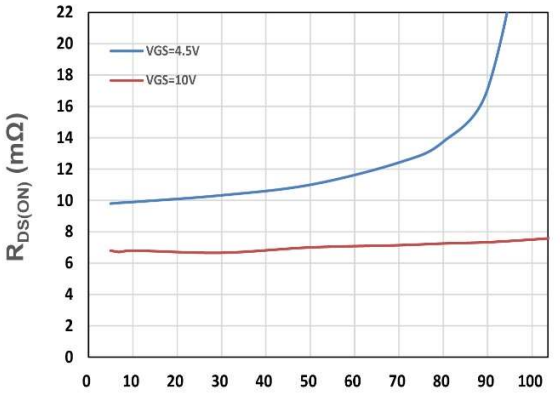


Figure 2. On-Resistance vs. ID

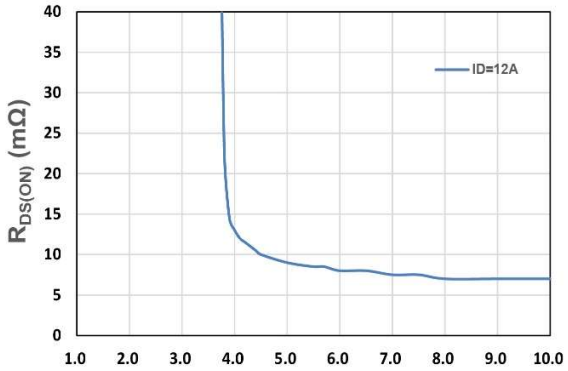


Figure 3. On-Resistance vs. VGS

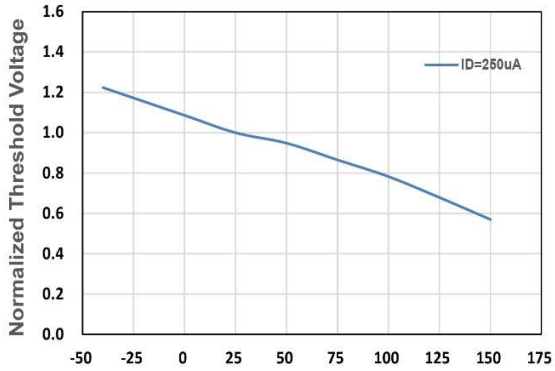


Figure 4. Gate Threshold Voltage

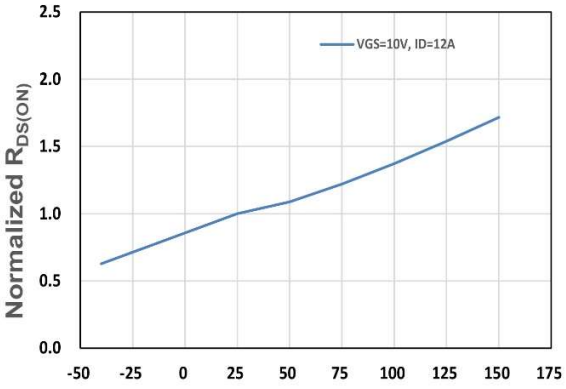


Figure 5. Drain-Source On Resistance

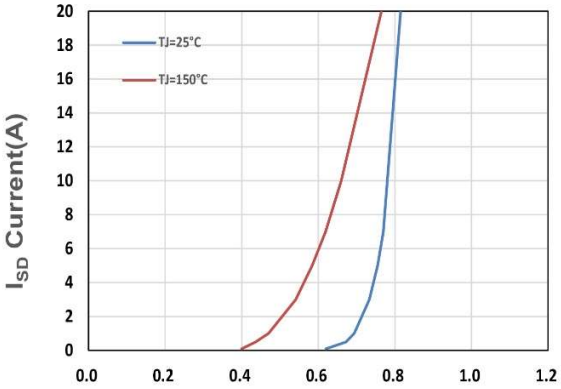


Figure 6. Source-Drain Diode Forward

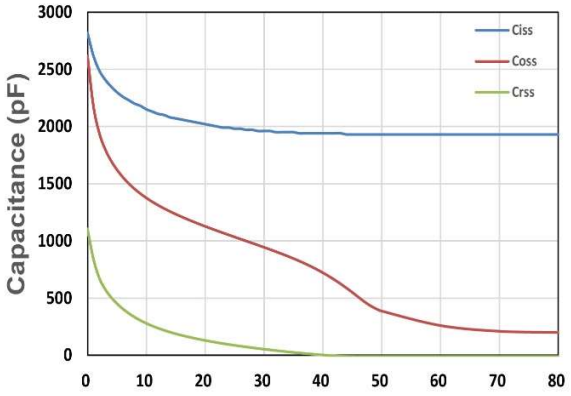


Figure 7. Capacitance

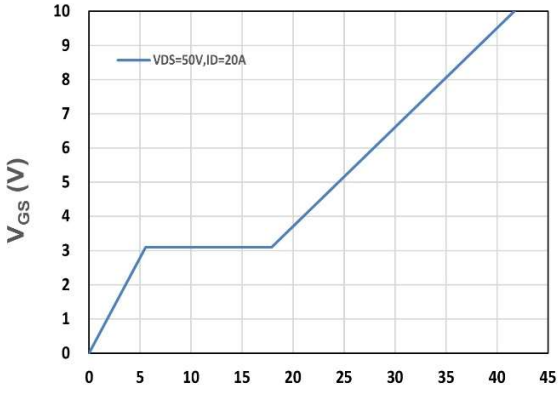


Figure 8. Gate Charge Characteristics

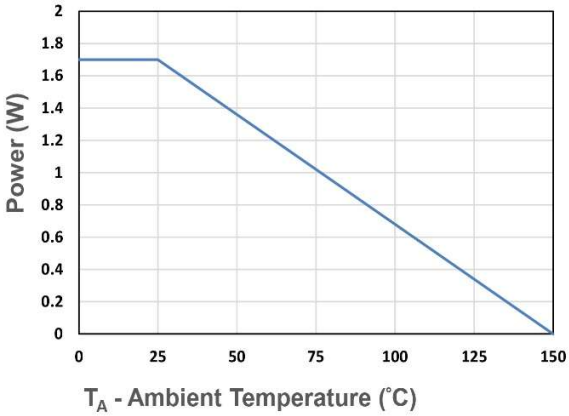


Figure 9. Power Dissipation

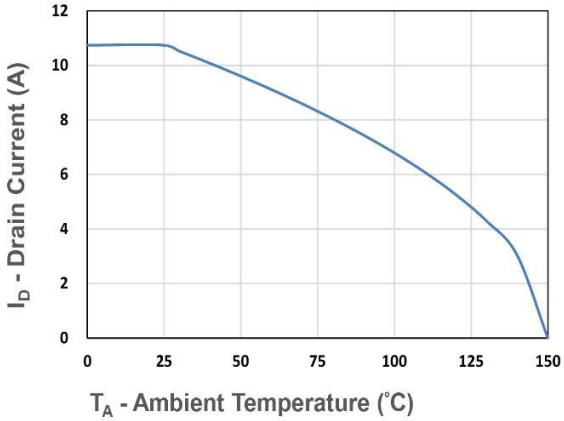


Figure 10. Drain Current

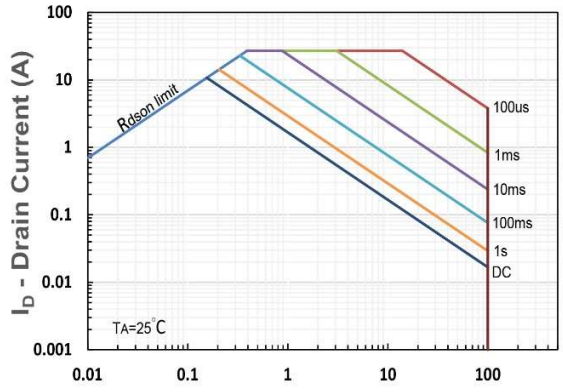


Figure 11. Safe Operating Area

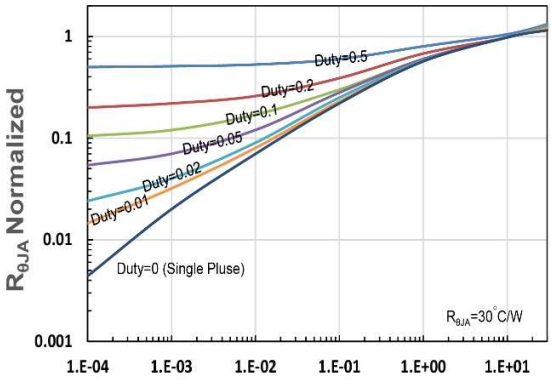


Figure 12. RθJA Transient Thermal Impedance