



Power MOSFETS

DATASHEET

LM1A075NAP3A

N-Channel
Enhancement Mode MOSFET

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Quality Management Systems

ISO 9001:2015 Certificate


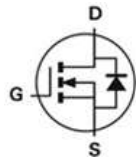
LM1A075NAP3A



N-Channel Enhancement Mode MOSFET

Pin Description

Product Summary

TO220-3L 	Symbol 	Symbol	N-Channel	Unit
		V _{DSS}	100	V
		R _{DS(ON)-Max}	8.5	mΩ
		I _D	91	A

Feature

- Fast switching speed
- Reliable and Rugged
- ROHS Compliant & Halogen-Free
- 100% UIS and Rg Tested

Applications

- Power Management in DC/DC Converters
- USB Power Delivery (USB PD)

Ordering Information

Orderable Part Number	Package Type	Form	Shipping	Marking
LM1A075NAP3A	TO220-3L	Tube	50 / Tube	1A075 □□□□□□

Note : □□□□□□ = Lot Code

Absolute Maximum Ratings (T_J=25°C Unless Otherwise Noted)

Symbol	Parameter	N-Channel	Unit
V _{DSS}	Drain-Source Voltage	100	V
V _{GSS}	Gate-Source Voltage	±20	
T _J	Maximum Junction Temperature	150	°C
T _{STG}	Storage Temperature Range	-55 to 150	°C
I _S	Diode Continuous Forward Current	T _C =25°C	71
I _{DM} ^①	Pulse Drain Current Tested	T _C =25°C	218
I _D	Continuous Drain Current	T _C =25°C	96
		T _C =100°C	61
P _D	Maximum Power Dissipation	T _C =25°C	78
		T _C =100°C	31
I _D	Continuous Drain Current	T _A =25°C	15.4
		T _A =70°C	12.3
P _D	Maximum Power Dissipation	T _A =25°C	2.0
		T _A =70°C	1.3
I _{AS} ^②	Avalanche Current, Single pulse	L=0.1mH	30
		L=0.5mH	19
E _{AS} ^②	Avalanche Energy, Single pulse	L=0.1mH	45
		L=0.5mH	90

Thermal Characteristics

Symbol	Parameter	Rating	Unit
R _{θJC}	Thermal Resistance-Junction to Case	Steady State	1.6
R _{θJA} ^③	Thermal Resistance-Junction to Ambient	Steady State	62

Note ① : Max. current is limited by bonding wire

Note ② : UIS tested and pulse width are limited by maximum junction temperature 150°C

Note ③ : Surface Mounted on 1in² FR-4 board with 1oz

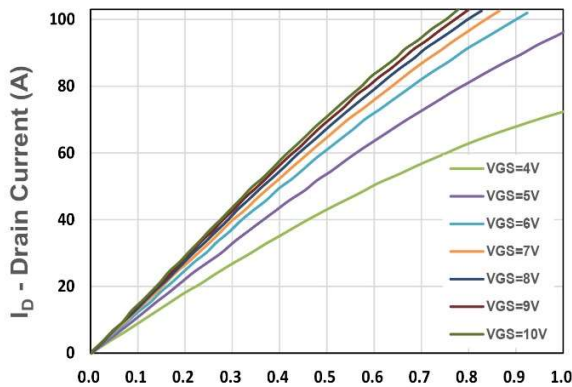
N-Channel Electrical Characteristics (T_J=25°C Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Static Electrical Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =250uA	100	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	V _{DS} =80V, V _{GS} =0V	-	-	1	uA
V_{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250uA	1	2	3	V
I_{GSS}	Gate Leakage Current	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
R_{DS(ON)} ^④	Drain-Source On-state Resistance	V _{GS} =10V, I _{DS} =20A	-	7	8.5	mΩ
		V _{GS} =4.5V, I _{DS} =10A	-	10	13	
gfs	Forward Transconductance	V _{DS} =5V, I _{DS} =10A	-	26	-	S
Dynamic Characteristics ^⑤						
R_G	Gate Resistance	V _{GS} =0V, V _{DS} =0V, Freq.=1MHz	-	1	-	Ω
C_{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =50V, Freq.=1MHz	-	2113	-	pF
C_{oss}	Output Capacitance		-	580	-	
C_{rss}	Reverse Transfer Capacitance		-	38	-	
td(ON)	Turn-on Delay Time	V _{GS} =10V, V _{DS} =30V, I _D =1A, R _{GEN} =1Ω	-	14.5	-	nS
t_r	Turn-on Rise Time		-	8.1	-	
t_{d(OFF)}	Turn-off Delay Time		-	13.6	-	
t_f	Turn-off Fall Time		-	107	-	
Q_g	Total Gate Charge	V _{GS} =4.5V, V _{DS} =50V, I _D =20A	-	22.5	-	nC
Q_g	Total Gate Charge	V _{GS} =10V, V _{DS} =50V, I _D =20A	-	43.3	-	
Q_{gs}	Gate-Source Charge		-	8.2	-	
Q_{gd}	Gate-Drain Charge		-	10.8	-	
Source-Drain Characteristics						
V_{SD} ^④	Diode Forward Voltage	I _{SD} =10A, V _{GS} =0V	-	0.75	1.1	V
t_{rr}	Reverse Recovery Time	I _F =10A, V _R =50V	-	45.5	-	nS
Q_{rr}	Reverse Recovery Charge	dI _F /dt=100A/μs	-	51.2	-	nC

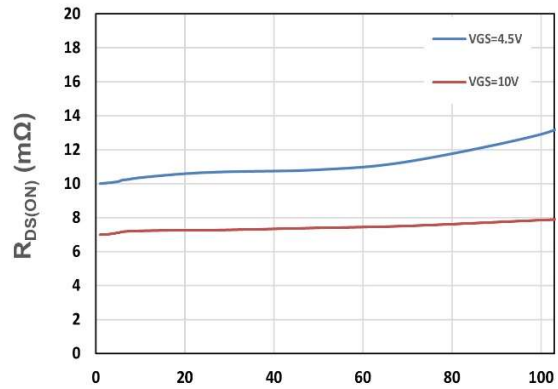
Note ④ : Pulse test (pulse width≤300us, duty cycle≤2%).

Note ⑤ : Guaranteed by design, not subject to production testing.

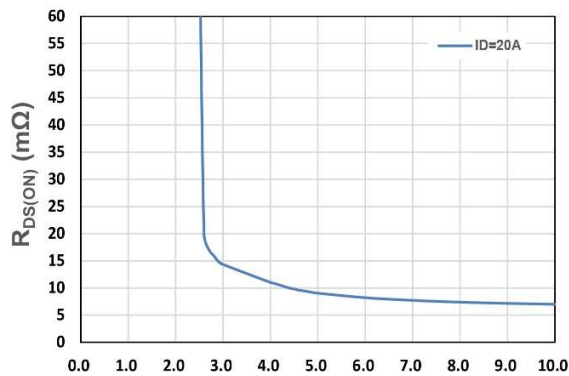
N-Channel Typical Characteristics



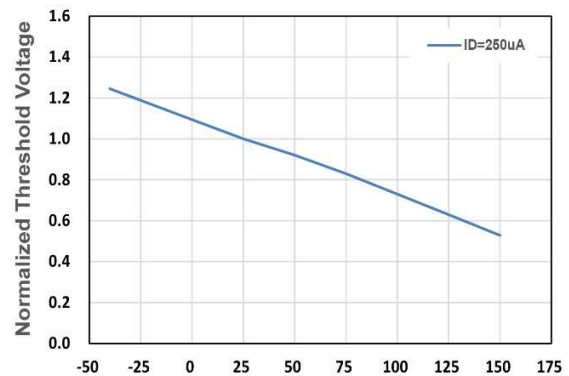
V_{DS} - Drain - Source Voltage (V)
Figure 1. Output Characteristics



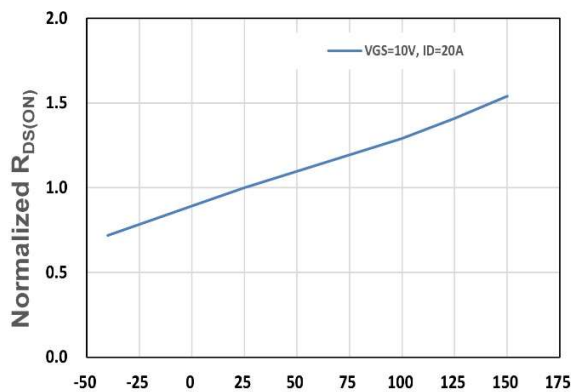
I_D - Drain Current (A)
Figure 2. On-Resistance vs. ID



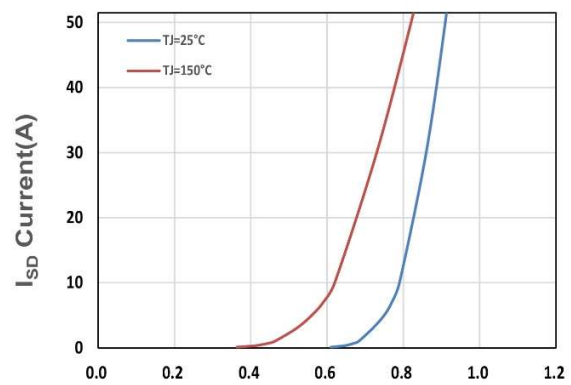
V_{GS} - Gate - Source Voltage (V)
Figure 3. On-Resistance vs. VGS



T_j, Junction Temperature(°C)
Figure 4. Gate Threshold Voltage

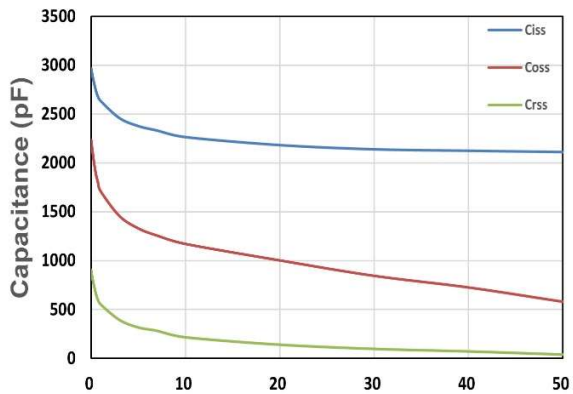


T_j, Junction Temperature(°C)
Figure 5. Drain-Source On Resistance

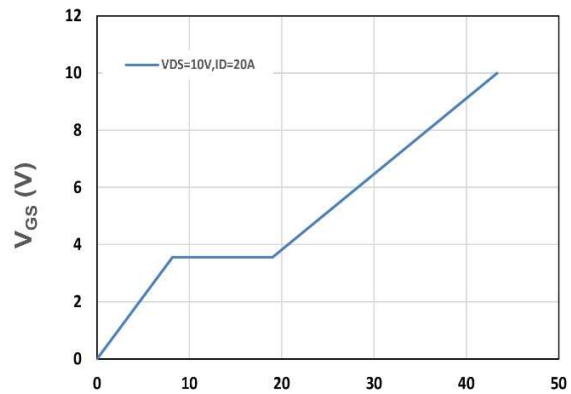


V_{SD}, Source-Drain Voltage(V)
Figure 6. Source-Drain Diode Forward

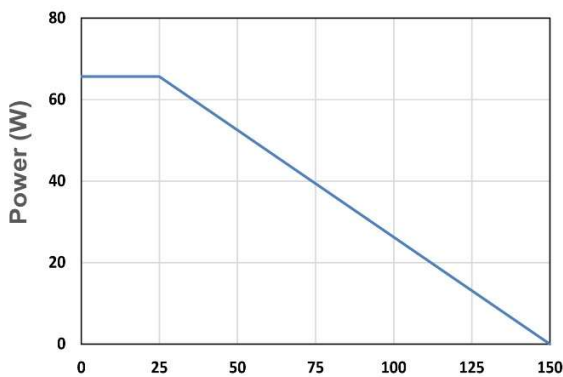
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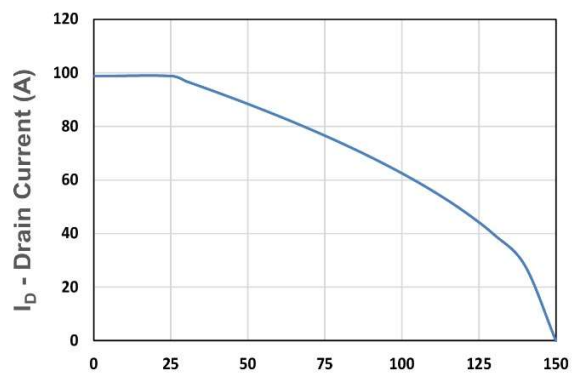
V_{DS} - Drain - Source Voltage (V)
Figure 7. Capacitance



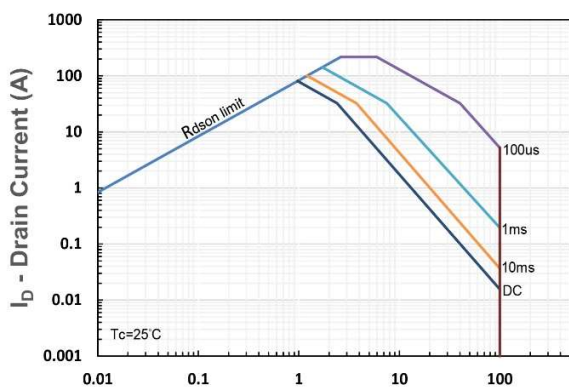
Qg , Total Gate Charge (nC)
Figure 8. Gate Charge Characteristics



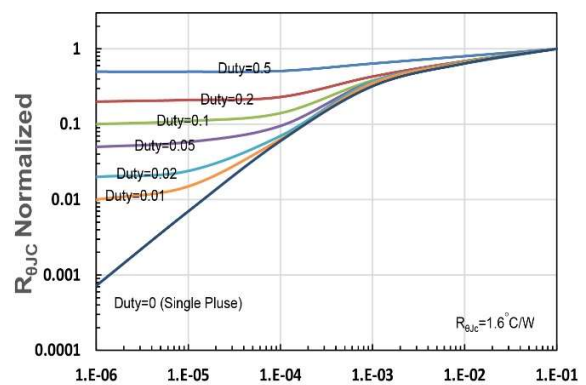
T_c - Case Temperature (°C)
Figure 9. Power Dissipation



T_c - Case Temperature (°C)
Figure 10. Drain Current



V_{DS} - Drain-Source Voltage (V)
Figure 11. Safe Operating Area



t_1 , Square Wave Pulse Duration(s)
Figure 12. $R_{\theta Jc}$ Transient Thermal Impedance