





Power MOSFETS

DATASHEET

LM1FB80NAG6A

N-Channel
Enhancement Mode MOSFET

 Leadpower-semiconductor Corp., Ltd

 sales@leadpower-semi.com

 (03) 6577339 FAX : (03) 6577229

 www.leadpower-semi.com



Quality Management Systems

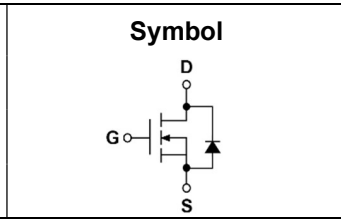
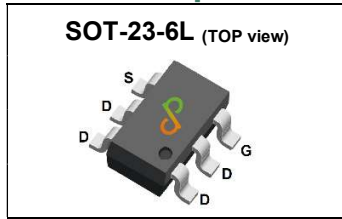
ISO 9001:2015 Certificate

LM1FB80NAG6A



N-Channel Enhancement Mode MOSFET

Pin Description



Ordering Information

Symbol	N-Channel	Unit
V_{DSS}	150	V
$R_{DS(ON)-Max}$	255	mΩ
I_D	1.9	A

Feature

- Fast switching speed
- Reliable and Rugged
- ROHS Compliant & Halogen-Free

Applications

- DC-DC converter switching for Networking
- General purpose switching

Ordering Information

Orderable Part Number	Package Type	Form	Shipping	Marking
LM1FB80NAG6A	SOT-23-6L	Tape & Reel	3000 / Tape & Reel	01□□□

Note : □□□ = Lot Code

Absolute Maximum Ratings (T_J=25°C Unless Otherwise Noted)

Symbol	Parameter	N-Channel	Unit	
V_{DSS}	Drain-Source Voltage	150	V	
V_{GSS}	Gate-Source Voltage	±20		
T_J	Maximum Junction Temperature	150	°C	
T_{STG}	Storage Temperature Range	-55 to 150	°C	
$I_{DM}^{①}$	Pulse Drain Current Tested	T _A =25°C	4.95	A
I_D	Continuous Drain Current	T _A =25°C	1.9	A
		T _A =70°C	1.5	
P_D	Maximum Power Dissipation	T _A =25°C	1.0	W
		T _A =70°C	0.6	
$I_{AS}^{②}$	Avalanche Current, Single pulse	L=0.1mH	7	A
		L=0.5mH	6	
$E_{AS}^{②}$	Avalanche Energy, Single pulse	L=0.1mH	2.5	mJ
		L=0.5mH	9	

Thermal Characteristics

Symbol	Parameter	Rating	Unit	
$R_{θJA}^{③}$	Thermal Resistance-Junction to Ambient	t ≤ 10s	90	°C/W
		Steady State	125	°C/W

Note ① : Max. current is limited by junction temperature

Note ② : UIS tested and pulse width are limited by maximum junction temperature 150°C

Note ③ : Surface Mounted on 1in² FR-4 board with 1oz.

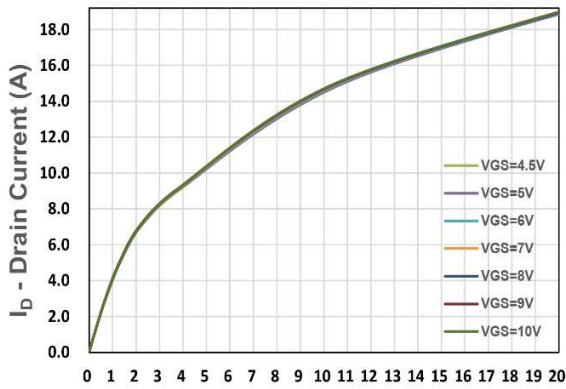
N-Channel Electrical Characteristics (T_J=25°C Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Static Electrical Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =250uA	150	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	V _{DS} =120V, V _{GS} =0V	-	-	1	uA
V_{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250uA	1	2	3	V
I_{GSS}	Gate Leakage Current	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
R_{DS(ON)} ^④	Drain-Source On-state Resistance	V _{GS} =10V, I _{DS} =1A	-	215	255	mΩ
		V _{GS} =4.5V, I _{DS} =0.5A	-	220	280	
gfs	Forward Transconductance	V _{DS} =5V, I _{DS} =0.5A	-	4.55	-	S
Dynamic Characteristics ^⑤						
R_G	Gate Resistance	V _{GS} =0V, V _{DS} =0V, Freq.=1MHz	-	9	-	Ω
C_{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =75V, Freq.=1MHz	-	712	-	pF
C_{oss}	Output Capacitance		-	19	-	
C_{rss}	Reverse Transfer Capacitance		-	7.9	-	
td(ON)	Turn-on Delay Time	V _{GS} =10V, V _{DS} =75V, I _D =1A, R _{GEN} =6Ω	-	11	-	nS
t_r	Turn-on Rise Time		-	6	-	
t_{d(OFF)}	Turn-off Delay Time		-	24	-	
t_f	Turn-off Fall Time		-	6	-	
Q_g	Total Gate Charge	V _{GS} =4.5V, V _{DS} =75V, I _D =1A	-	6.3	-	nC
Q_g	Total Gate Charge	V _{GS} =10V, V _{DS} =75V, I _D =1A	-	13	-	
Q_{gs}	Gate-Source Charge		-	1.86	-	
Q_{gd}	Gate-Drain Charge		-	2.5	-	
Source-Drain Characteristics						
V_{SD} ^④	Diode Forward Voltage	I _{SD} =1A, V _{GS} =0V	-	0.75	1.1	V
t_{rr}	Reverse Recovery Time	I _F =1A, V _R =75V	-	30	-	nS
Q_{rr}	Reverse Recovery Charge	dI _F /dt=100A/μs	-	15	-	nC

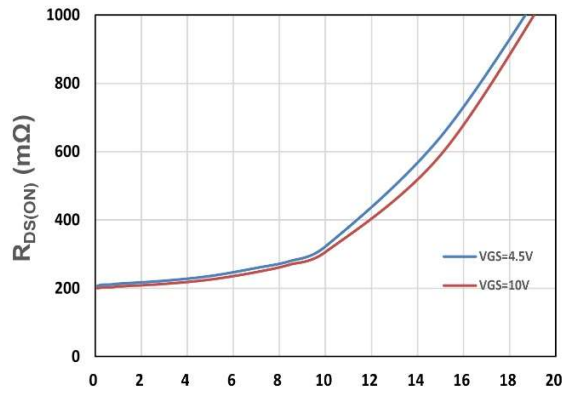
Note ④ : Pulse test (pulse width≤300us, duty cycle≤2%).

Note ⑤ : Guaranteed by design, not subject to production testing.

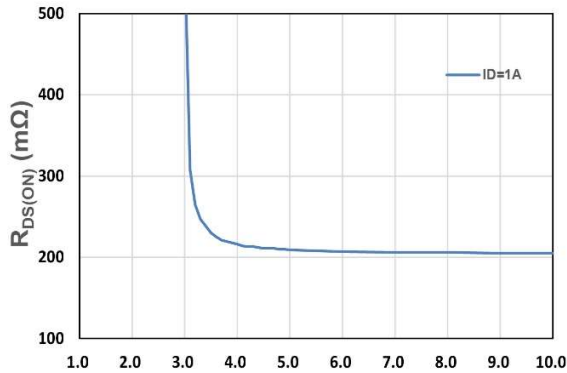
N-Channel Typical Characteristics



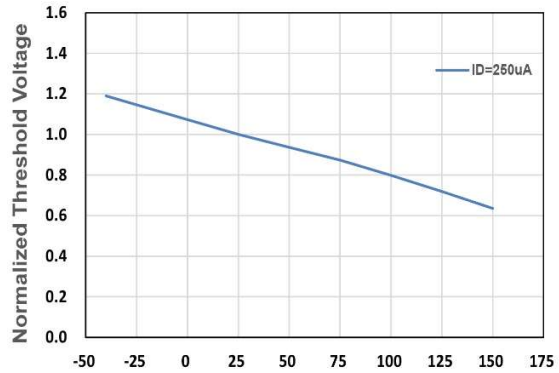
V_{DS} - Drain - Source Voltage (V)
Figure 1. Output Characteristics



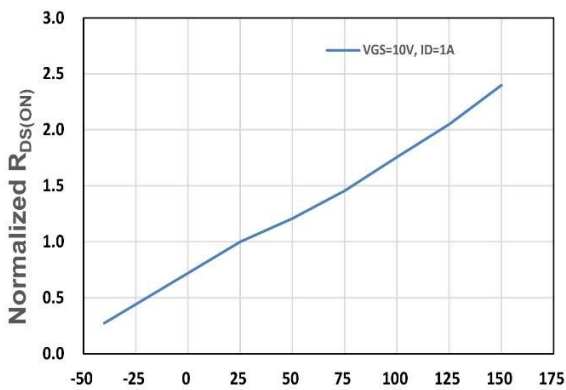
I_D - Drain Current (A)
Figure 2. On-Resistance vs. I_D



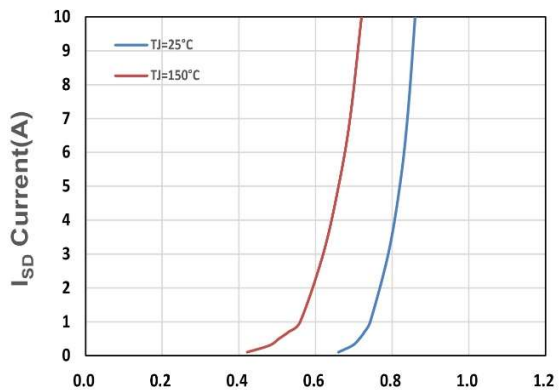
V_{GS} - Gate - Source Voltage (V)
Figure 3. On-Resistance vs. V_{GS}



T_j , Junction Temperature($^{\circ}C$)
Figure 4. Gate Threshold Voltage

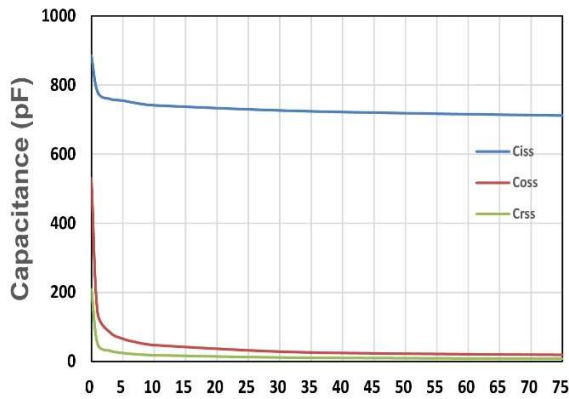


T_j , Junction Temperature($^{\circ}C$)
Figure 5. Drain-Source On Resistance

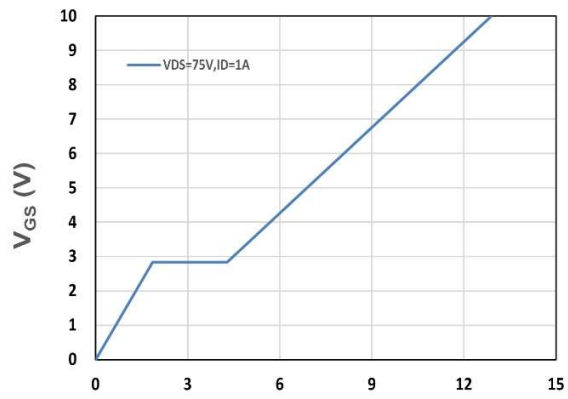


V_{SD} , Source-Drain Voltage(V)
Figure 6. Source-Drain Diode Forward

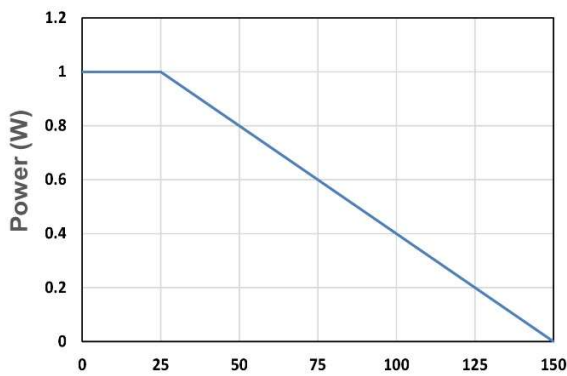
LM1FB80NAG6A



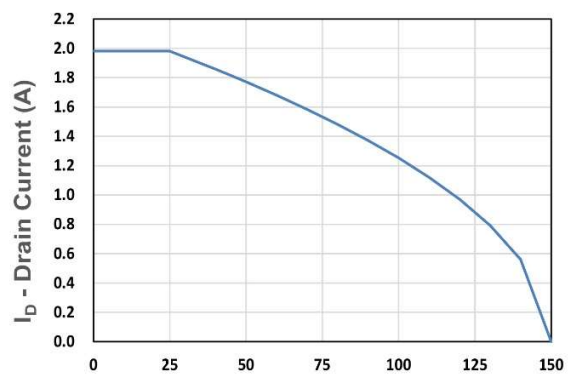
V_{DS} - Drain - Source Voltage (V)
Figure 7. Capacitance



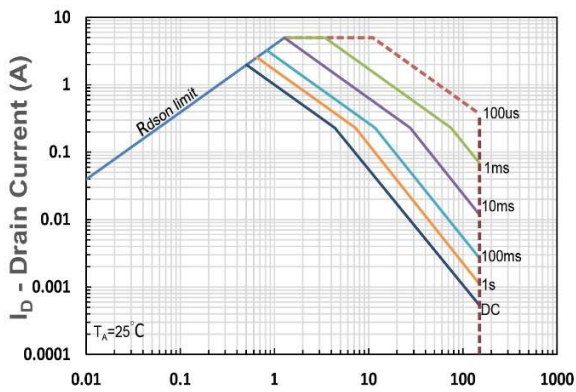
Q_g , Total Gate Charge (nC)
Figure 8. Gate Charge Characteristics



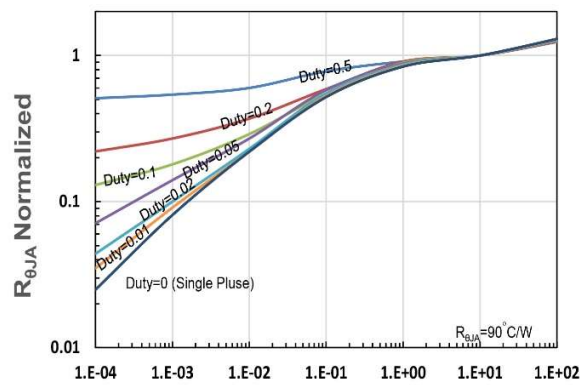
T_A - Ambient Temperature (°C)
Figure 9. Power Dissipation



T_A - Ambient Temperature (°C)
Figure 10. Drain Current



V_{DS} - Drain-Source Voltage (V)
Figure 11. Safe Operating Area



t_1 , Square Wave Pulse Duration(s)
Figure 12. $R_{\theta JA}$ Transient Thermal Impedance