



# Power MOSFETS

## DATASHEET

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**LM60105NAP3A**

N-Channel  
Enhancement Mode MOSFET

 Leadpower-semiconductor Corp., Ltd

 [sales@leadpower-semi.com](mailto:sales@leadpower-semi.com)

 (03) 6577339 FAX : (03) 6577229

 [www.leadpower-semi.com](http://www.leadpower-semi.com)



Quality Management Systems

ISO 9001:2015 Certificate


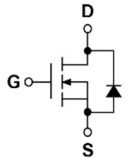
# LM60105NAP3A



## N-Channel Enhancement Mode MOSFET

### Pin Description

### Product Summary

<b>TO-220-3L (TOP view)</b> 	<b>Symbol</b> 	<b>Symbol</b>	<b>N-Channel</b>	<b>Unit</b>
		$V_{DSS}$	60	V
		$R_{DS(ON)-Max}$	11.5	m $\Omega$
		$I_D$	60	A

### Feature

- Fast switching speed
- Reliable and Rugged
- ROHS Compliant & Halogen-Free
- 100% UIS Tested

### Applications

- Portable Equipment
- Battery Powered System

### Ordering Information

Orderable Part Number	Package Type	Form	Shipping	Marking
LM60105NAP3A	TO-220-3L	Tube	50 / Tube	60105 □□□□□□

Note : □□□□□□ = Lot Code

### Absolute Maximum Ratings (T<sub>J</sub>=25°C Unless Otherwise Noted)

Symbol	Parameter	N-Channel	Unit
$V_{DSS}$	Drain-Source Voltage	60	V
$V_{GSS}$	Gate-Source Voltage	±20	
$T_J$	Maximum Junction Temperature	150	°C
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
$I_{DM}^{①}$	Pulse Drain Current Tested	$T_c=25^\circ C$ 150	A
$I_D$	Continuous Drain Current	$T_c=25^\circ C$ 60	A
		$T_c=100^\circ C$ 38	
$P_D$	Maximum Power Dissipation	$T_c=25^\circ C$ 83	W
		$T_c=100^\circ C$ 33	
$I_{AS}^{②}$	Avalanche Current, Single pulse	L=0.1mH 30	A
$E_{AS}^{③}$	Avalanche Energy, Single pulse	L=0.1mH 45	mJ

### Thermal Characteristics

Symbol	Parameter	Rating	Unit
$R_{\theta JC}$	Thermal Resistance-Junction to Case	Steady State 1.5	°C/W
$R_{\theta JA}^{③}$	Thermal Resistance-Junction to Ambient	Steady State 62.5	°C/W

Note ① : Max. current is limited by junction temperature.

Note ② : UIS tested and pulse width are limited by maximum junction temperature 150°C

Note ③ : Surface Mounted on 1in<sup>2</sup> FR-4 board with 1oz.

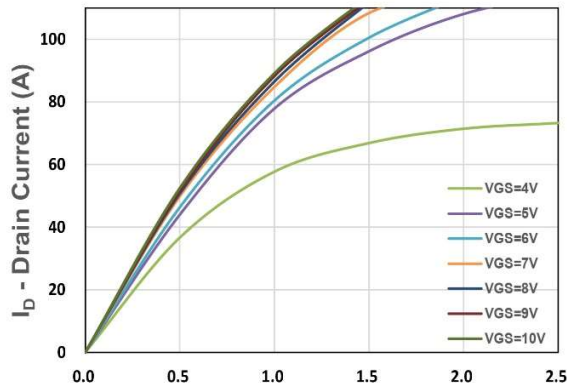
## N-Channel Electrical Characteristics (T<sub>J</sub>=25°C Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
<b>Static Electrical Characteristics</b>						
<b>BV<sub>DSS</sub></b>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>DS</sub> =250uA	60	-	-	V
<b>I<sub>DSS</sub></b>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =48V, V <sub>GS</sub> =0V	-	-	1	uA
<b>V<sub>GS(th)</sub></b>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>DS</sub> =250uA	1.4	1.7	2	V
<b>I<sub>GSS</sub></b>	Gate Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	±100	nA
<b>R<sub>DS(ON)</sub></b> <sup>④</sup>	Drain-Source On-state Resistance	V <sub>GS</sub> =10V, I <sub>DS</sub> =20A	-	9.5	11.5	mΩ
		V <sub>GS</sub> =4.5V, I <sub>DS</sub> =15A	-	10.5	13.5	
<b>gfs</b>	Forward Transconductance	V <sub>DS</sub> =5V, I <sub>DS</sub> =20A	-	17.8	-	S
<b>Dynamic Characteristics</b> <sup>⑤</sup>						
<b>R<sub>G</sub></b>	Gate Resistance	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, Freq.=1MHz	-	2.8	-	Ω
<b>C<sub>iss</sub></b>	Input Capacitance	V <sub>GS</sub> =0V, V <sub>DS</sub> =30V, Freq.=1MHz	-	3200	-	pF
<b>C<sub>oss</sub></b>	Output Capacitance		-	173	-	
<b>C<sub>rss</sub></b>	Reverse Transfer Capacitance		-	131	-	
<b>td(ON)</b>	Turn-on Delay Time	V <sub>GS</sub> =10V, V <sub>DS</sub> =30V, I <sub>D</sub> =1A, R <sub>GEN</sub> =6Ω	-	16	-	nS
<b>t<sub>r</sub></b>	Turn-on Rise Time		-	65	-	
<b>t<sub>d(OFF)</sub></b>	Turn-off Delay Time		-	135	-	
<b>t<sub>f</sub></b>	Turn-off Fall Time		-	60	-	
<b>Q<sub>g</sub></b>	Total Gate Charge	V <sub>GS</sub> =4.5V, V <sub>DS</sub> =30V I <sub>D</sub> =20A	-	36.7	-	nC
<b>Q<sub>g</sub></b>	Total Gate Charge	V <sub>GS</sub> =10V, V <sub>DS</sub> =30V, I <sub>D</sub> =20A	-	79	-	
<b>Q<sub>gs</sub></b>	Gate-Source Charge		-	11.5	-	
<b>Q<sub>gd</sub></b>	Gate-Drain Charge		-	13.7	-	
<b>Source-Drain Characteristics</b>						
<b>V<sub>SD</sub></b> <sup>④</sup>	Diode Forward Voltage	I <sub>SD</sub> =15A, V <sub>GS</sub> =0V	-	0.8	1.1	V
<b>t<sub>rr</sub></b>	Reverse Recovery Time	I <sub>F</sub> =15A, V <sub>R</sub> =30V	-	31.8	-	nS
<b>Q<sub>rr</sub></b>	Reverse Recovery Charge	dI <sub>F</sub> /dt=100A/μs	-	33.7	-	nC

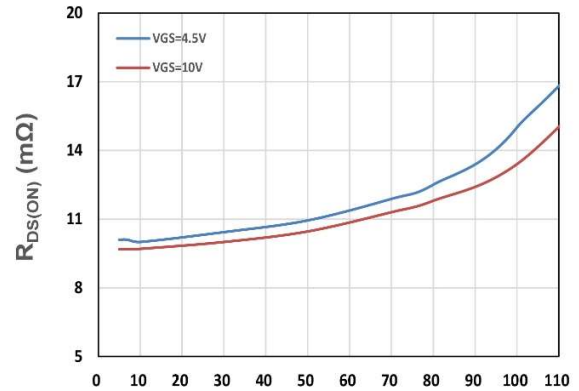
Note ④ : Pulse test (pulse width≤300us, duty cycle≤2%).

Note ⑤ : Guaranteed by design, not subject to production testing.

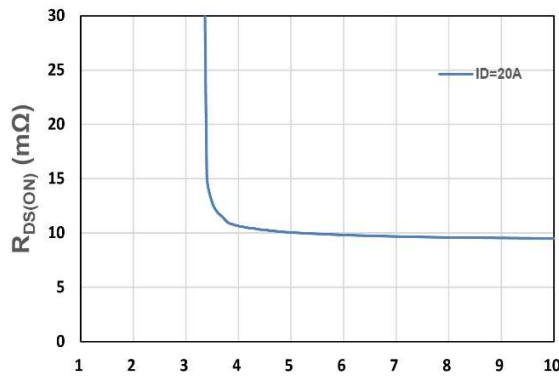
## N-Channel Typical Characteristics



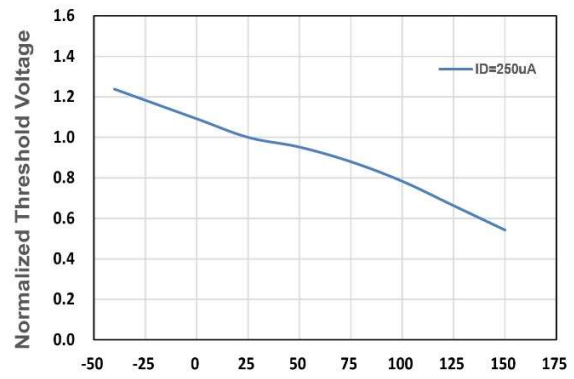
$V_{DS}$  - Drain - Source Voltage (V)  
Figure 1. Output Characteristics



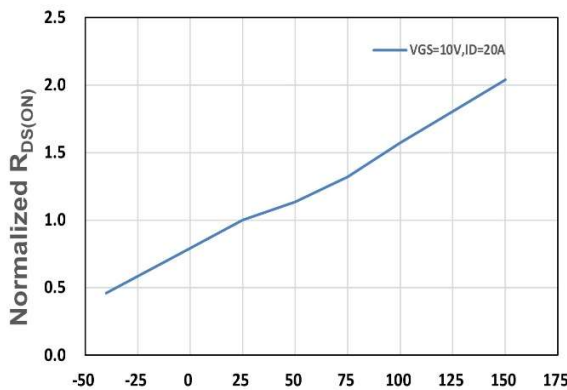
$I_D$ - Drain Current (A)  
Figure 2. On-Resistance vs. ID



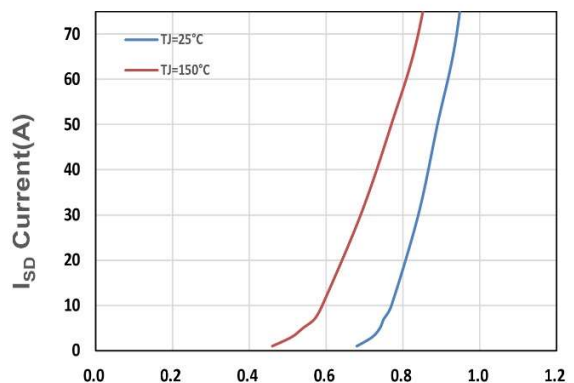
$V_{GS}$  - Gate - Source Voltage (V)  
Figure 3. On-Resistance vs. VGS



$T_j$ , Junction Temperature( $^{\circ}C$ )  
Figure 4. Gate Threshold Voltage

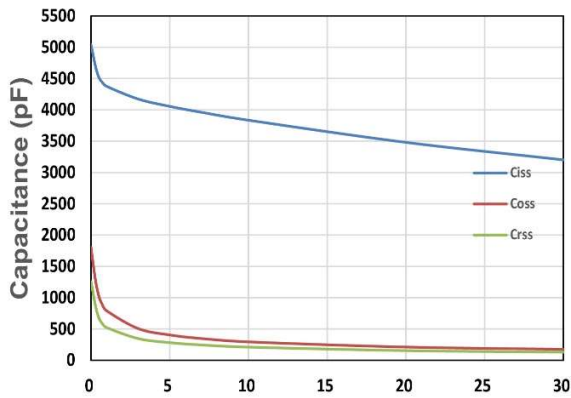


$T_j$ , Junction Temperature( $^{\circ}C$ )  
Figure 5. Drain-Source On Resistance

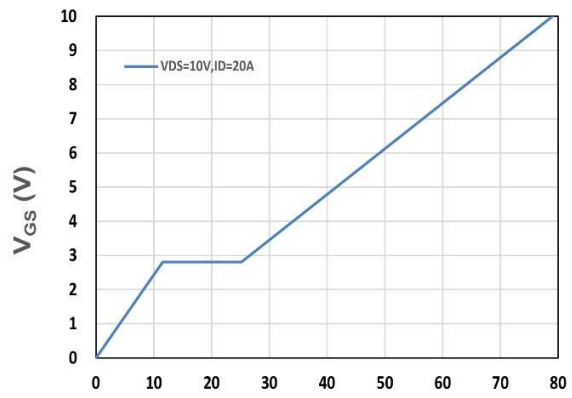


$V_{SD}$ , Source-Drain Voltage(V)  
Figure 6. Source-Drain Diode Forward

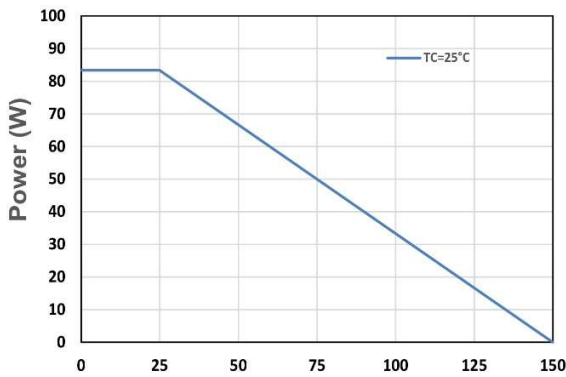
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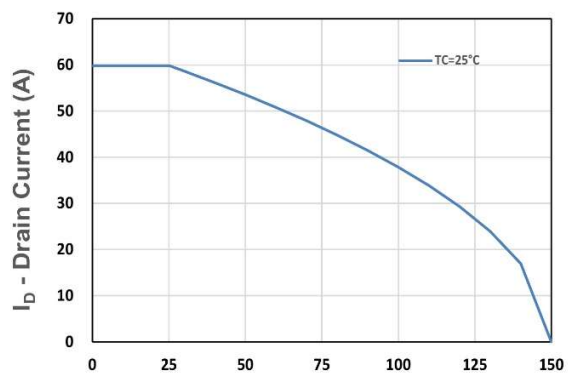
$V_{DS}$  - Drain - Source Voltage (V)  
Figure 7. Capacitance



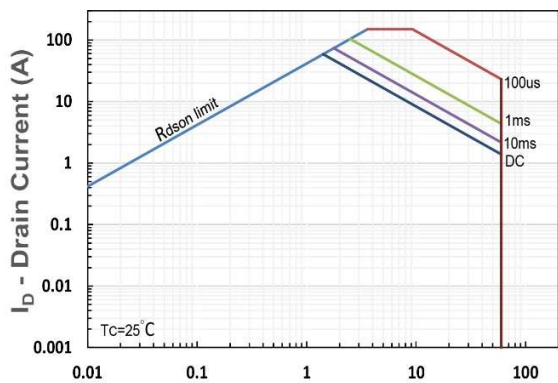
$Q_g$ , Total Gate Charge (nC)  
Figure 8. Gate Charge Characteristics



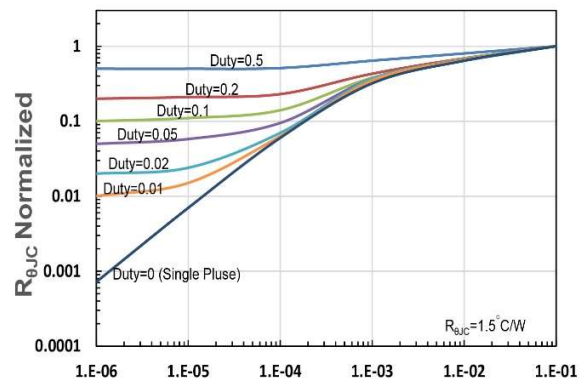
$T_j$  - Junction Temperature ( $^{\circ}C$ )  
Figure 9. Power Dissipation



$T_j$  - Junction Temperature ( $^{\circ}C$ )  
Figure 10. Drain Current



$V_{DS}$  - Drain-Source Voltage (V)  
Figure 11. Safe Operating Area



$t_1$ , Square Wave Pulse Duration(s)  
Figure 12.  $R_{\theta JC}$  Transient Thermal Impedance