





Power MOSFETS

DATASHEET

LM1A014NHT3A

N-Channel
Enhancement Mode MOSFET

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Quality Management Systems

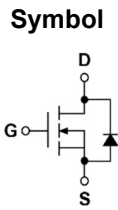
ISO 9001:2015 Certificate

LM1A014NHT3A



N-Channel Enhancement Mode MOSFET

Pin Description



Product Summary

TO-247-3L	Symbol	Symbol	N-Channel	Unit
		V_{DSS}	100	V
		$R_{DS(ON)-Max}$	1.68	mΩ
		I_D	411	A

Feature

- Fast switching speed
- Reliable and Rugged
- ROHS Compliant & Halogen-Free
- 100% UIS and Rg Tested

Applications

- Battery Management System

Ordering Information

Orderable Part Number	Package Type	Form	Shipping	Marking
LM1A014NHT3A	TO-247-3L	Tube	30 / Tube	1A014 □□□□□□

Note : □□□□□□ = Lot Code

Absolute Maximum Ratings (T_J=25°C Unless Otherwise Noted)

Symbol	Parameter	N-Channel	Unit
V_{DSS}	Drain-Source Voltage	100	V
V_{GSS}	Gate-Source Voltage	±20	V
T_J	Maximum Junction Temperature	150	°C
T_{STG}	Storage Temperature Range	-55 to 150	°C
I_S	Diode Continuous Forward Current	$T_C=25^\circ C$ 258	A
$I_{DM}^{(1)}$	Pulse Drain Current Tested	$T_C=25^\circ C$ 1028	A
I_D	Continuous Drain Current	$T_C=25^\circ C$ 411 $T_C=100^\circ C$ 260	A
P_D	Maximum Power Dissipation	$T_C=25^\circ C$ 284 $T_C=100^\circ C$ 114	W
I_D	Continuous Drain Current	$T_A=25^\circ C$ 43 $T_A=70^\circ C$ 35	A
P_D	Maximum Power Dissipation	$T_A=25^\circ C$ 3.1 $T_A=70^\circ C$ 2.0	W
$I_{AS}^{(2)}$	Avalanche Current, Single pulse	L=0.1mH 105 L=0.5mH 72	A
$E_{AS}^{(2)}$	Avalanche Energy, Single pulse	L=0.1mH 550 L=0.5mH 1300	mJ

Thermal Characteristics

Symbol	Parameter	Rating	Unit
$R_{\theta JC}$	Thermal Resistance-Junction to Case	Steady State	0.44 °C/W
$R_{\theta JA}^{(3)}$	Thermal Resistance-Junction to Ambient	Steady State	40 °C/W

Note ① : Max. current is limited by junction limit

Note ② : UIS tested and pulse width are limited by maximum junction temperature 150°C

Note ③ : Surface Mounted on 1in² FR-4 board with 1oz

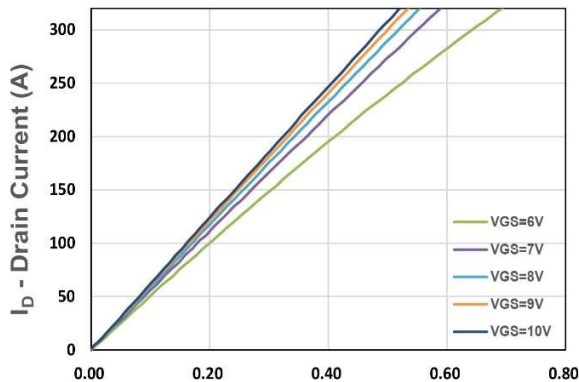
N-Channel Electrical Characteristics (T_J=25°C Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Static Electrical Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =250uA	100	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	V _{DS} =80V, V _{GS} =0V	-	-	1	uA
V_{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250uA	2	3	4	V
I_{GSS}	Gate Leakage Current	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
R_{DS(ON)}^④	Drain-Source On-state Resistance	V _{GS} =10V, I _{DS} =100A	-	1.4	1.68	mΩ
gfs	Forward Transconductance	V _{DS} =5V, I _{DS} =10A	-	4	-	S
Dynamic Characteristics^⑥						
R_G	Gate Resistance	V _{GS} =0V, V _{DS} =0V, Freq.=1MHz	-	1	-	Ω
C_{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =50V, Freq.=1MHz	-	18320	-	pF
C_{oss}	Output Capacitance		-	6095	-	
C_{rss}	Reverse Transfer Capacitance		-	556	-	
t_{d(ON)}	Turn-on Delay Time	V _{GS} =10V, V _{DS} =50V, I _D =1A, R _{GEN} =1Ω	-	70.8	-	nS
t_r	Turn-on Rise Time		-	37.9	-	
t_{d(OFF)}	Turn-off Delay Time		-	162.1	-	
t_f	Turn-off Fall Time		-	358.3	-	
Q_g	Total Gate Charge	V _{GS} =6V, V _{DS} =50V I _D =100A	-	205	-	nC
Q_g	Total Gate Charge	V _{GS} =10V, V _{DS} =50V, I _D =100A	-	313	-	
Q_{gs}	Gate-Source Charge		-	97.8	-	
Q_{gd}	Gate-Drain Charge		-	83.7	-	
Source-Drain Characteristics						
V_{SD}^④	Diode Forward Voltage	I _{SD} =10A, V _{GS} =0V	-	0.75	1.1	V
t_{rr}	Reverse Recovery Time	I _F =50A, V _R =50V	-	147	-	nS
Q_{rr}	Reverse Recovery Charge	dI _F /dt=100A/μs	-	419	-	nC

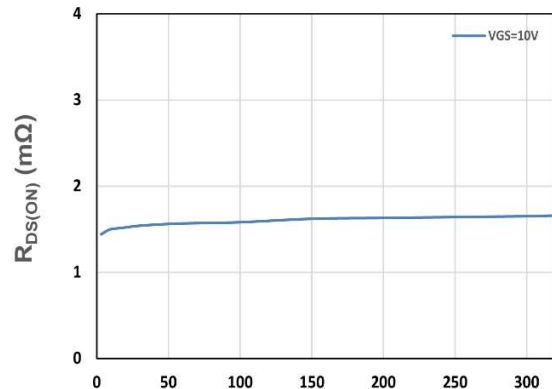
Note ④ : Pulse test (pulse width≤300us, duty cycle≤2%).

Note ⑤ : Guaranteed by design, not subject to production testing.

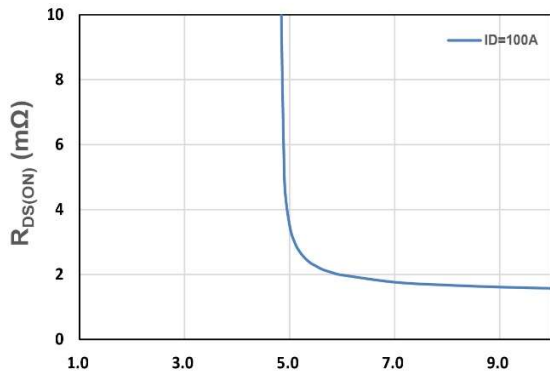
N-Channel Typical Characteristics



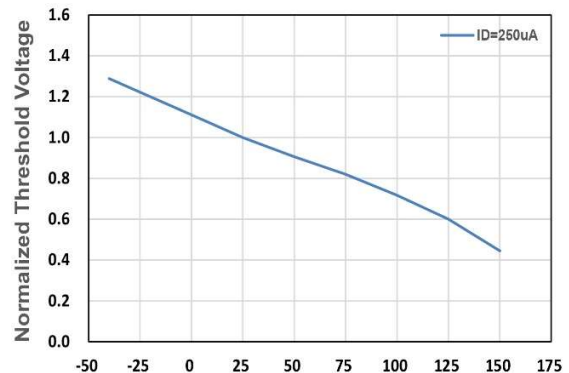
V_{DS} - Drain - Source Voltage (V)
Figure 1. Output Characteristics



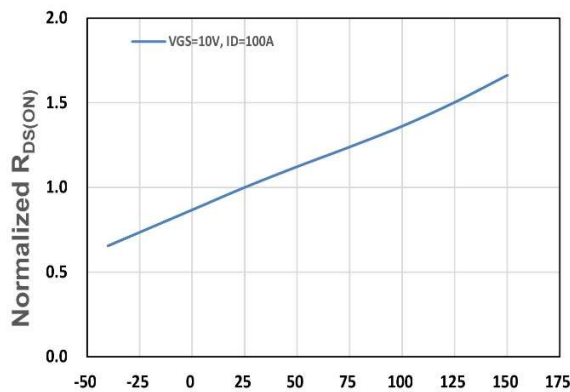
I_D - Drain Current (A)
Figure 2. On-Resistance vs. ID



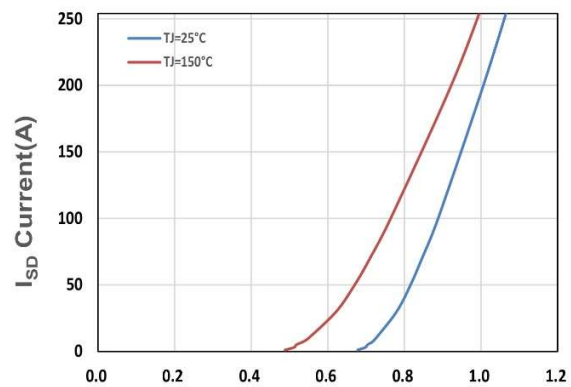
V_{GS} - Gate - Source Voltage (V)
Figure 3. On-Resistance vs. VGS



T_j , Junction Temperature($^{\circ}C$)
Figure 4. Gate Threshold Voltage

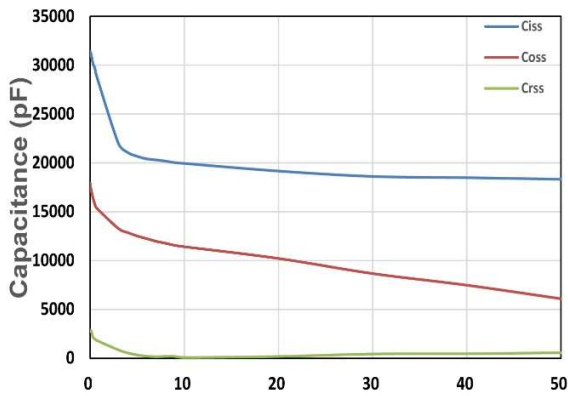


T_j , Junction Temperature($^{\circ}C$)
Figure 5. Drain-Source On Resistance

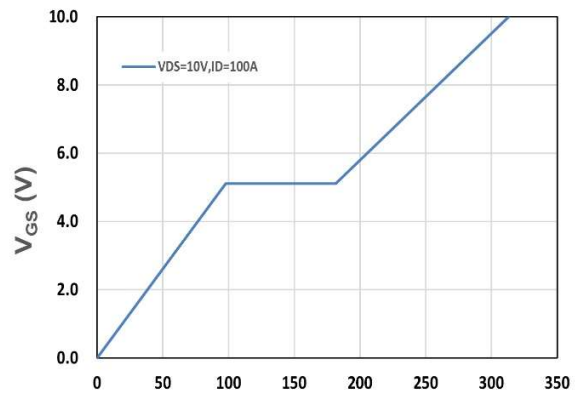


V_{SD} , Source-Drain Voltage(V)
Figure 6. Source-Drain Diode Forward

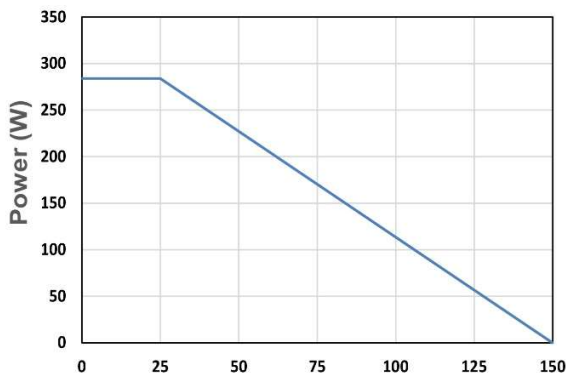
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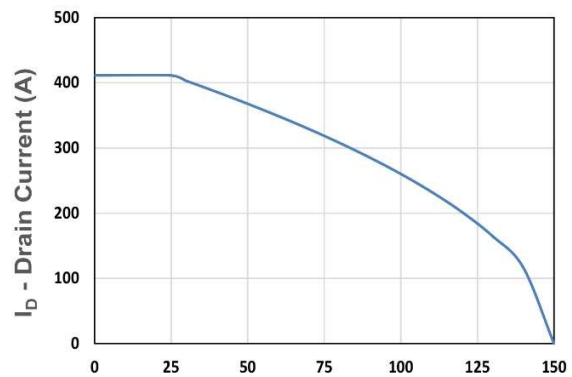
V_{DS} - Drain - Source Voltage (V)
Figure 7. Capacitance



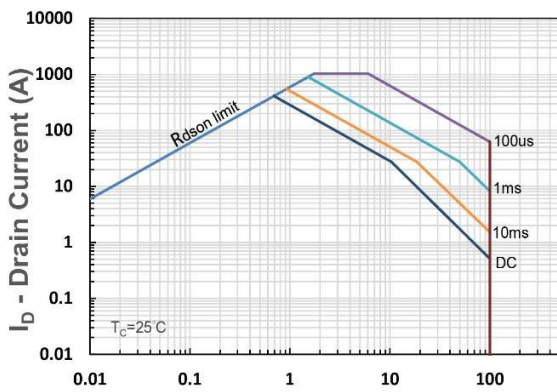
Qg , Total Gate Charge (nC)
Figure 8. Gate Charge Characteristics



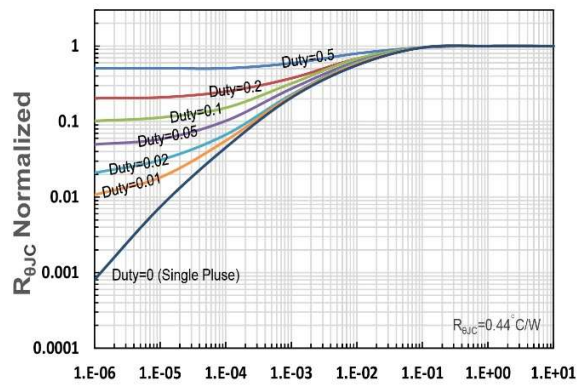
T_c - Case Temperature ($^{\circ}C$)
Figure 9. Power Dissipation



T_c - Case Temperature ($^{\circ}C$)
Figure 10. Drain Current



V_{DS} - Drain-Source Voltage (V)
Figure 11. Safe Operating Area



t_1 , Square Wave Pulse Duration(s)
Figure 12. $R_{\theta Jc}$ Transient Thermal Impedance