Power MOSFETS

PRELIMINARY DATASHEET

LM1F098NHP3A

N-Channel

Enhancement Mode MOSFET

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Quality Management Systems

ISO 9001:2015 Certificate

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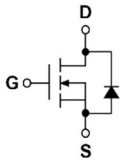
N-Channel Enhancement Mode MOSFET

Pin Description

TO-220-3L (TOP view)	Symbol
	

Product Summary

Symbol	N-Channel	Unit
V_{DSS}	150	V
$R_{DS(ON)-Max}$	10	mΩ
ID	111	A

Feature

- Fast switching speed
- Reliable and Rugged
- ROHS Compliant & Halogen-Free
- 100% UIS and Rg Tested

Applications

- Synchronous Rectification in SMPS
- Hard Switching and High Speed Circuit
- DC/DC in Telecoms and Inductrial

Ordering Information

Orderable Part Number	Package Type	Form	Shipping	Marking
LM1F098NHP3A	TO-220-3L	Tube	50 / Tape & Reel	1F098 □□□□□□

Note：□□□□□□ = Lot Code

Absolute Maximum Ratings (T_J=25°C Unless Otherwise Noted)

Symbol	Parameter		N-Channel	Unit
V_{DSS}	Drain-Source Voltage		150	V
V_{GSS}	Gate-Source Voltage		±20	
T_J	Maximum Junction Temperature		150	°C
T_{STG}	Storage Temperature Range		-55 to 150	°C
I_S	Diode Continuous Forward Current	T_C=25°C	114	A
I_{DM}①	Pulse Drain Current Tested	T_C=25°C	277	A
I_D	Continuous Drain Current	T_C=25°C	111	A
		T_C=100°C	70	
P_D	Maximum Power Dissipation	T_C=25°C	125	W
		T_C=100°C	50	
I_D	Continuous Drain Current	T_A=25°C	14	A
		T_A=70°C	11	
P_D	Maximum Power Dissipation	T_A=25°C	2.0	W
		T_A=70°C	1.3	
I_{AS}②	Avalanche Current, Single pulse	L=0.4mH	40	A
E_{AS}②	Avalanche Energy, Single pulse	L=0.4mH	320	mJ

Thermal Characteristics

Symbol	Parameter		Rating	Unit
$R_{\theta JC}$	Thermal Resistance-Junction to Case	Steady State	1	°C/W
$R_{\theta JA}$③	Thermal Resistance-Junction to Ambient	Steady State	62.5	°C/W

Note ①：Max. current is limited by junction temperature

Note ②：UIS tested and pulse width are limited by maximum junction temperature 150°C

Note ③：Surface Mounted on 1in^2 FR-4 board with 1oz

N-Channel Electrical Characteristics (T_J=25°C Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Static Electrical Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V, I_{DS}=250uA	150	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS}=120V, V_{GS}=0V	-	-	1	uA
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS}, I_{DS}=250uA	2	3	4	V
I_{GSS}	Gate Leakage Current	V_{GS}=±20V, V_{DS}=0V	-	-	±100	nA
$R_{DS(ON)}$④	Drain-Source On-state Resistance	V_{GS}=10V, I_{DS}=20A	-	8.5	10	mΩ
gfs	Forward Transconductance	V_{DS}=5V, I_{DS}=20A	-	76	-	S
Dynamic Characteristics ⑤						
R_G	Gate Resistance	V_{GS}=0V,V_{DS}=0V, Freq.=1MHz	-	1.1	-	Ω
C_{iss}	Input Capacitance	V_{GS}=0V, V_{DS}=75V, Freq.=1MHz	-	4805	-	pF
C_{oss}	Output Capacitance		-	366	-	
C_{rss}	Reverse Transfer Capacitance		-	12.7	-	
$td_{(ON)}$	Turn-on Delay Time	V_{GS}=10V,V_{DS}=75V, I_D=20A,R_{GEN}=10Ω	-	21	-	nS
t_r	Turn-on Rise Time		-	11	-	
$t_{d(OFF)}$	Turn-off Delay Time		-	32	-	
t_f	Turn-off Fall Time		-	13.2	-	
Q_g	Total Gate Charge	V_{GS}=10V,V_{DS}=75V, I_D=20A	-	57	-	nC
Q_{gs}	Gate-Source Charge		-	21	-	
Q_{gd}	Gate-Drain Charge		-	5.6	-	
Source-Drain Characteristics						
V_{SD}④	Diode Forward Voltage	I_{SD}=20A, V_{GS}=0V	-	0.8	1.2	V
t_{rr}	Reverse Recovery Time	I_F=20A, V_R=75V dI$_F$/dt=100A/μs	-	88	-	nS
Q_{rr}	Reverse Recovery Charge		-	176	-	nC

Note ④ : Pulse test (pulse width≤300us, duty cycle≤2%).
Note ⑤ : Guaranteed by design, not subject to production testing.

N-Channel Typical Characteristics

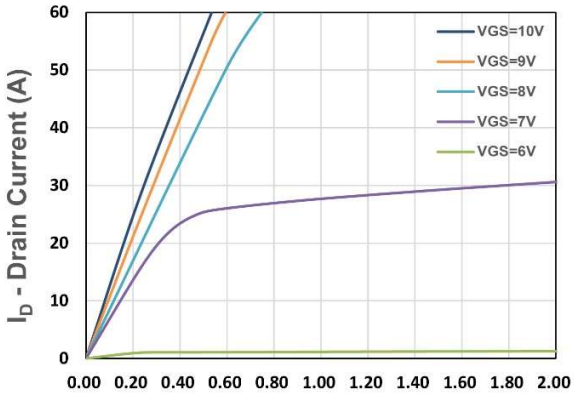


Figure 1. Output Characteristics

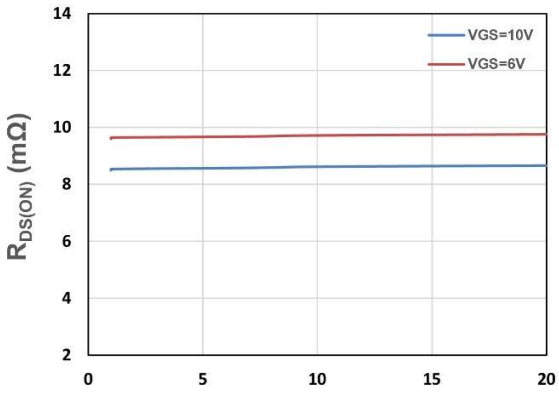


Figure 2. On-Resistance vs. ID

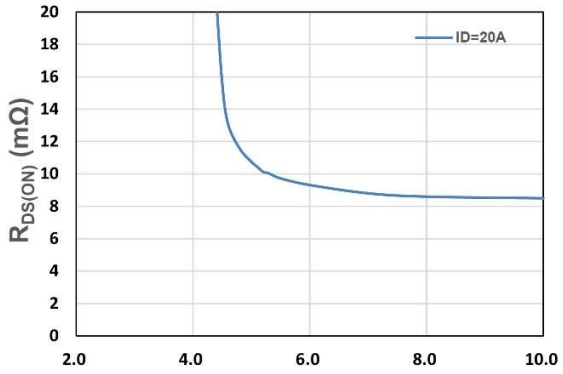


Figure 3. On-Resistance vs. VGS

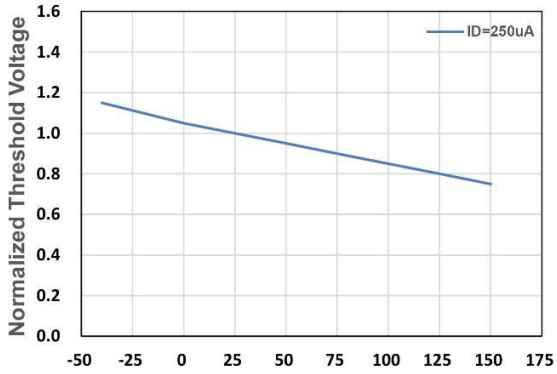


Figure 4. Gate Threshold Voltage

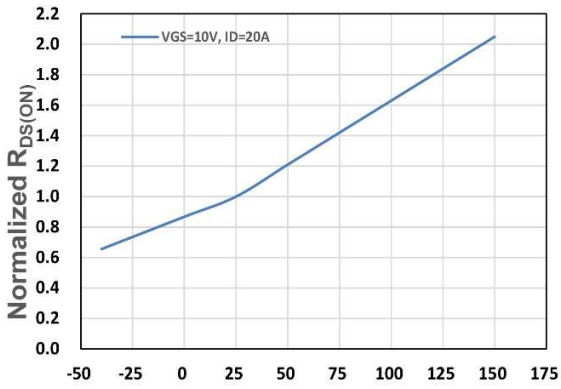


Figure 5. Drain-Source On Resistance

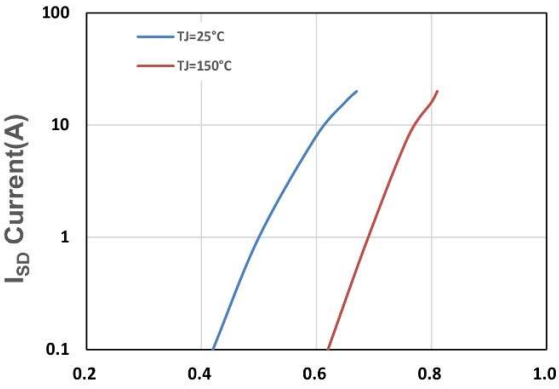


Figure 6. Source-Drain Diode Forward

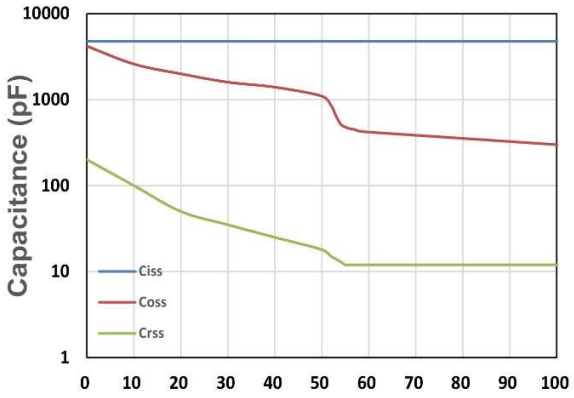


Figure 7. Capacitance

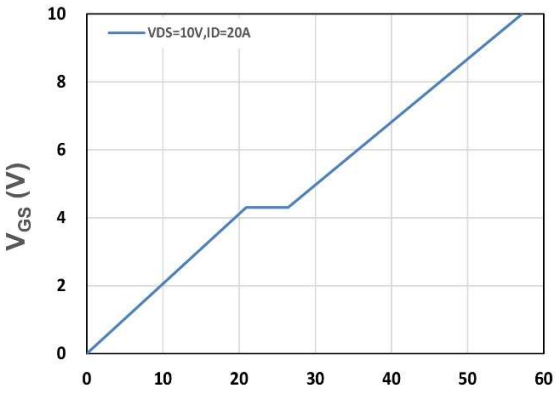


Figure 8. Gate Charge Characteristics

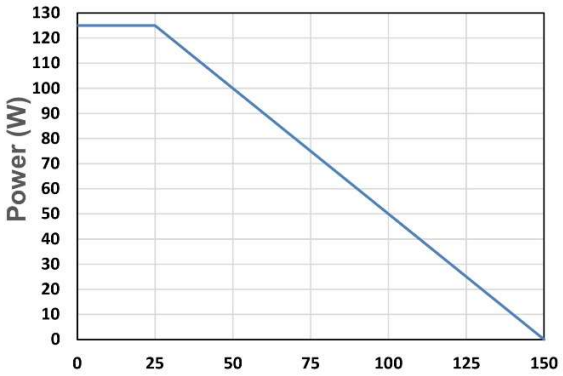


Figure 9. Power Dissipation

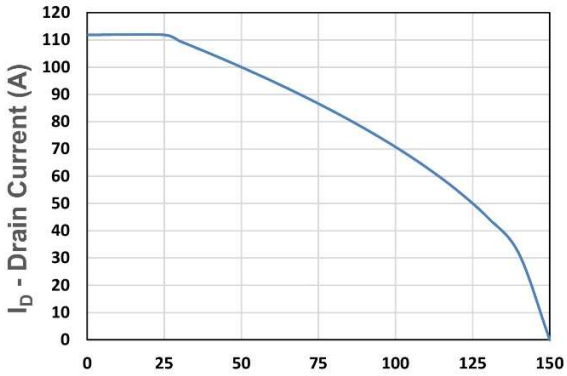


Figure 10. Drain Current

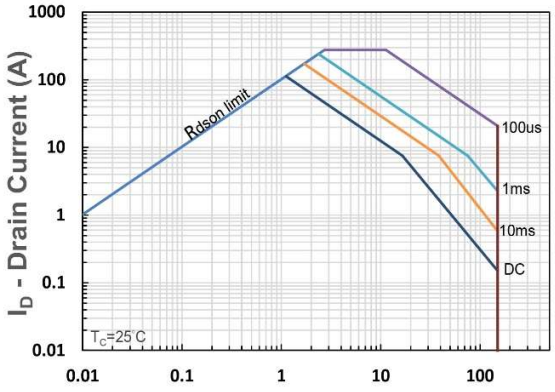


Figure 11. Safe Operating Area

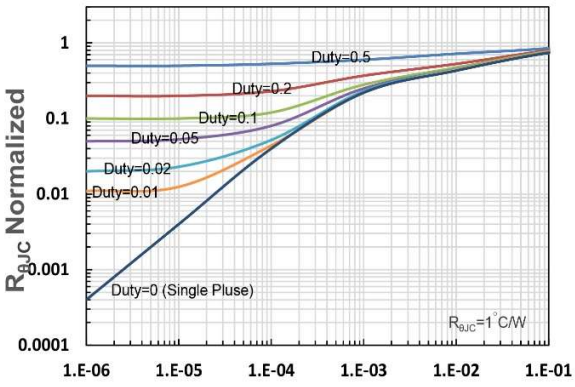


Figure 12. RθJC Transient Thermal Impedance