



Power MOSFETS

DATASHEET

LM1F195NHV2A

N-Channel
Enhancement Mode MOSFET

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Quality Management Systems

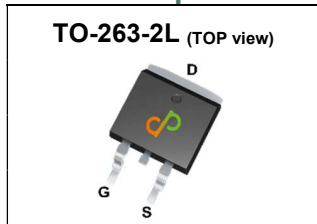
ISO 9001:2015 Certificate

LM1F195NHV2A

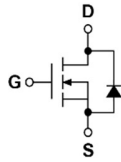


N-Channel Enhancement Mode MOSFET

Pin Description



Symbol



Product Summary

Symbol	N-Channel	Unit
V_{DSS}	150	V
$R_{DS(ON)-Max}$	19.5	mΩ
I_D	200	A

Feature

- Fast switching speed
- Reliable and Rugged
- ROHS Compliant & Halogen-Free
- 100% UIS and Rg Tested

Applications

- Synchronous Rectification in SMPS
- Hard Switching and High Speed Circuit
- DC/DC in Telecoms and Industrial

Ordering Information

Orderable Part Number	Package Type	Form	Shipping	Marking
LM1F195NHV2A	TO-263-2L	Tape & Reel	800 / Tape & Reel	1F195 □□□□□□

Note : □□□□□□ = Lot Code

Absolute Maximum Ratings (T_J=25°C Unless Otherwise Noted)

Symbol	Parameter	N-Channel	Unit	
V_{DSS}	Drain-Source Voltage	150	V	
V_{GSS}	Gate-Source Voltage	±20		
T_J	Maximum Junction Temperature	150	°C	
T_{STG}	Storage Temperature Range	-55 to 150	°C	
I_S	Diode Continuous Forward Current	T _C =25°C	114	A
$I_{DM}^{①}$	Pulse Drain Current Tested	T _C =25°C	200	A
I_D	Continuous Drain Current	T _C =25°C	80	A
		T _C =100°C	51	
P_D	Maximum Power Dissipation	T _C =25°C	125	W
		T _C =100°C	50	
I_D	Continuous Drain Current	T _A =25°C	9.7	A
		T _A =70°C	7.8	
P_D	Maximum Power Dissipation	T _A =25°C	1.8	W
		T _A =70°C	1.2	
$I_{AS}^{②}$	Avalanche Current, Single pulse	L=0.1mH	21	A
		L=0.5mH	18	
$E_{AS}^{②}$	Avalanche Energy, Single pulse	L=0.1mH	22	mJ
		L=0.5mH	81	

Thermal Characteristics

Symbol	Parameter	Rating	Unit	
$R_{θJC}$	Thermal Resistance-Junction to Case	Steady State	1	°C/W
$R_{θJA}^{③}$	Thermal Resistance-Junction to Ambient	Steady State	68	°C/W

Note ① : Max. current is limited by junction temperature

Note ② : UIS tested and pulse width are limited by maximum junction temperature 150°C

Note ③ : Surface Mounted on 1in² FR-4 board with 1oz

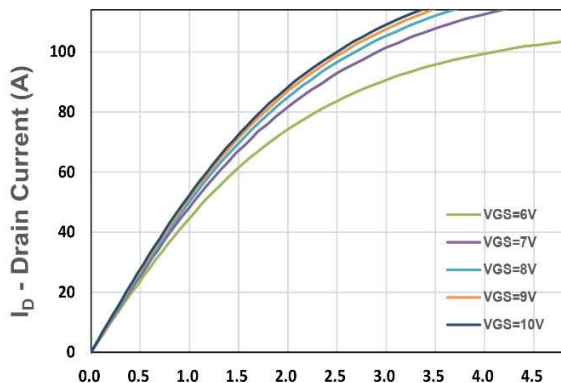
N-Channel Electrical Characteristics (T_J=25°C Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Static Electrical Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =250uA	150	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	V _{DS} =120V, V _{GS} =0V	-	-	1	uA
V_{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250uA	2	3	4	V
I_{GSS}	Gate Leakage Current	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
R_{DS(ON)} ^④	Drain-Source On-state Resistance	V _{GS} =10V, I _{DS} =20A	-	17.5	19.5	mΩ
gfs	Forward Transconductance	V _{DS} =5V, I _{DS} =10A	-	32.9	-	S
Dynamic Characteristics ^⑤						
R_G	Gate Resistance	V _{GS} =0V, V _{DS} =0V, Freq.=1MHz	-	2	-	Ω
C_{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =75V, Freq.=1MHz	-	1895	-	pF
C_{oss}	Output Capacitance		-	145	-	
C_{rss}	Reverse Transfer Capacitance		-	34	-	
t_{d(ON)}	Turn-on Delay Time	V _{GS} =10V, V _{DS} =25V, I _D =1A, R _{GEN} =1Ω	-	10	-	nS
t_r	Turn-on Rise Time		-	3.2	-	
t_{d(OFF)}	Turn-off Delay Time		-	21.5	-	
t_f	Turn-off Fall Time		-	66.6	-	
Q_g	Total Gate Charge	V _{GS} =6V, V _{DS} =75V I _D =50A	-	15.5	-	nC
Q_g	Total Gate Charge	V _{GS} =10V, V _{DS} =75V, I _D =50A	-	24.8	-	
Q_{gs}	Gate-Source Charge		-	11.1	-	
Q_{gd}	Gate-Drain Charge		-	2.87	-	
Source-Drain Characteristics						
V_{SD} ^④	Diode Forward Voltage	I _{SD} =10A, V _{GS} =0V	-	0.75	1.1	V
t_{rr}	Reverse Recovery Time	I _F =25A, V _R =75V	-	65.9	-	nS
Q_{rr}	Reverse Recovery Charge	dI _F /dt=100A/μs	-	130.5	-	nC

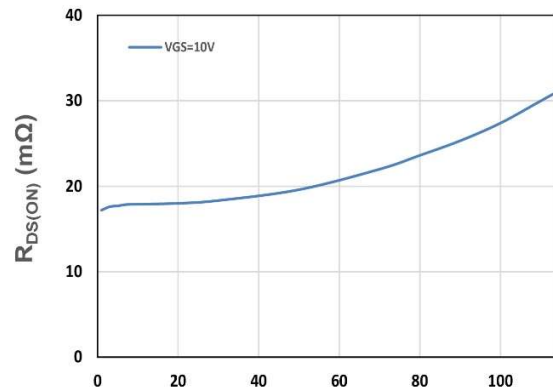
Note ④ : Pulse test (pulse width≤300us, duty cycle≤2%).

Note ⑤ : Guaranteed by design, not subject to production testing.

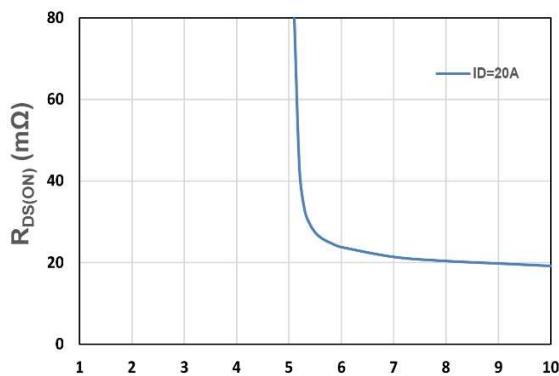
N-Channel Typical Characteristics



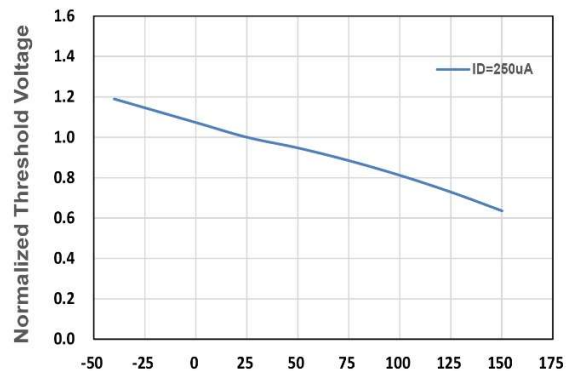
V_{DS} - Drain - Source Voltage (V)
Figure 1. Output Characteristics



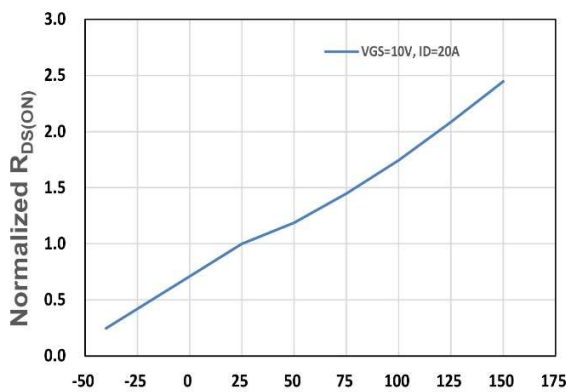
I_D - Drain Current (A)
Figure 2. On-Resistance vs. ID



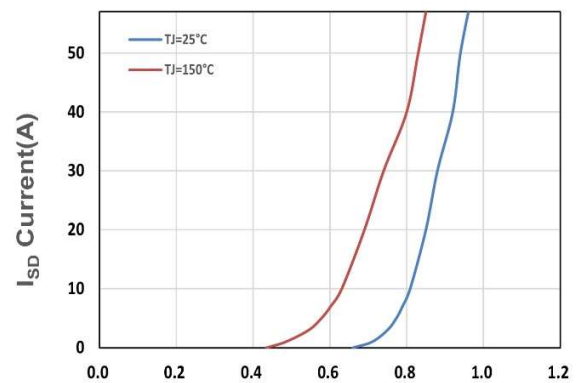
V_{GS} - Gate - Source Voltage (V)
Figure 3. On-Resistance vs. VGS



T_j , Junction Temperature($^{\circ}C$)
Figure 4. Gate Threshold Voltage

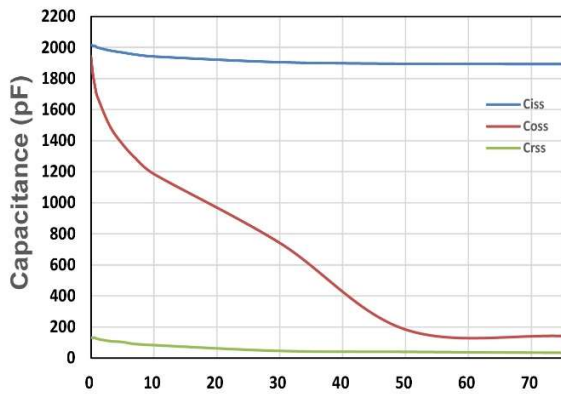


T_j , Junction Temperature($^{\circ}C$)
Figure 5. Drain-Source On Resistance

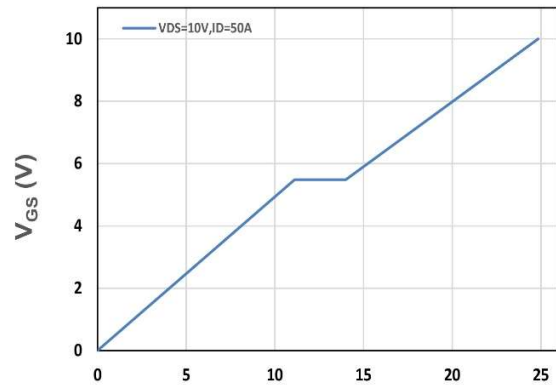


V_{SD} , Source-Drain Voltage(V)
Figure 6. Source-Drain Diode Forward

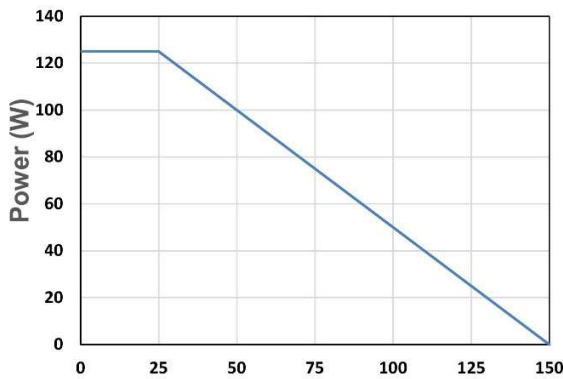
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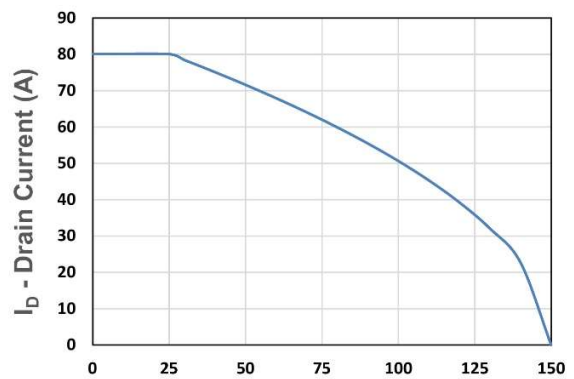
V_{DS} - Drain - Source Voltage (V)
Figure 7. Capacitance



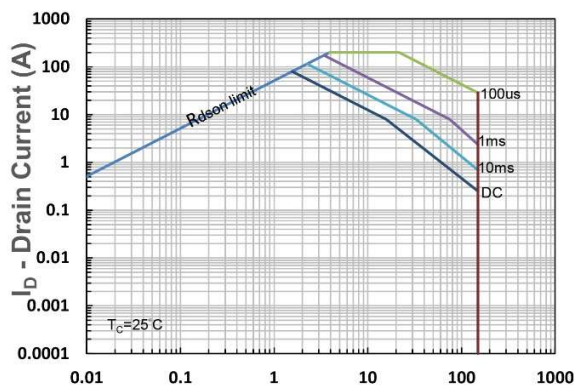
Q_g , Total Gate Charge (nC)
Figure 8. Gate Charge Characteristics



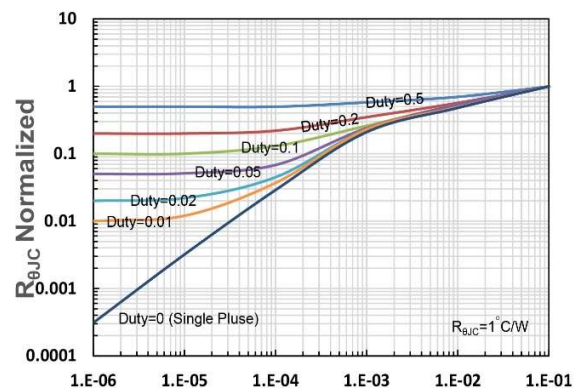
T_c - Case Temperature ($^{\circ}C$)
Figure 9. Power Dissipation



T_c - Case Temperature ($^{\circ}C$)
Figure 10. Drain Current



V_{DS} - Drain-Source Voltage (V)
Figure 11. Safe Operating Area



t_1 , Square Wave Pulse Duration (s)
Figure 12. $R_{\theta JC}$ Transient Thermal Impedance