Power MOSFETS

DATASHEET

LM20F40PGB3A

P-Channel

Enhancement Mode MOSFET

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Quality Management Systems

ISO 9001:2015 Certificate

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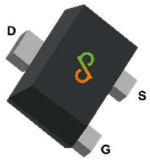
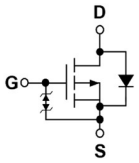
P-Channel Enhancement Mode MOSFET

Pin Description

SOT-723 (TOP view)	Symbol
	

Ordering Information

Symbol	N-Channel	Unit
V_{DSS}	-20	V
$R_{DS(ON)-Max}$	580	mΩ
I_D	-0.51	A

Feature

- Reliable and Rugged
- ROHS Compliant & Halogen-Free
- ESD Protection

Applications

- Small Signal Switch
- Load Switch

Ordering Information

Orderable Part Number	Package Type	Form	Shipping	Marking
LM20F40PGB3A	SOT-723	Tape & Reel	8000 / Tape & Reel	2□

Note：□ = Lot Code

Absolute Maximum Ratings (T_J=25°C Unless Otherwise Noted)

Symbol	Parameter		P-Channel	Unit
V_{DSS}	Drain-Source Voltage		-20	V
V_{GSS}	Gate-Source Voltage		±12	
T_J	Maximum Junction Temperature		150	°C
T_{STG}	Storage Temperature Range		-55 to 150	°C
I_{DM}①	Pulse Drain Current Tested	T_A=25°C	-1.27	A
I_D	Continuous Drain Current	T_A=25°C	-0.51	A
		T_A=70°C	-0.32	
P_D	Maximum Power Dissipation	T_A=25°C	0.15	W
		T_A=70°C	0.1	

Thermal Characteristics

Symbol	Parameter		Rating	Unit
$R_{\theta JA}$②	Thermal Resistance-Junction to Ambient	Steady State	833	°C/W

Note ①：Max. current is limited by junction temperature.
Note ②：Surface Mounted on 1in^2 FR-4 board with 1oz.

P-Channel Electrical Characteristics (T_J=25°C Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Static Electrical Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V, I_{DS}=-250uA	-20	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS}=-16V, V_{GS}=0V	-	-	-1	uA
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS}, I_{DS}=-250uA	-0.5	-0.75	-1	V
I_{GSS}	Gate Leakage Current	V_{GS}=±12V, V_{DS}=0V	-	-	±10	uA
$R_{DS(ON)}$③	Drain-Source On-state Resistance	V_{GS}=-4.5V, I_{DS}=-550mA	-	482	580	mΩ
		V_{GS}=-2.5V, I_{DS}=-450mA	-	666	865	
		V_{GS}=-1.8V, I_{DS}=-350mA	-	1037	1566	
gfs	Forward Transconductance	V_{DS}=-5V, I_{DS}=-550mA	-	1	-	S
Dynamic Characteristics ④						
C_{iss}	Input Capacitance	V_{GS}=0V, V_{DS}=-10V, Freq.=1MHz	-	58	-	pF
C_{oss}	Output Capacitance		-	5.7	-	
C_{rss}	Reverse Transfer Capacitance		-	4.4	-	
$td_{(ON)}$	Turn-on Delay Time	V_{GS}=-4.5V,V_{DS}=-10V, I_D=-1A,R_{GEN}=3Ω	-	0.4	-	uS
t_r	Turn-on Rise Time		-	0.03	-	
$t_{d(OFF)}$	Turn-off Delay Time		-	0.04	-	
t_f	Turn-off Fall Time		-	1.1	-	
Q_g	Total Gate Charge	V_{GS}=-2.5V,V_{DS}=-10V I_D=-0.55A	-	0.6	-	nC
Q_g	Total Gate Charge	V_{GS}=-4.5V,V_{DS}=-10V, I_D=-0.55A	-	1	-	
Q_{gs}	Gate-Source Charge		-	0.17	-	
Q_{gd}	Gate-Drain Charge		-	0.18	-	
Source-Drain Characteristics						
V_{SD}③	Diode Forward Voltage	I_{SD}=-0.55A, V_{GS}=0V	-	-0.75	-1.1	V

Note ③：Pulse test (pulse width≤300us, duty cycle≤2%).
Note ④：Guaranteed by design, not subject to production testing.

P-Channel Typical Characteristics

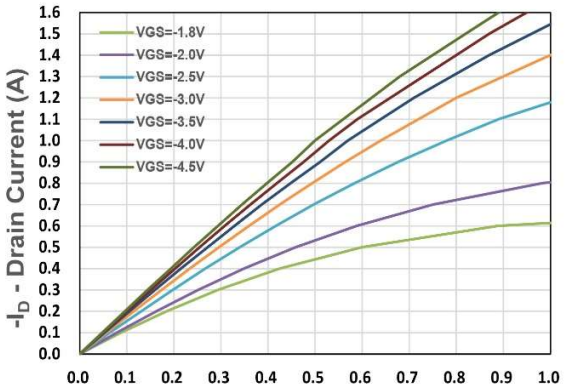


Figure 1. Output Characteristics

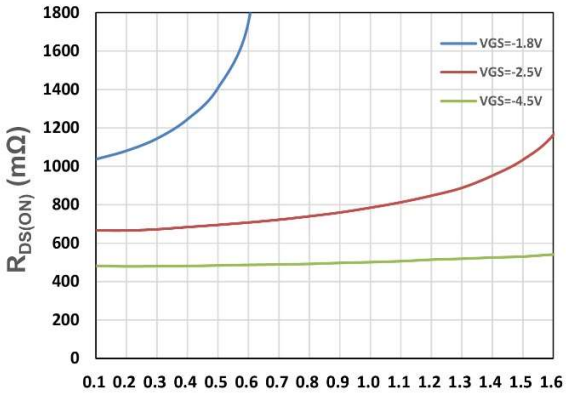


Figure 2. On-Resistance vs. ID

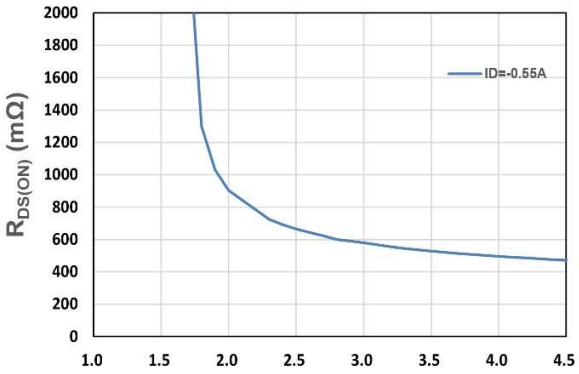


Figure 3. On-Resistance vs. VGS

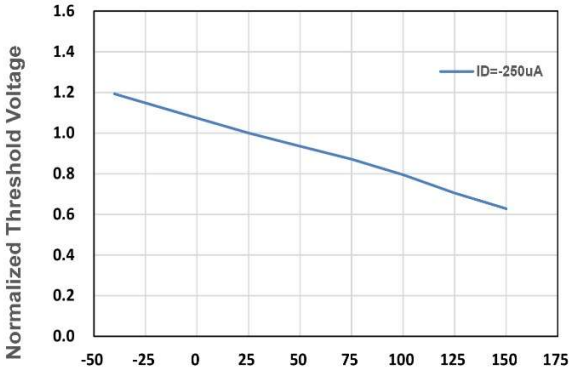


Figure 4. Gate Threshold Voltage

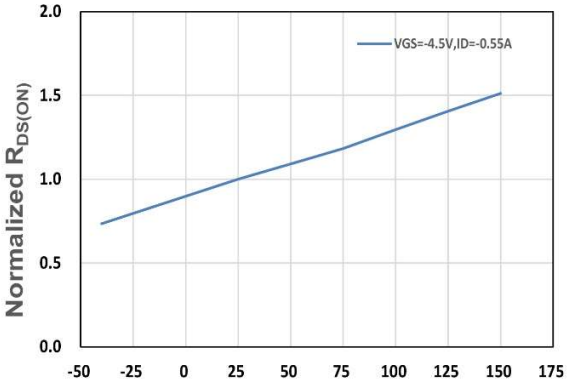


Figure 5. Drain-Source On Resistance

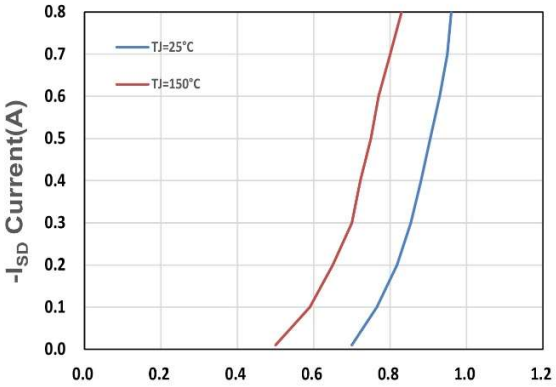


Figure 6. Source-Drain Diode Forward

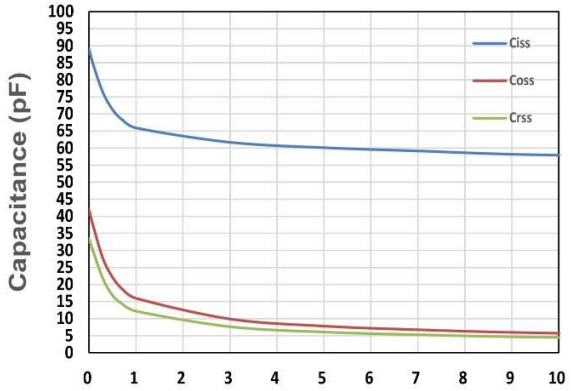


Figure 7. Capacitance

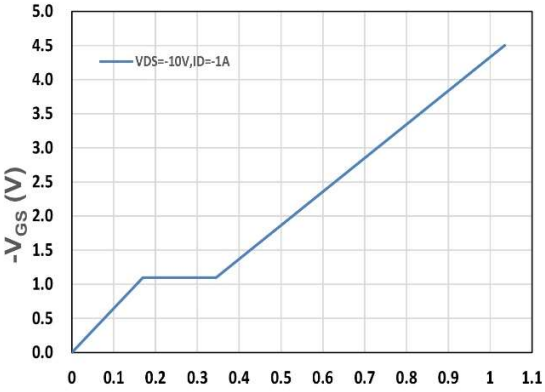


Figure 8. Gate Charge Characteristics

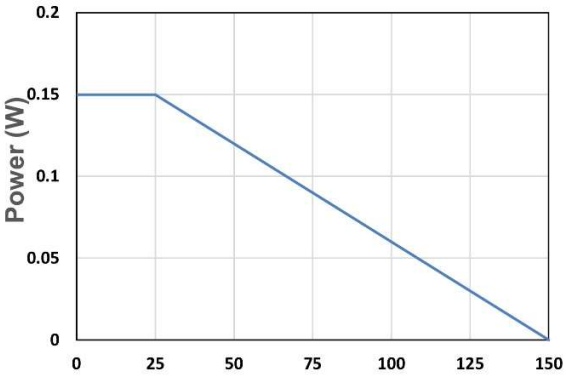


Figure 9. Power Dissipation

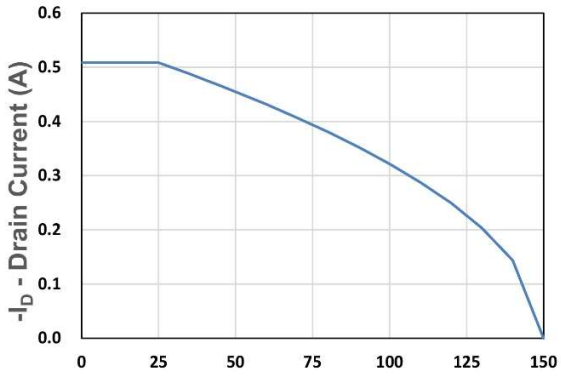


Figure 10. Drain Current

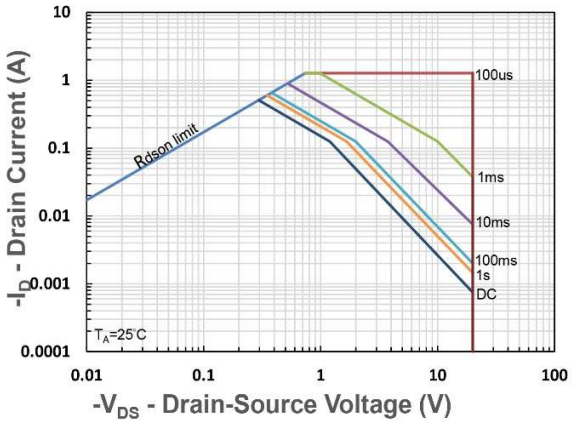


Figure 11. Safe Operating Area

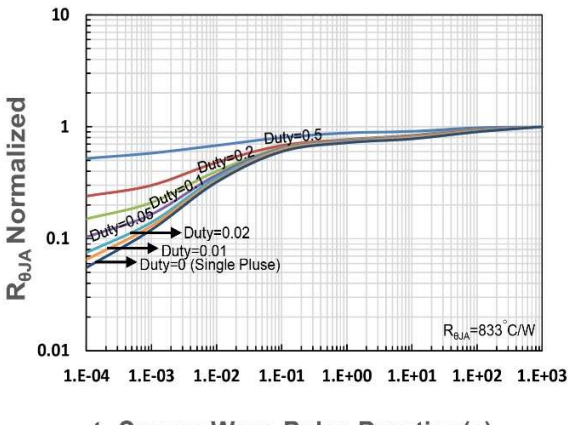


Figure 12. RθJA Transient Thermal Impedance