



Power MOSFETS


DATASHEET

LM60107NHP3A

N-Channel
Enhancement Mode MOSFET

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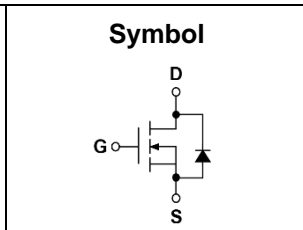
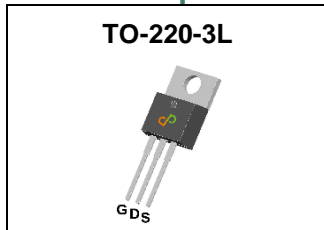


Quality Management Systems

ISO 9001:2015 Certificate

N-Channel Enhancement Mode MOSFET

Pin Description



Product Summary

Symbol	N-Channel	Unit
V_{DSS}	60	V
$R_{DS(ON)-Max}$	10.7	m Ω
ID	88	A

Feature

- Fast switching speed
- Reliable and Rugged
- ROHS Compliant & Halogen-Free
- 100% UIS Tested

Applications

- Power Management in DC/DC Converters
- Power Load Switching Application

Ordering Information

Orderable Part Number	Package Type	Form	Shipping	Marking
LM60107NHP3A	TO-220-3L	Tube	50 / Tube	60107 □□□□□□

Note : □□□□□□ = Lot Code

Absolute Maximum Ratings (T_J=25°C Unless Otherwise Noted)

Symbol	Parameter	N-Channel	Unit	
V_{DSS}	Drain-Source Voltage	60	V	
V_{GSS}	Gate-Source Voltage	±20		
T_J	Maximum Junction Temperature	150	°C	
T_{STG}	Storage Temperature Range	-55 to 150	°C	
I_S	Diode Continuous Forward Current	T _C =25°C	76	A
$I_{DM}^{①}$	Pulse Drain Current Tested	T _C =25°C	220	A
I_D	Continuous Drain Current	T _C =25°C	88	A
		T _C =100°C	56	
P_D	Maximum Power Dissipation	T _C =25°C	83	W
		T _C =100°C	33	
I_D	Continuous Drain Current	T _A =25°C	13.7	A
		T _A =70°C	10.9	
P_D	Maximum Power Dissipation	T _A =25°C	2.0	W
		T _A =70°C	1.3	
$I_{AS}^{②}$	Avalanche Current, Single pulse	L=0.1mH	33	A
		L=0.5mH	21	
$E_{AS}^{②}$	Avalanche Energy, Single pulse	L=0.1mH	54	mJ
		L=0.5mH	110	

Thermal Characteristics

Symbol	Parameter	Rating	Unit	
$R_{\theta JC}$	Thermal Resistance-Junction to Case	Steady State	1.5	°C/W
$R_{\theta JA}^{③}$	Thermal Resistance-Junction to Ambient	Steady State	62.5	°C/W

Note ① : Max. current is limited by bonding wire

Note ② : UIS tested and pulse width are limited by maximum junction temperature 150°C

Note ③ : Surface Mounted on 1in² FR-4 board with 1oz

N-Channel Electrical Characteristics (T_J=25°C Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Static Electrical Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =250uA	60	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	V _{DS} =48V, V _{GS} =0V	-	-	1	uA
V_{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250uA	2	2.8	4	V
I_{GSS}	Gate Leakage Current	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
R_{DS(ON)}^④	Drain-Source On-state Resistance	V _{GS} =10V, I _{DS} =15A	-	8.8	10.7	mΩ
		V _{GS} =6V, I _{DS} =8A	-	12.3	16	
g_{fs}	Forward Transconductance	V _{DS} =5V, I _{DS} =15A	-	20	-	S
Dynamic Characteristics[®]						
R_G	Gate Resistance	V _{GS} =0V, V _{DS} =0V, Freq.=1MHz	-	2.6	-	Ω
C_{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =30V, Freq.=1MHz	-	2490	-	pF
C_{oss}	Output Capacitance					
C_{rss}	Reverse Transfer Capacitance					
t_{d(ON)}	Turn-on Delay Time	V _{GS} =10V, V _{DS} =30V, I _D =1A, R _{GEN} =6Ω	-	17	-	nS
t_r	Turn-on Rise Time					
t_{d(OFF)}	Turn-off Delay Time					
t_f	Turn-off Fall Time					
Q_g	Total Gate Charge	V _{GS} =6V, V _{DS} =30V, I _D =15A	-	32	-	nC
Q_g	Total Gate Charge	V _{GS} =10V, V _{DS} =30V, I _D =15A	-	52	-	
Q_{gs}	Gate-Source Charge		-	20	-	
Q_{gd}	Gate-Drain Charge		-	12	-	
Source-Drain Characteristics						
V_{SD}^④	Diode Forward Voltage	I _{SD} =10A, V _{GS} =0V	-	0.8	1.1	V
t_{rr}	Reverse Recovery Time	I _F =10A, V _R =0V	-	26	-	nS
Q_{rr}	Reverse Recovery Charge	dI _F /dt=100A/μs	-	32	-	nC

Note ④ : Pulse test (pulse width≤300us, duty cycle≤2%).

Note ⑤ : Guaranteed by design, not subject to production testing.

N-Channel Typical Characteristics

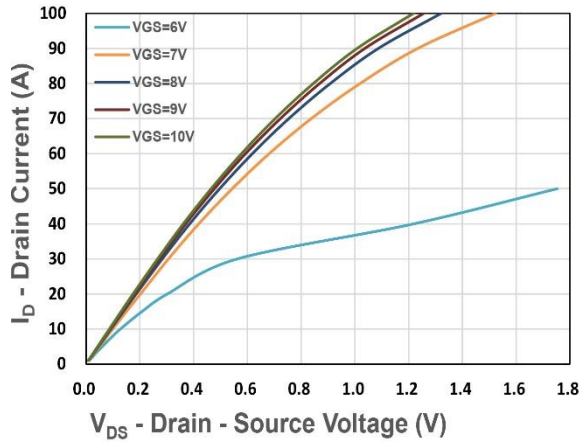


Figure 1. Output Characteristics

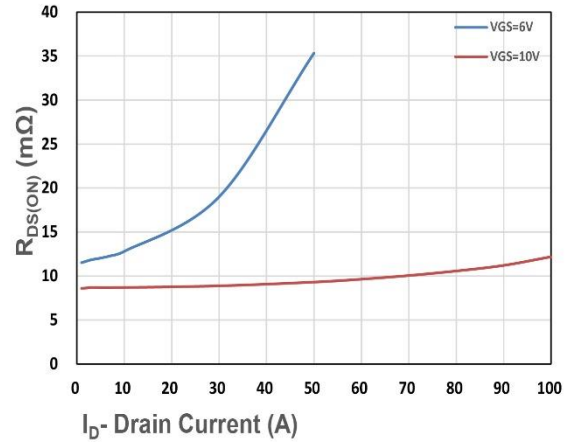


Figure 2. On-Resistance vs. ID

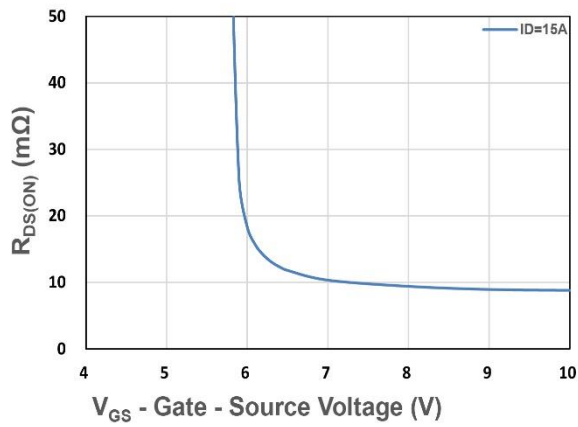


Figure 3. On-Resistance vs. VGS

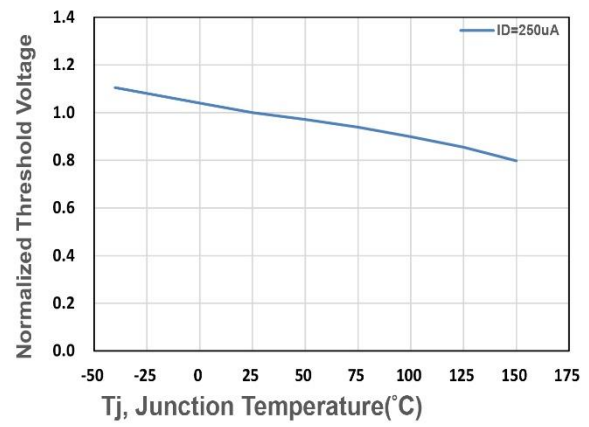


Figure 4. Gate Threshold Voltage

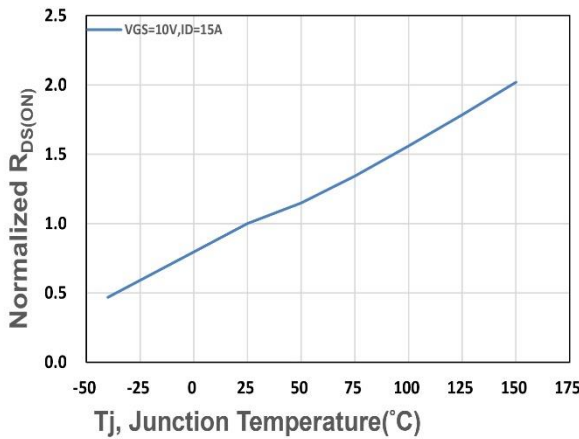


Figure 5. Drain-Source On Resistance

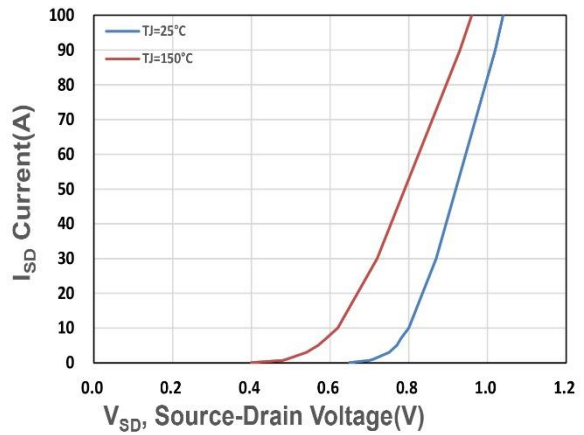
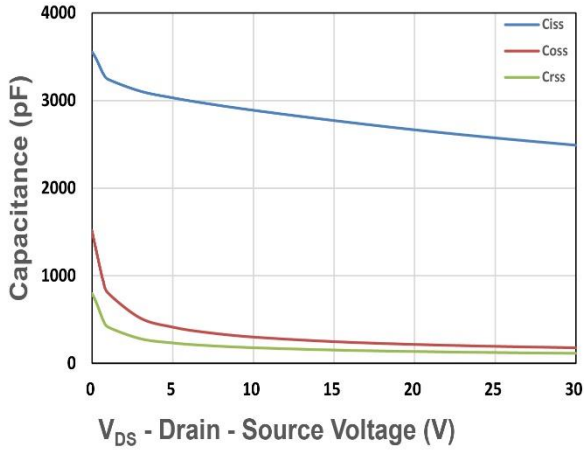
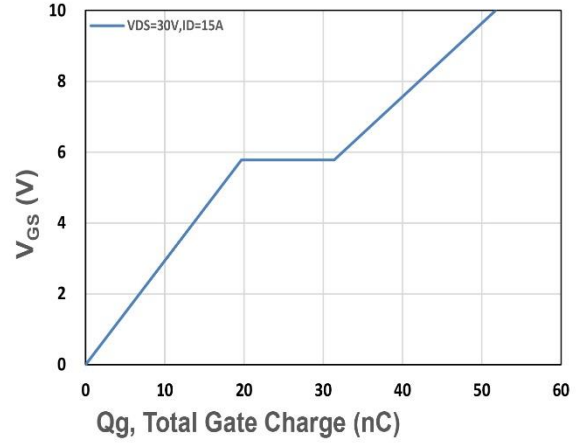


Figure 6. Source-Drain Diode Forward



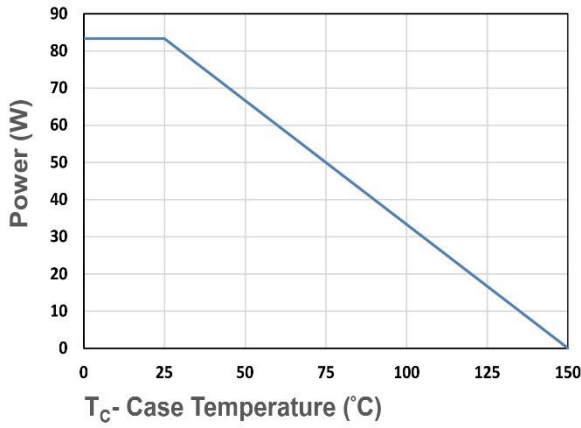
V_{DS} - Drain - Source Voltage (V)

Figure 7. Capacitance



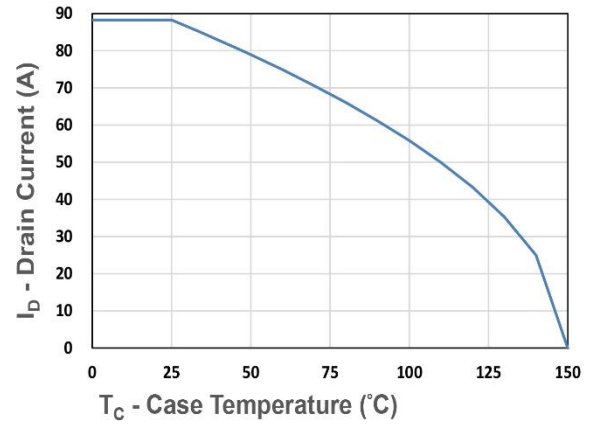
Q_g , Total Gate Charge (nC)

Figure 8. Gate Charge Characteristics



T_C - Case Temperature (°C)

Figure 9. Power Dissipation



T_C - Case Temperature (°C)

Figure 10. Drain Current

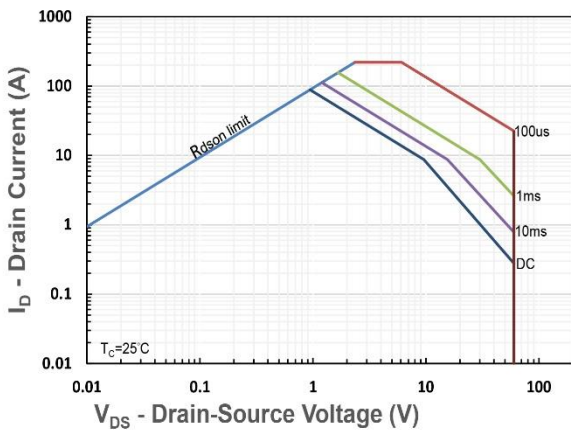


Figure 11. Safe Operating Area

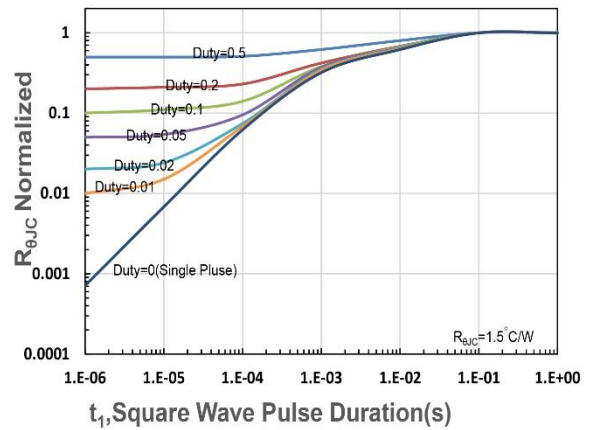


Figure 12. $R_{\theta JC}$ Transient Thermal Impedance