




# Power MOSFETS


## DATASHEET


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**LM1A029NHM8A**

N-Channel  
Enhancement Mode MOSFET

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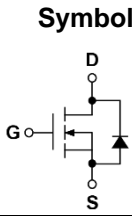
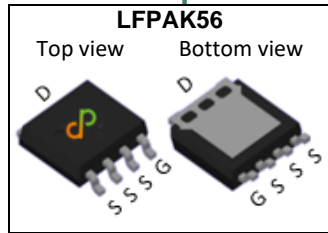


Quality Management Systems

ISO 9001:2015 Certificate

## N-Channel Enhancement Mode MOSFET

### Pin Description



### Product Summary

Symbol	N-Channel	Unit
$V_{DSS}$	100	V
$R_{DS(ON)-Max}$	2.9	m $\Omega$
ID	177	A

### Feature

- Fast switching speed
- Reliable and Rugged
- ROHS Compliant & Halogen-Free
- 100% UIS and Rg Tested
- Moisture Sensitivity Level MSL1

### Applications

- DC-to-DC converters
- Switch mode power supply
- Brushless DC motor control

### Ordering Information

Orderable Part Number	Package Type	Form	Shipping	Marking
LM1A029NHM8A	LFPACK56	Tape & Reel	4000 / Tape & Reel	1A029 □□□□□□

Note : □□□□□□ = Lot Code

### Absolute Maximum Ratings (T<sub>J</sub>=25°C Unless Otherwise Noted)

Symbol	Parameter	N-Channel	Unit
$V_{DSS}$	Drain-Source Voltage	100	V
$V_{GSS}$	Gate-Source Voltage	±20	
$T_J$	Maximum Junction Temperature	175	°C
$T_{STG}$	Storage Temperature Range	-55 to 175	°C
$I_S$	Diode Continuous Forward Current	T <sub>C</sub> =25°C 113	A
$I_{SP}$	Diode Pulse Current	T <sub>C</sub> =25°C 400	A
$I_{DM}$	Pulse Drain Current Tested	T <sub>C</sub> =25°C 443 <sup>①</sup>	A
$I_D$	Continuous Drain Current	T <sub>C</sub> =25°C 177	A
		T <sub>C</sub> =100°C 125	
$P_D$	Maximum Power Dissipation	T <sub>C</sub> =25°C 188	W
		T <sub>C</sub> =100°C 94	
$I_D$	Continuous Drain Current	T <sub>A</sub> =25°C 26.8	A
		T <sub>A</sub> =70°C 22.4	
$P_D$	Maximum Power Dissipation	T <sub>A</sub> =25°C 4.3	W
		T <sub>A</sub> =70°C 3.0	
$I_{AS}^{②}$	Avalanche Current, Single pulse	L=0.5mH 34	A
$E_{AS}^{②}$	Avalanche Energy, Single pulse	L=0.5mH 289	mJ

### Thermal Characteristics

Symbol	Parameter	Rating	Unit
$R_{\theta JC}$	Thermal Resistance-Junction to Case	Steady State 0.8	°C/W
$R_{\theta JA}^{③}$	Thermal Resistance-Junction to Ambient	Steady State 35	°C/W

Note ① : Max. current is limited by junction temperature

Note ② : UIS tested and pulse width are limited by maximum junction temperature 175°C

Note ③ : Surface Mounted on 1in<sup>2</sup> FR-4 board with 1oz

## N-Channel Electrical Characteristics (T<sub>J</sub>=25°C Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
<b>Static Electrical Characteristics</b>						
<b>BV<sub>DSS</sub></b>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>DS</sub> =250uA	100	-	-	V
<b>I<sub>DSS</sub></b>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =80V, V <sub>GS</sub> =0V	-	-	1	uA
<b>V<sub>GS(th)</sub></b>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>DS</sub> =250uA	2.2	3	3.8	V
<b>I<sub>GSS</sub></b>	Gate Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	±100	nA
<b>R<sub>DS(ON)</sub><sup>④</sup></b>	Drain-Source On-state Resistance	V <sub>GS</sub> =10V, I <sub>DS</sub> =50A	-	2.4	2.9	mΩ
		V <sub>GS</sub> =6V, I <sub>DS</sub> =30A		3.8	5	
<b>gfs</b>	Forward Transconductance	V <sub>DS</sub> =5V, I <sub>DS</sub> =10A	-	29	-	S
<b>Dynamic Characteristics<sup>®</sup></b>						
<b>R<sub>G</sub></b>	Gate Resistance	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, Freq.=1MHz	-	0.8	-	Ω
<b>C<sub>iss</sub></b>	Input Capacitance	V <sub>GS</sub> =0V, V <sub>DS</sub> =40V, Freq.=1MHz	-	5293	-	pF
<b>C<sub>oss</sub></b>	Output Capacitance					
<b>C<sub>rss</sub></b>	Reverse Transfer Capacitance					
<b>td(ON)</b>	Turn-on Delay Time	V <sub>GS</sub> =10V, V <sub>DS</sub> =50V, I <sub>D</sub> =1A, R <sub>GEN</sub> =1Ω	-	21	-	nS
<b>t<sub>r</sub></b>	Turn-on Rise Time					
<b>t<sub>d(OFF)</sub></b>	Turn-off Delay Time					
<b>t<sub>f</sub></b>	Turn-off Fall Time					
<b>Q<sub>g</sub></b>	Total Gate Charge	V <sub>GS</sub> =10V, V <sub>DS</sub> =50V, I <sub>D</sub> =20A	-	84	-	nC
<b>Q<sub>gs</sub></b>	Gate-Source Charge					
<b>Q<sub>gd</sub></b>	Gate-Drain Charge					
<b>Source-Drain Characteristics</b>						
<b>V<sub>SD</sub><sup>④</sup></b>	Diode Forward Voltage	I <sub>SD</sub> =10A, V <sub>GS</sub> =0V	-	0.75	1.1	V
<b>t<sub>rr</sub></b>	Reverse Recovery Time	I <sub>F</sub> =10A, V <sub>R</sub> =50V	-	89	-	nS
<b>Q<sub>rr</sub></b>	Reverse Recovery Charge	diF/dt=100A/μs	-	116	-	nC

Note ④ : Pulse test (pulse width≤300us, duty cycle≤2%).

Note ⑤ : Guaranteed by design, not subject to production testing.

## N-Channel Typical Characteristics

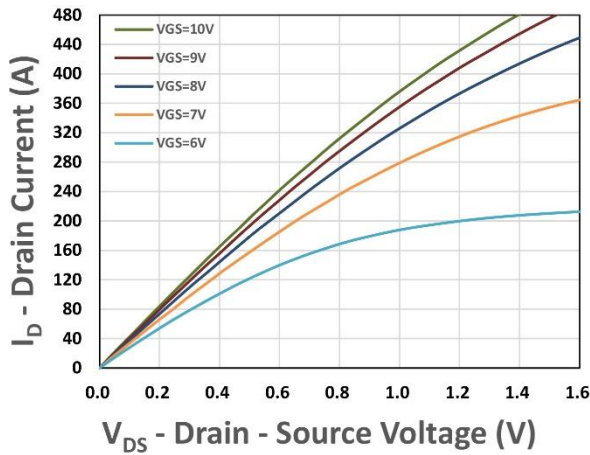


Figure 1. Output Characteristics

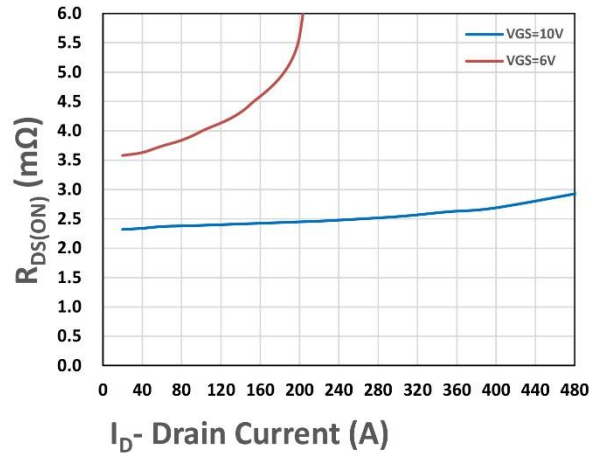


Figure 2. On-Resistance vs. ID

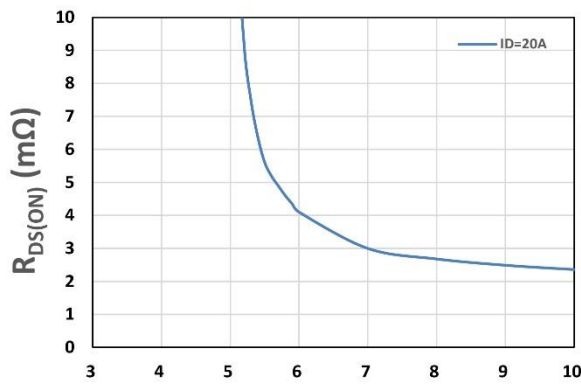


Figure 3. On-Resistance vs. VGS

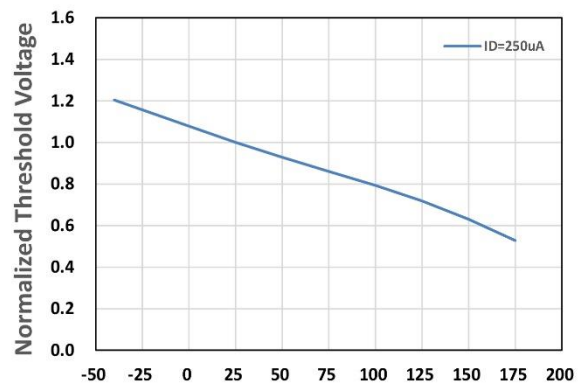


Figure 4. Gate Threshold Voltage

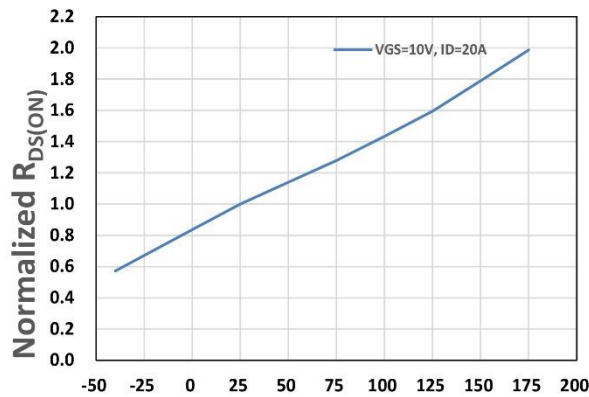


Figure 5. Drain-Source On Resistance

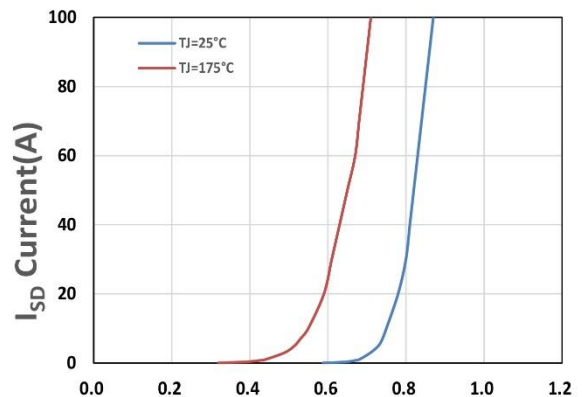
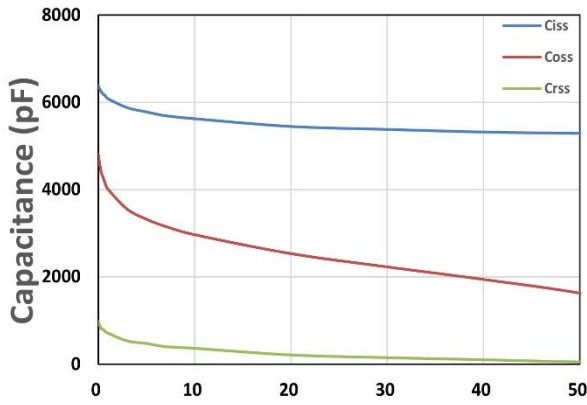
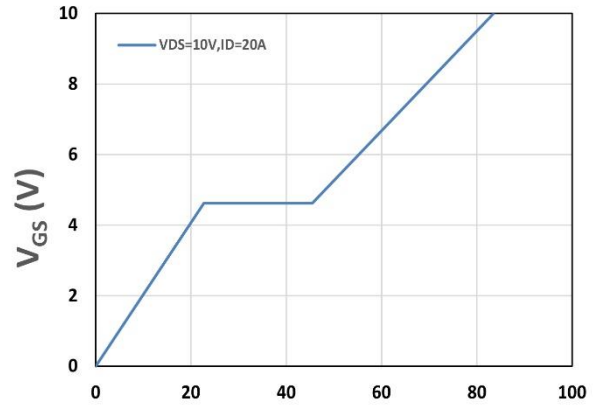


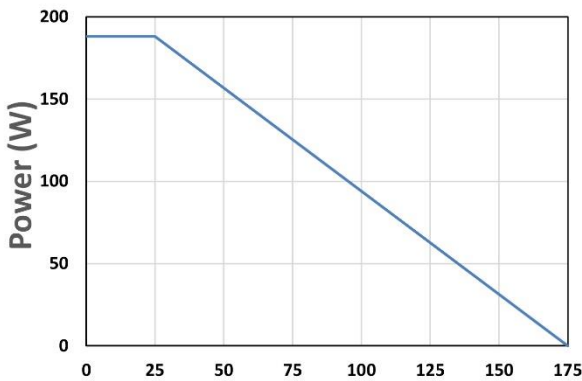
Figure 6. Source-Drain Diode Forward



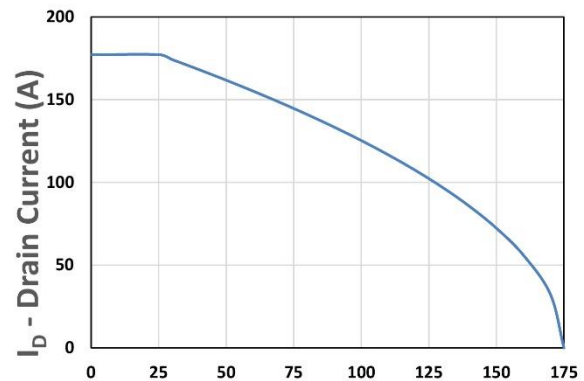
$V_{DS}$  - Drain - Source Voltage (V)  
Figure 7. Capacitance



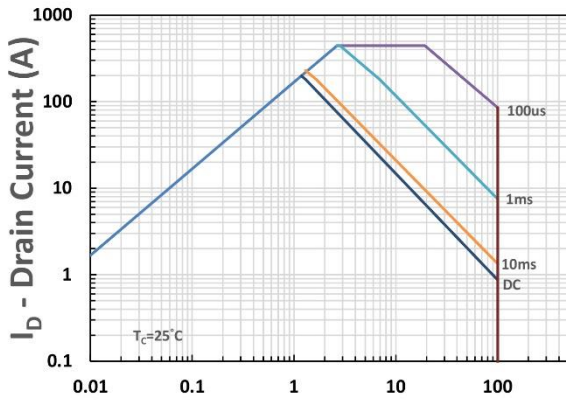
$Q_g$ , Total Gate Charge (nC)  
Figure 8. Gate Charge Characteristics



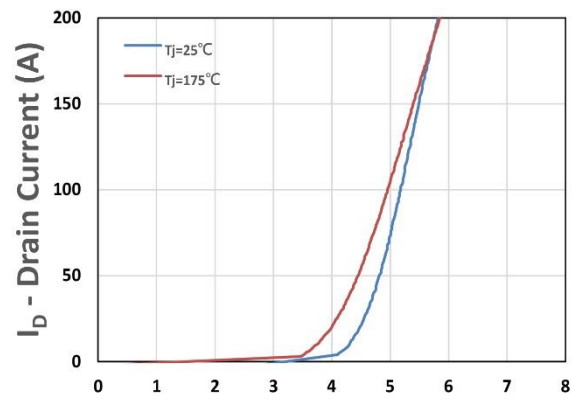
$T_c$ -Case Temperature ( $^{\circ}C$ )  
Figure 9. Power Dissipation



$T_c$ -Case Temperature ( $^{\circ}C$ )  
Figure 10. Drain Current



$V_{DS}$  - Drain-Source Voltage (V)  
Figure 11. Safe Operating Area



$V_{GS}$  - Gate - Source Voltage (V)  
Figure 12. Transfer Characteristics

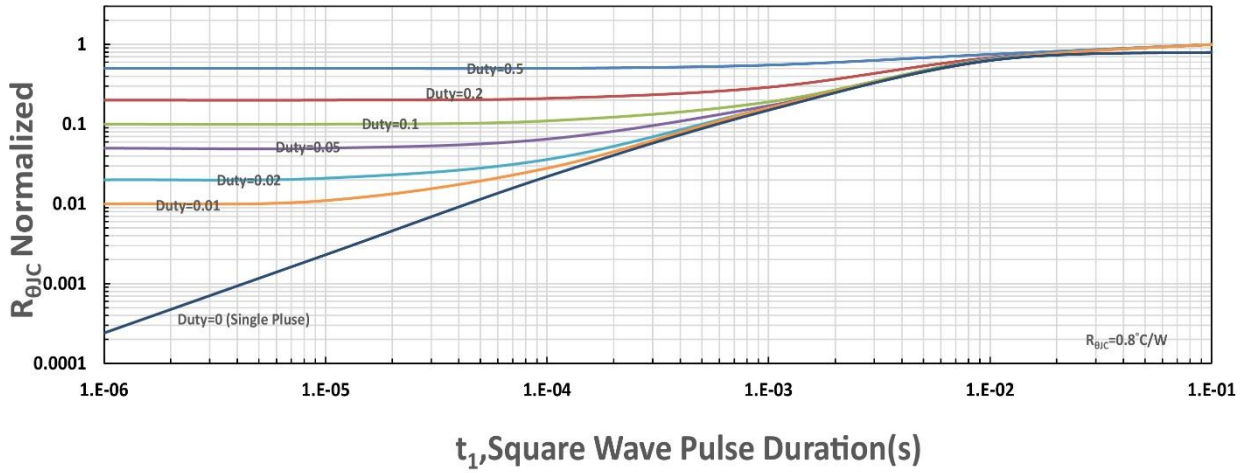


Figure 13.  $R_{\theta JC}$  Transient Thermal Impedance