





Power MOSFETS

DATASHEET

LM60110NAK8A

N-Channel
Enhancement Mode MOSFET

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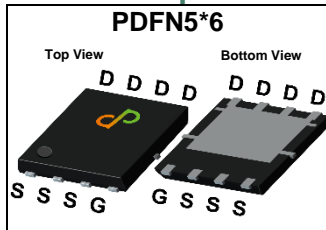


Quality Management Systems

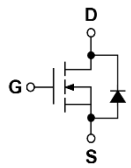
ISO 9001:2015 Certificate

N-Channel Enhancement Mode MOSFET

Pin Description



Symbol



Product Summary

Symbol	N-Channel	Unit
V_{DSS}	60	V
$R_{DS(ON)-Max}$	11	m Ω
ID	65	A

Feature

- Fast switching speed
- Reliable and Rugged
- ROHS Compliant & Halogen-Free
- 100% UIS and Rg Tested

Applications

- Power Management in DC/DC Converters
- USB Power Delivery (USB PD)

Ordering Information

Orderable Part Number	Package Type	Form	Shipping	Marking
LM60110NAK8A	PDFN5*6	Tape & Reel	5000 / Tape & Reel	60110 □□□□□□

Note : □□□□□□ = Lot Code

Absolute Maximum Ratings (T_J=25°C Unless Otherwise Noted)

Symbol	Parameter	N-Channel	Unit
V_{DSS}	Drain-Source Voltage	60	V
V_{GSS}	Gate-Source Voltage	±20	V
T_J	Maximum Junction Temperature	150	°C
T_{STG}	Storage Temperature Range	-55 to 150	°C
I_S	Diode Continuous Forward Current	$T_C=25^\circ C$ 42	A
$I_{DM}^{(1)}$	Pulse Drain Current Tested	$T_C=25^\circ C$ 162	A
I_D	Continuous Drain Current	$T_C=25^\circ C$ 65 $T_C=100^\circ C$ 41	A
P_D	Maximum Power Dissipation	$T_C=25^\circ C$ 46 $T_C=100^\circ C$ 19	W
I_D	Continuous Drain Current	$T_A=25^\circ C$ 15 $T_A=70^\circ C$ 12	A
P_D	Maximum Power Dissipation	$T_A=25^\circ C$ 2.5 $T_A=70^\circ C$ 1.6	W
$I_{AS}^{(2)}$	Avalanche Current, Single pulse	L=0.1mH 24 L=0.5mH 13	A
$E_{AS}^{(2)}$	Avalanche Energy, Single pulse	L=0.1mH 29 L=0.5mH 42	mJ

Thermal Characteristics

Symbol	Parameter	Rating	Unit
$R_{\theta JC}$	Thermal Resistance-Junction to Case	Steady State	2.7
$R_{\theta JA}^{(3)}$	Thermal Resistance-Junction to Ambient	Steady State	50

Note ① : Max. current is limited by junction temperature

Note ② : UIS tested and pulse width are limited by maximum junction temperature 150°C

Note ③ : Surface Mounted on 1in² FR-4 board with 1oz

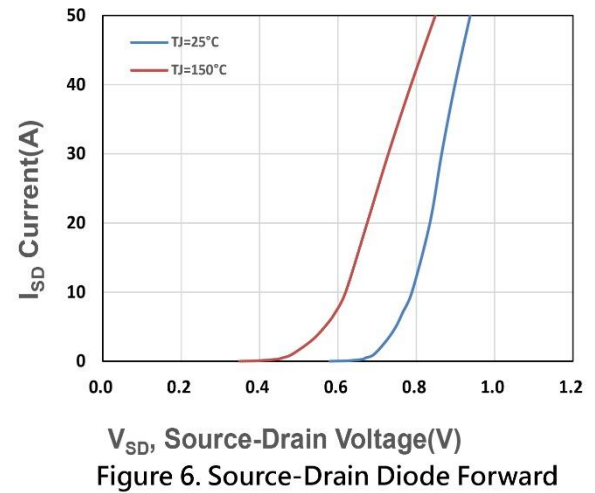
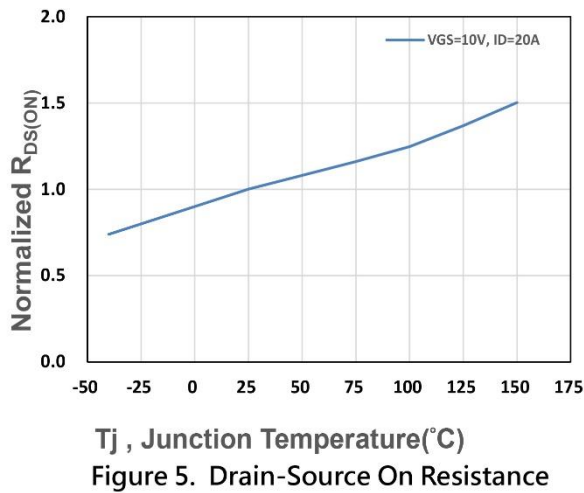
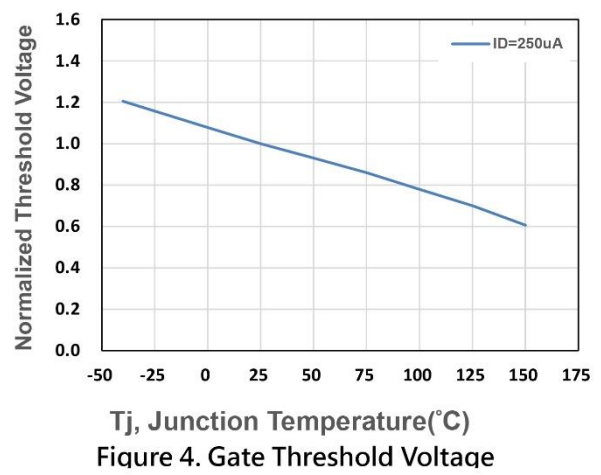
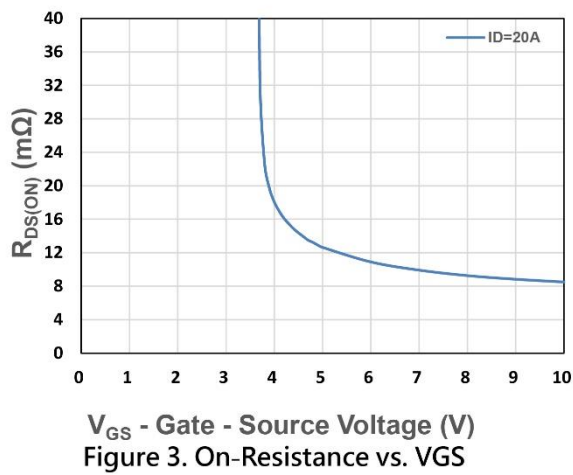
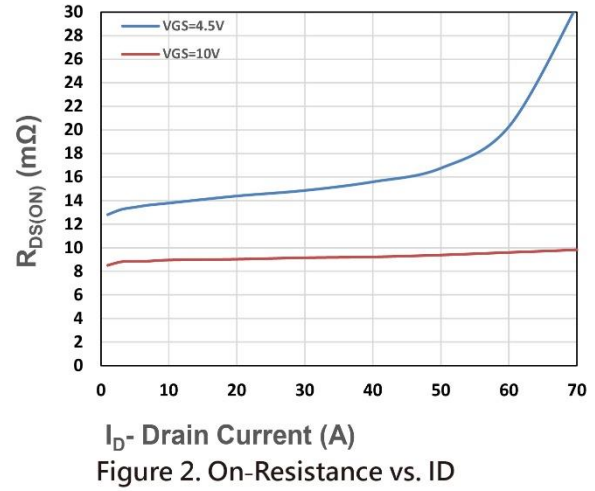
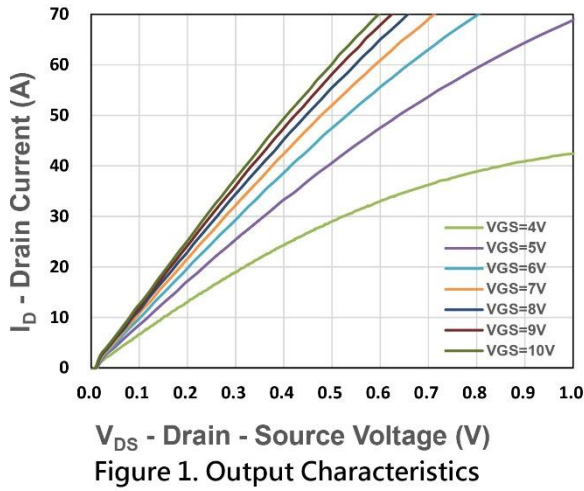
N-Channel Electrical Characteristics (T_J=25°C Unless Otherwise Noted)

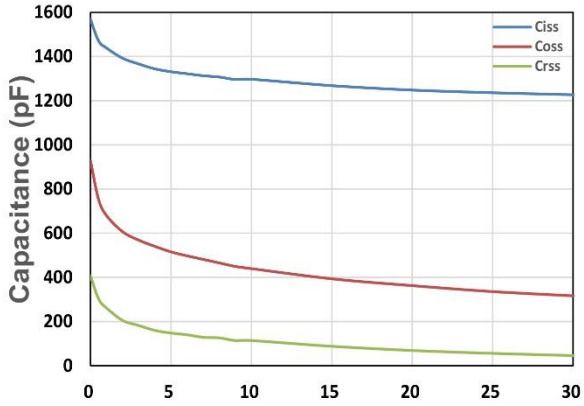
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Static Electrical Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =250uA	60	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	V _{DS} =48V, V _{GS} =0V	-	-	1	uA
V_{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250uA	1	1.8	2.5	V
I_{GSS}	Gate Leakage Current	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
R_{DS(ON)}^④	Drain-Source On-state Resistance	V _{GS} =10V, I _{DS} =20A	-	8.5	11	mΩ
		V _{GS} =4.5V, I _{DS} =10A	-	13	17	
gfs	Forward Transconductance	V _{DS} =5V, I _{DS} =10A	-	16.8	-	S
Dynamic Characteristics[®]						
R_G	Gate Resistance	V _{GS} =0V, V _{DS} =0V, Freq.=1MHz	-	1	-	Ω
C_{ISS}	Input Capacitance	V _{GS} =0V, V _{DS} =30V, Freq.=1MHz	-	1227	-	pF
C_{OSS}	Output Capacitance		-	317	-	
C_{rss}	Reverse Transfer Capacitance		-	46	-	
td(ON)	Turn-on Delay Time	V _{GS} =10V, V _{DS} =30V, I _D =1A, R _{GEN} =1Ω	-	8.8	-	nS
t_r	Turn-on Rise Time		-	19.6	-	
t_{d(OFF)}	Turn-off Delay Time		-	25.8	-	
t_f	Turn-off Fall Time		-	29	-	
Q_g	Total Gate Charge	V _{GS} =4.5V, V _{DS} =30V, I _D =20A	-	11.2	-	nC
Q_g	Total Gate Charge	V _{GS} =10V, V _{DS} =30V, I _D =20A	-	22.3	-	
Q_{gs}	Gate-Source Charge		-	4.9	-	
Q_{gd}	Gate-Drain Charge		-	4.8	-	
Source-Drain Characteristics						
V_{SD}^④	Diode Forward Voltage	I _{SD} =10A, V _{GS} =0V	-	0.8	1.1	V
t_{rr}	Reverse Recovery Time	I _F =10A, V _R =48V	-	22.2	-	nS
Q_{rr}	Reverse Recovery Charge	dI _F /dt=100A/μs	-	11.1	-	nC

Note ④ : Pulse test (pulse width≤300us, duty cycle≤2%).

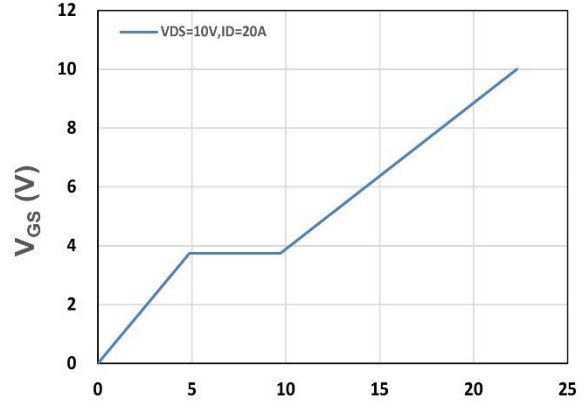
Note ⑤ : Guaranteed by design, not subject to production testing.

N-Channel Typical Characteristics

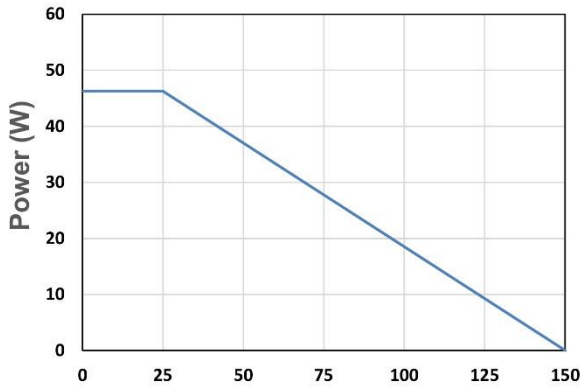




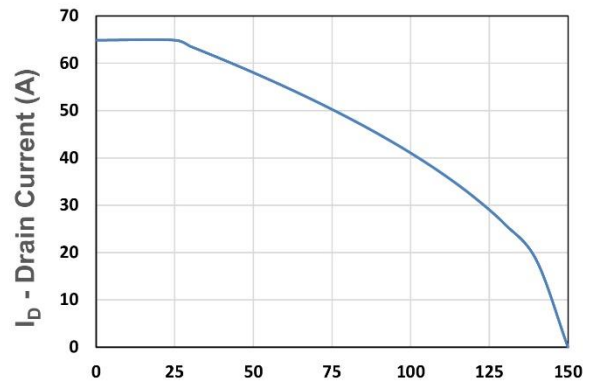
V_{DS} - Drain - Source Voltage (V)
Figure 7. Capacitance



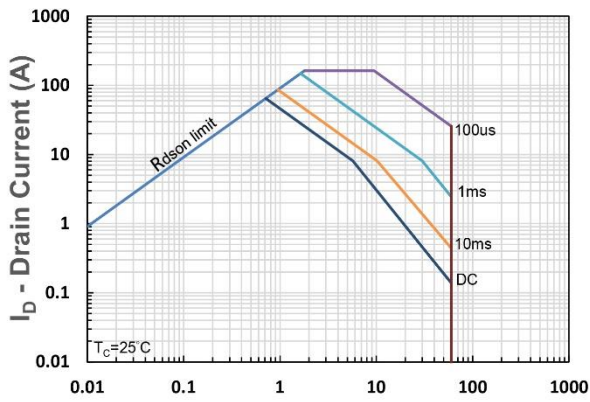
Qg, Total Gate Charge (nC)
Figure 8. Gate Charge Characteristics



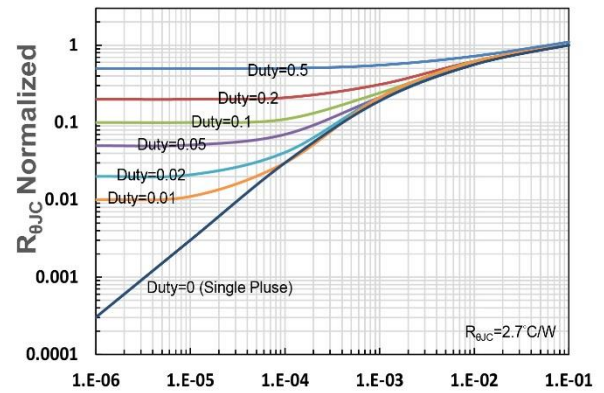
Tc - Case Temperature (°C)
Figure 9. Power Dissipation



Tc - Case Temperature (°C)
Figure 10. Drain Current



V_{DS} - Drain-Source Voltage (V)
Figure 11. Safe Operating Area



t₁, Square Wave Pulse Duration (s)
Figure 12. R_{θJC} Transient Thermal Impedance