Power MOSFETS

DATASHEET

LM20450PLI3A

P-Channel

Enhancement Mode MOSFET

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Quality Management Systems

ISO 9001:2015 Certificate

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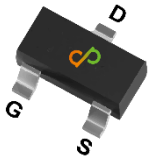
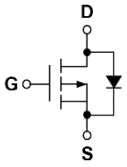
P-Channel Enhancement Mode MOSFET

Pin Description

SOT-23 (TOP view)



Symbol



Ordering Information

Symbol	P-Channel	Unit
V_{DSS}	**-20**	**V**
$R_{DS(ON)-Max}$	**50**	**mΩ**
I_D	**-3.9**	**A**

Feature

- Fast switching speed
- Reliable and Rugged
- ROHS Compliant & Halogen-Free

Applications

- Portable Equipment
- Battery Powered System

Ordering Information

Orderable Part Number	Package Type	Form	Shipping	Marking
LM20450PLI3A	SOT-23	Tape & Reel	3000 / Tape & Reel	14□□□

Note：□□□= Lot Code

Absolute Maximum Ratings (T_J=25°C Unless Otherwise Noted)

Symbol	Parameter		P-Channel	Unit
V_{DSS}	Drain-Source Voltage		-20	V
V_{GSS}	Gate-Source Voltage		±12	
T_J	Maximum Junction Temperature		150	°C
T_{STG}	Storage Temperature Range		-55 to 150	°C
I_{DM}①	Pulse Drain Current Tested	T_A=25°C	-9.8	A
I_D	Continuous Drain Current	T_A=25°C	-3.9	A
		T_A=70°C	-3.1	
P_D	Maximum Power Dissipation	T_A=25°C	1.1	W
		T_A=70°C	0.7	
I_{AS}②	Avalanche Current, Single pulse	L=0.1mH	-8	A
E_{AS}②	Avalanche Energy, Single pulse	L=0.1mH	3.2	mJ

Thermal Characteristics

Symbol	Parameter		Rating	Unit
$R_{\theta JA}$③	Thermal Resistance-Junction to Ambient	Steady State	110	°C/W

Note ①：Max. current is limited by junction temperature.
Note ②：UIS tested and pulse width are limited by maximum junction temperature 150°C
Note ③：Surface Mounted on 1in² FR-4 board with 1oz.

P-Channel Electrical Characteristics (T_J=25°C Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Static Electrical Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V, I_{DS}=-250uA	-20	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS}=-16V, V_{GS}=0V	-	-	-1	uA
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS}, I_{DS}=-250uA	-0.45	-0.7	-0.95	V
I_{GSS}	Gate Leakage Current	V_{GS}=±12V, V_{DS}=0V	-	-	±100	nA
$R_{DS(ON)}$④	Drain-Source On-state Resistance	V_{GS}=-4.5V, I_{DS}=-4.2A	-	42	50	mΩ
		V_{GS}=-2.5V, I_{DS}=-2A		55	71	
		V_{GS}=-1.8V, I_{DS}=-1A	-	75	97	
gfs	Forward Transconductance	V_{DS}=-5V, I_{DS}=-4.2A	-	11	-	S
Dynamic Characteristics ⑤						
R_G	Gate Resistance	V_{GS}=0V,V_{DS}=0V, Freq.=1MHz	-	7.6	-	Ω
C_{iss}	Input Capacitance	V_{GS}=0V, V_{DS}=-10V, Freq.=1MHz	-	707	-	pF
C_{oss}	Output Capacitance		-	77	-	
C_{rss}	Reverse Transfer Capacitance		-	72	-	
$td_{(ON)}$	Turn-on Delay Time	V_{GS}=-4.5V,V_{DS}=-10V, I_D=-1A,R_{GEN}=6Ω	-	5.5	-	nS
t_r	Turn-on Rise Time		-	10.2	-	
$t_{d(OFF)}$	Turn-off Delay Time		-	19.7	-	
t_f	Turn-off Fall Time		-	12.9	-	
Q_g	Total Gate Charge	V_{GS}=-4.5V,V_{DS}=-10V, I_D=-5A	-	5.2	-	nC
Qgs	Gate-Source Charge		-	0.6	-	
Qgd	Gate-Drain Charge		-	1.9	-	
Source-Drain Characteristics						
V_{SD}④	Diode Forward Voltage	I_{SD}=-1A, V_{GS}=0V	-	-0.8	-1.1	V
t_{rr}	Reverse Recovery Time	I_F=-5A, V_R=-10V dI$_F$/dt=100A/μs	-	15.5	-	nS
Q_{rr}	Reverse Recovery Charge		-	6	-	nC

Note ④：Pulse test (pulse width≤300us, duty cycle≤2%).
Note ⑤：Guaranteed by design, not subject to production testing.

P-Channel Typical Characteristics

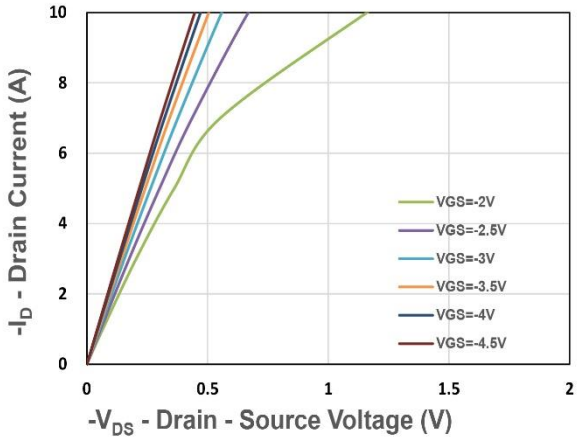


Figure 1. Output Characteristics

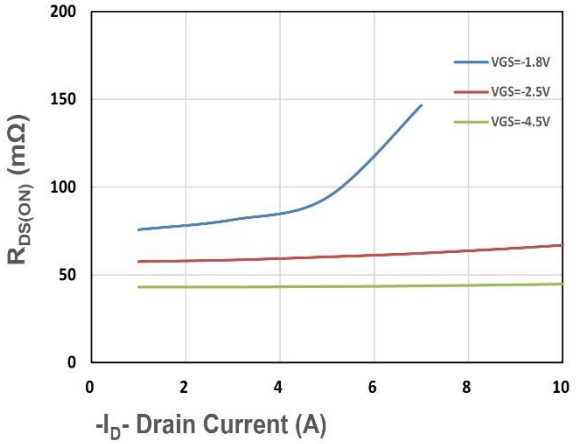


Figure 2. On-Resistance vs. ID

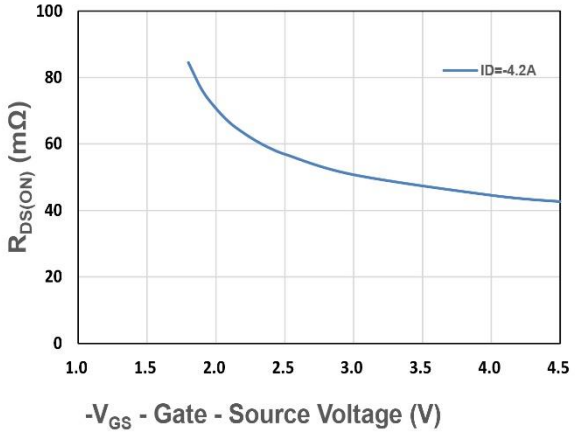


Figure 3. On-Resistance vs. VGS

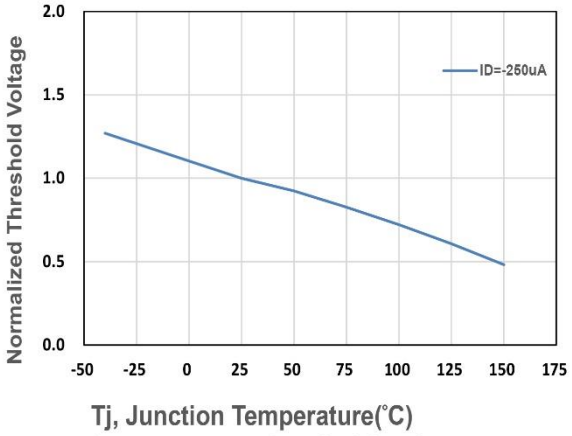


Figure 4. Gate Threshold Voltage

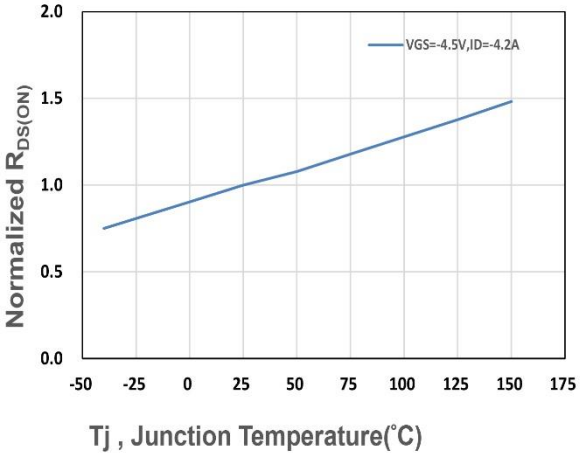


Figure 5. Drain-Source On Resistance

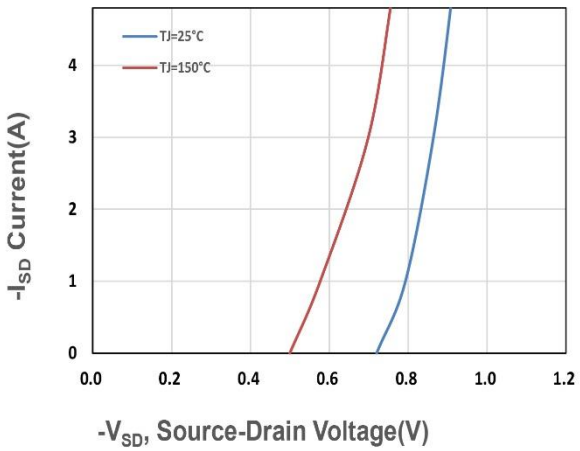


Figure 6. Source-Drain Diode Forward

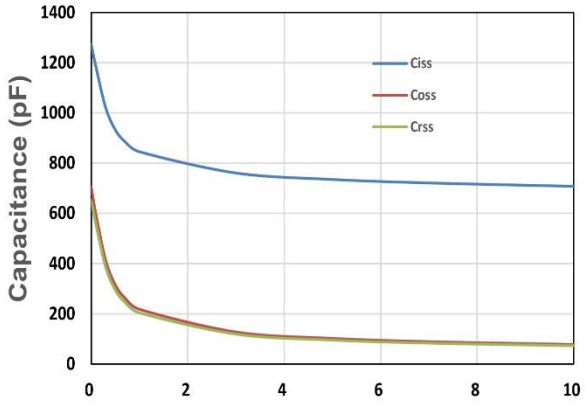


Figure 7. Capacitance

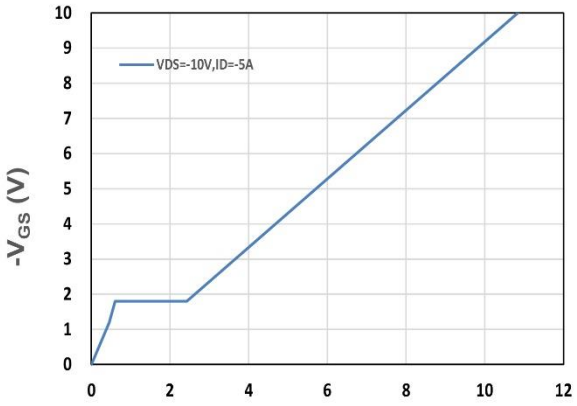


Figure 8. Gate Charge Characteristics

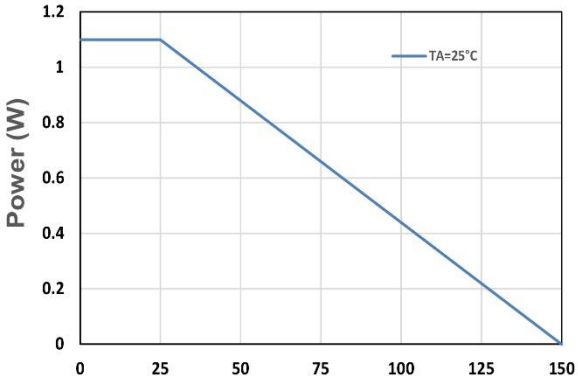


Figure 9. Power Dissipation

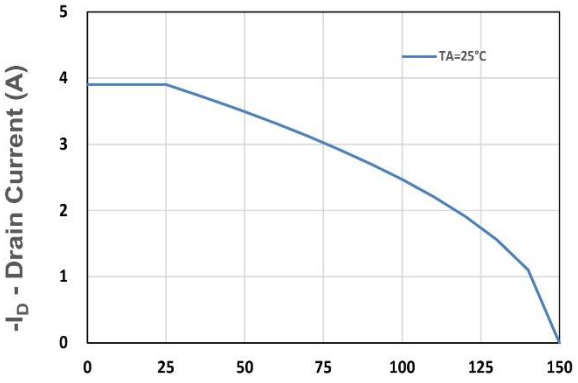


Figure 10. Drain Current

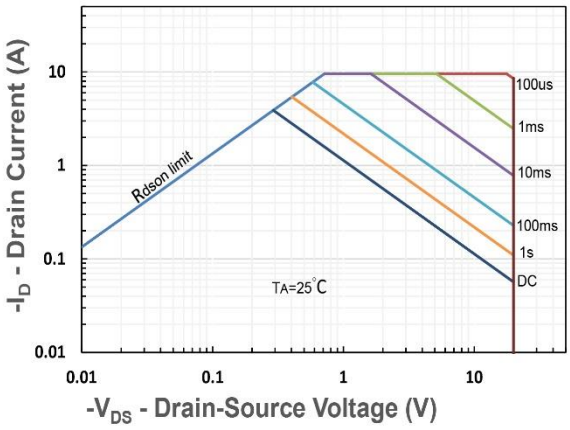


Figure 11. Safe Operating Area

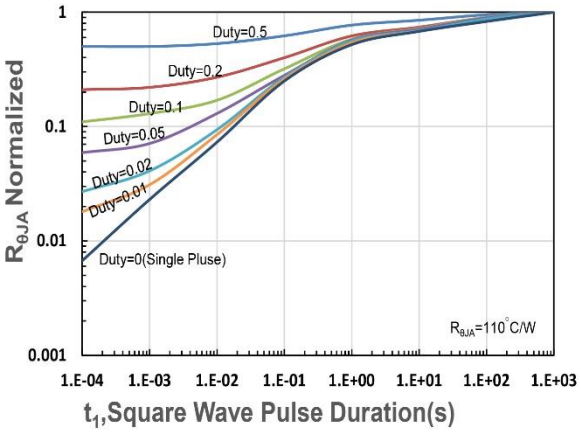


Figure 12. RθJA Transient Thermal Impedance