



Power MOSFETS

DATASHEET

LM1A013NHP3A

N-Channel
Enhancement Mode MOSFET

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Quality Management Systems
ISO 9001:2015 Certificate

N-Channel Enhancement Mode MOSFET**Pin Description**

TO-220-3L (TOP view)	Symbol	Product Summary		
		Symbol	N-Channel	Unit
		V_{DSS}	100	V
		$R_{DS(ON)-Max}$	2.2	$m\Omega$
		ID	243	A

Feature

- High Threshold Voltage
- Reliable and Rugged
- ROHS Compliant & Halogen-Free
- 100% UIS and R_g Tested

Applications

- Battery Management System
- Machine tool
- High power inverter system

Ordering Information

Orderable Part Number	Package Type	Form	Shipping	Marking
LM1A013NHP3A	TO-220-3L	Tube	50 / Tube	1A013 □□□□□□

Note : □□□□□□ = Lot Code

Absolute Maximum Ratings ($T_J=25^\circ C$ Unless Otherwise Noted)

Symbol	Parameter	N-Channel		Unit
V_{DSS}	Drain-Source Voltage	100		V
V_{GSS}	Gate-Source Voltage	± 20		
T_J	Maximum Junction Temperature	175		$^\circ C$
T_{STG}	Storage Temperature Range	-55 to 175		$^\circ C$
I_S	Diode Continuous Forward Current	$T_c=25^\circ C$	114	A
$I_{DM}^{\text{①}}$	Pulse Drain Current Tested	$T_c=25^\circ C$	608	A
I_D	Continuous Drain Current	$T_c=25^\circ C$	243	A
		$T_c=100^\circ C$	172	
P_D	Maximum Power Dissipation	$T_c=25^\circ C$	250	W
		$T_c=100^\circ C$	125	
I_D	Continuous Drain Current	$T_a=25^\circ C$	24.7	A
		$T_a=70^\circ C$	20.7	
P_D	Maximum Power Dissipation	$T_a=25^\circ C$	2.6	W
		$T_a=70^\circ C$	1.8	
$I_{AS}^{\text{②}}$	Avalanche Current, Single pulse	$L=0.1mH$	100	A
		$L=0.5mH$	55	
$E_{AS}^{\text{②}}$	Avalanche Energy, Single pulse	$L=0.1mH$	500	mJ
		$L=0.5mH$	756	

Thermal Characteristics

Symbol	Parameter	Rating	Unit
$R_{θJC}$	Thermal Resistance-Junction to Case	Steady State	$^\circ C/W$
$R_{θJA}^{\text{③}}$	Thermal Resistance-Junction to Ambient	Steady State	$^\circ C/W$

Note ① : Max. current is limited by junction temperature

Note ② : UIS tested and pulse width are limited by maximum junction temperature $150^\circ C$ Note ③ : Surface Mounted on 1in² FR-4 board with 1oz

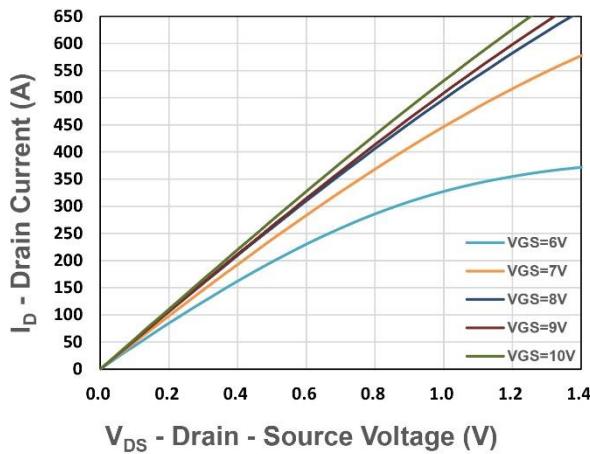
N-Channel Electrical Characteristics ($T_J=25^\circ\text{C}$ Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Static Electrical Characteristics						
$\mathbf{BV_{DSS}}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}, I_{DS}=250\mu\text{A}$	100	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=80\text{V}, V_{GS}=0\text{V}$	-	-	1	μA
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_{DS}=250\mu\text{A}$	2	3	4	V
I_{GSS}	Gate Leakage Current	$V_{GS}=\pm 20\text{V}, V_{DS}=0\text{V}$	-	-	± 100	nA
$R_{DS(\text{ON})}^{(4)}$	Drain-Source On-state Resistance	$V_{GS}=10\text{V}, I_{DS}=30\text{A}$	-	1.8	2.2	$\text{m}\Omega$
g_{fs}	Forward Transconductance	$V_{DS}=5\text{V}, I_{DS}=50\text{A}$	-	140	-	S
Dynamic Characteristics ⁽⁵⁾						
R_G	Gate Resistance	$V_{GS}=0\text{V}, V_{DS}=0\text{V},$ Freq.=1MHz	-	0.75	-	Ω
C_{iss}	Input Capacitance	$V_{GS}=0\text{V},$ $V_{DS}=50\text{V},$ Freq.=1MHz	-	12340	-	pF
C_{oss}	Output Capacitance		-	3995	-	
C_{rss}	Reverse Transfer Capacitance		-	37	-	
$t_{d(\text{ON})}$	Turn-on Delay Time	$V_{GS}=10\text{V}, V_{DS}=50\text{V},$ $I_D=1\text{A}, R_{\text{GEN}}=1\Omega$	-	37	-	nS
t_r	Turn-on Rise Time		-	21	-	
$t_{d(\text{OFF})}$	Turn-off Delay Time		-	78	-	
t_f	Turn-off Fall Time		-	107	-	
Q_g	Total Gate Charge	$V_{GS}=6\text{V}, V_{DS}=50\text{V}$ $I_D=30\text{A}$	-	133	-	nC
Q_g	Total Gate Charge	$V_{GS}=10\text{V}, V_{DS}=50\text{V},$ $I_D=30\text{A}$	-	203	-	
Q_{gs}	Gate-Source Charge		-	60	-	
Q_{gd}	Gate-Drain Charge		-	53	-	
Source-Drain Characteristics						
$V_{SD}^{(4)}$	Diode Forward Voltage	$I_{SD}=30\text{A}, V_{GS}=0\text{V}$	-	0.75	1.1	V
t_{rr}	Reverse Recovery Time	$I_F=10\text{A}, V_R=30\text{V}$	-	119	-	nS
Q_{rr}	Reverse Recovery Charge	$dI_F/dt=100\text{A}/\mu\text{s}$	-	347	-	nC

Note ④ : Pulse test (pulse width $\leq 300\text{us}$, duty cycle $\leq 2\%$).

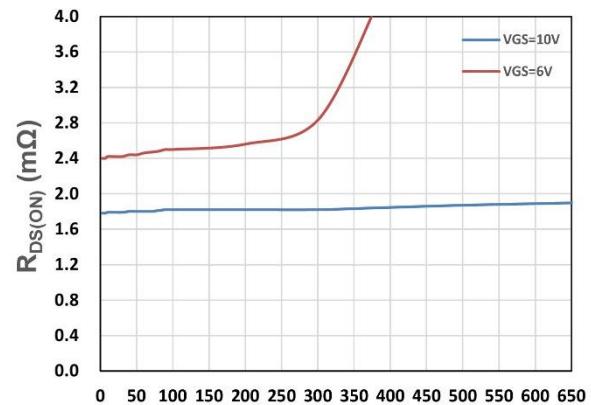
Note ⑤ : Guaranteed by design, not subject to production testing.

N-Channel Typical Characteristics



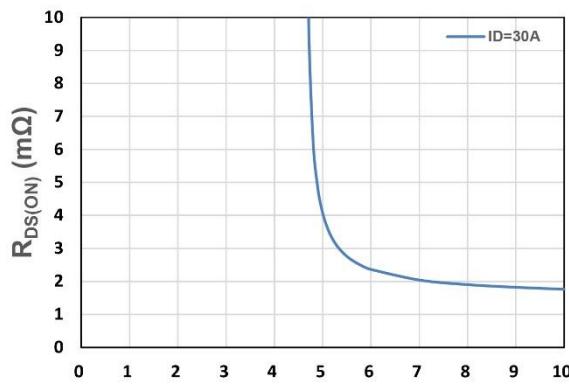
V_{DS} - Drain - Source Voltage (V)

Figure 1. Output Characteristics



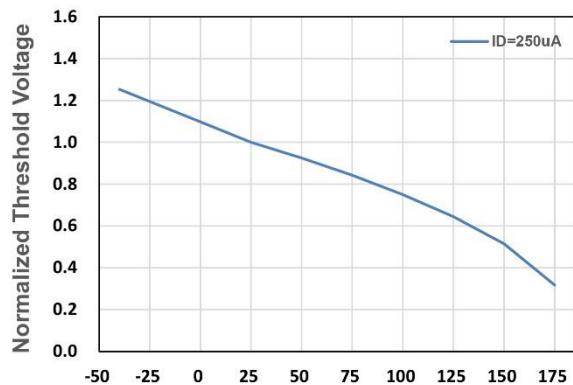
I_D - Drain Current (A)

Figure 2. On-Resistance vs. ID



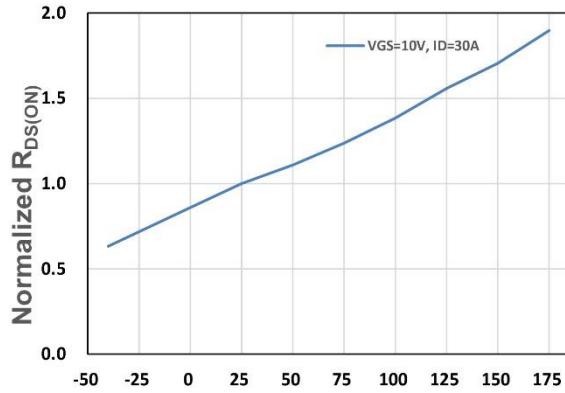
V_{GS} - Gate - Source Voltage (V)

Figure 3. On-Resistance vs. VGS



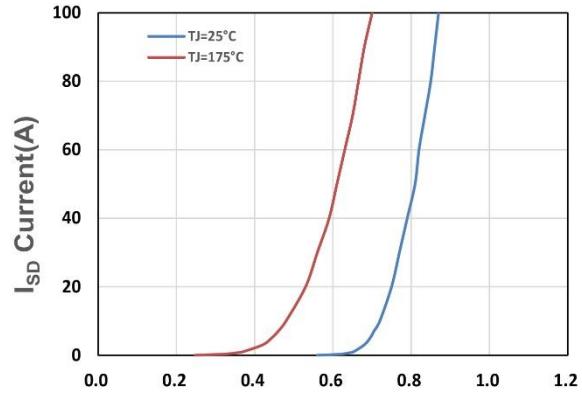
T_j , Junction Temperature(°C)

Figure 4. Gate Threshold Voltage



T_j , Junction Temperature(°C)

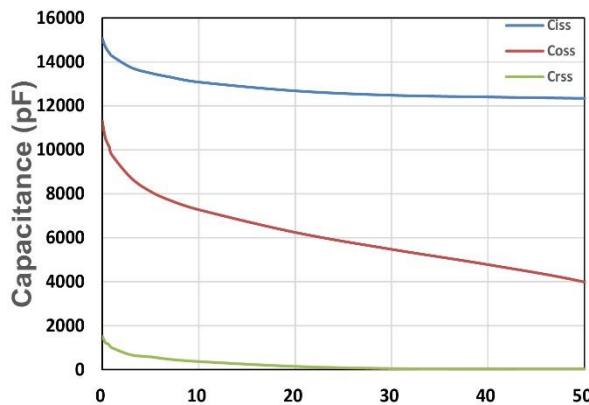
Figure 5. Drain-Source On Resistance



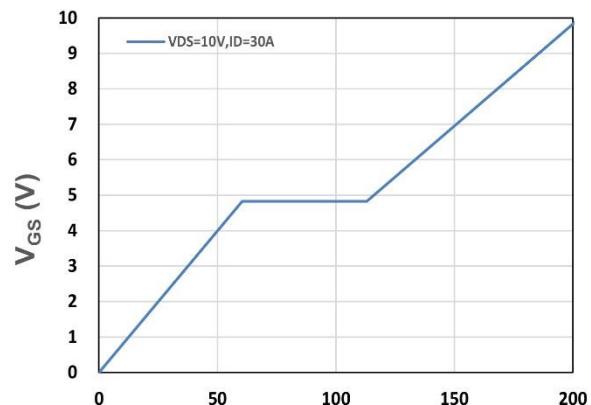
V_{SD} , Source-Drain Voltage(V)

Figure 6. Source-Drain Diode Forward

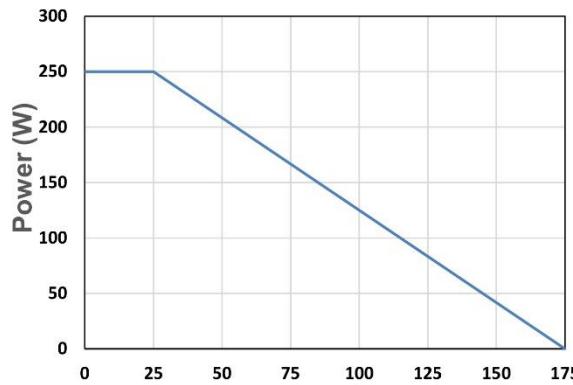
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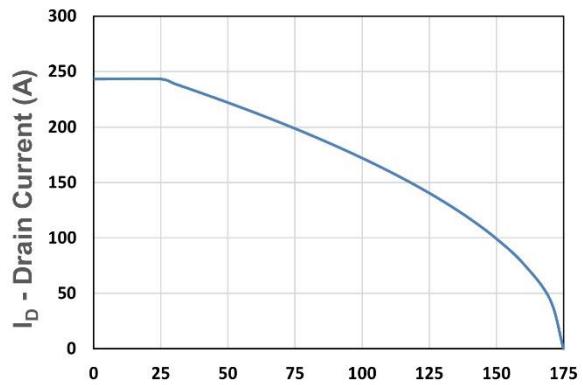
V_{DS} - Drain - Source Voltage (V)
Figure 7. Capacitance



Q_g , Total Gate Charge (nC)
Figure 8. Gate Charge Characteristics



T_c - Case Temperature (°C)
Figure 9. Power Dissipation



I_D - Drain Current (A)
Figure 10. Drain Current

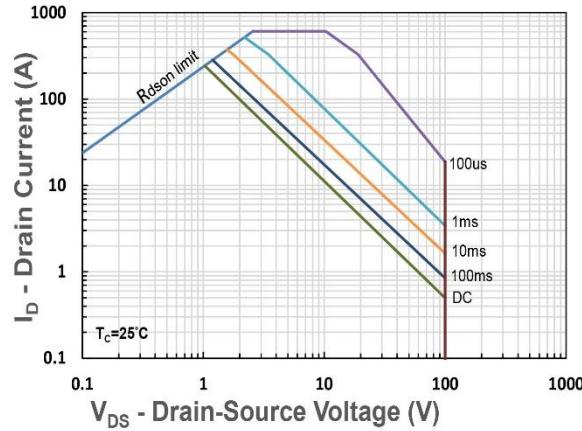


Figure 11. Safe Operating Area

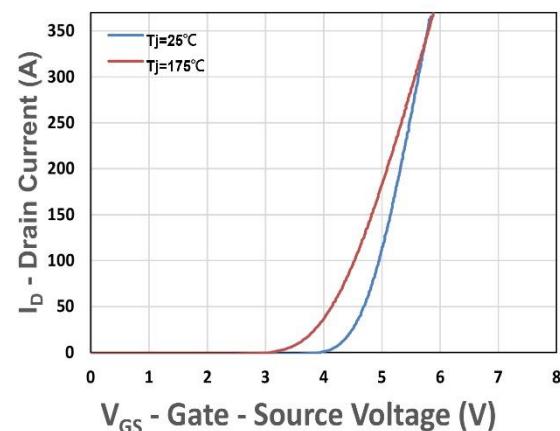


Figure 12. Transfer Characteristics

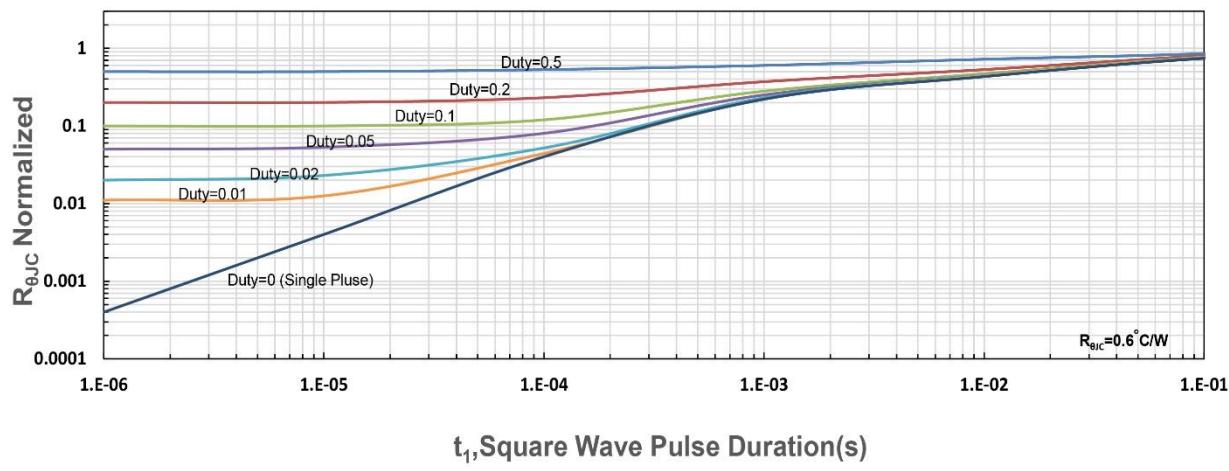


Figure 13. $R_{\theta JC}$ Transient Thermal Impedance