



Power MOSFETS

DATASHEET

LM1A013NHV2A

N-Channel
Enhancement Mode MOSFET



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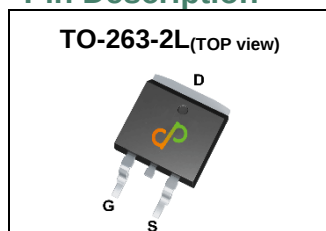


Quality Management Systems

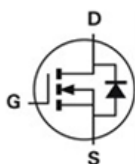
ISO 9001:2015 Certificate

N-Channel Enhancement Mode MOSFET

Pin Description



Symbol



Product Summary

Symbol	N-Channel	Unit
V_{DSS}	100	V
$R_{DS(ON)-Max}$	1.7	mΩ
I_D	362	A

Feature

- High Threshold Voltage
- Reliable and Rugged
- ROHS Compliant & Halogen-Free
- 100% UIS and Rg Tested

Applications

- Battery Management System
- Machine tool
- High power inverter system

Ordering Information

Orderable Part Number	Package Type	Form	Shipping	Marking
LM1A013NHV2A	TO-263-2L	Tape & Reel	800 / Tape & Reel	1A013 □□□□□□

Note : □□□□□□ = Lot Code

Absolute Maximum Ratings ($T_J=25^\circ\text{C}$ Unless Otherwise Noted)

Symbol	Parameter		N-Channel	Unit
V _{DSS}	Drain-Source Voltage		100	V
V _{GSS}	Gate-Source Voltage		±20	
T _J	Maximum Junction Temperature		175	°C
T _{STG}	Storage Temperature Range		-55 to 175	°C
I _S	Diode Continuous Forward Current	T _C =25°C	195	A
I _{DM}	Pulse Drain Current Tested	T _C =25°C	906	A
I _D	Continuous Drain Current	T _C =25°C	362 ^①	A
		T _C =100°C	256	
P _D	Maximum Power Dissipation	T _C =25°C	429	W
		T _C =100°C	214	
I _D	Continuous Drain Current	T _A =25°C	32	A
		T _A =70°C	26.7	
P _D	Maximum Power Dissipation	T _A =25°C	3.3	W
		T _A =70°C	2.3	
I _{AS} ^②	Avalanche Current, Single pulse	L=0.1mH	100	A
		L=0.5mH	55	
E _{AS} ^②	Avalanche Energy, Single pulse	L=0.1mH	500	mJ
		L=0.5mH	756	

Thermal Characteristics

Symbol	Parameter		Rating	Unit
R _{θJC}	Thermal Resistance-Junction to Case	Steady State	0.35	°C/W
R _{θJA} ^③	Thermal Resistance-Junction to Ambient	Steady State	45	°C/W

Note ① : Max. current is limited by bonding wire

Note ② : UIS tested and pulse width are limited by maximum junction temperature 150°C

Note ③ : Surface Mounted on 1in^2 FR-4 board with 1oz

N-Channel Electrical Characteristics ($T_J=25^{\circ}\text{C}$ Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Static Electrical Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =250uA	100	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =80V, V _{GS} =0V	-	-	1	uA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250uA	2	3	4	V
I _{GSS}	Gate Leakage Current	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
R _{DS(ON)} ®	Drain-Source On-state Resistance	V _{GS} =10V, I _{DS} =30A	-	1.4	1.7	mΩ
gfs	Forward Transconductance	V _{DS} =5V, I _{DS} =50A	-	150	-	S
Dynamic Characteristics ®						
R _G	Gate Resistance	V _{GS} =0V,V _{DS} =0V, Freq.=1MHz	-	0.8	-	Ω
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =50V, Freq.=1MHz	-	12345	-	pF
C _{oss}	Output Capacitance		-	3995	-	
C _{rss}	Reverse Transfer Capacitance		-	37	-	
td(ON)	Turn-on Delay Time	V _{GS} =10V,V _{DS} =50V, I _D =1A,R _{GEN} =1Ω	-	37	-	nS
tr	Turn-on Rise Time		-	21	-	
td(OFF)	Turn-off Delay Time		-	78	-	
tf	Turn-off Fall Time		-	107	-	
Qg	Total Gate Charge	V _{GS} =6V,V _{DS} =50V I _D =30A	-	133	-	nC
Qg	Total Gate Charge	V _{GS} =10V,V _{DS} =50V, I _D =30A	-	203	-	
Qgs	Gate-Source Charge		-	60	-	
Qgd	Gate-Drain Charge		-	53	-	
Source-Drain Characteristics						
VSD®	Diode Forward Voltage	ISD=30A, VGS=0V	-	0.8	1.1	V
trr	Reverse Recovery Time	IF=10A, VR=30V	-	119	-	nS
Qrr	Reverse Recovery Charge	dIF/dt=100A/μs	-	347	-	nC

Note ④ : Pulse test (pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$).

Note ⑤ : Guaranteed by design, not subject to production testing.

N-Channel Typical Characteristics

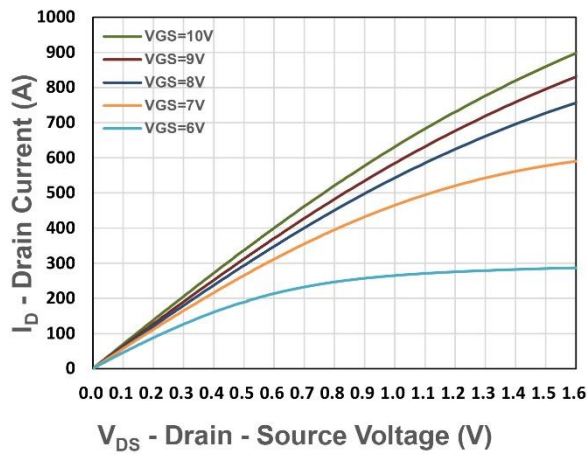


Figure 1. Output Characteristics

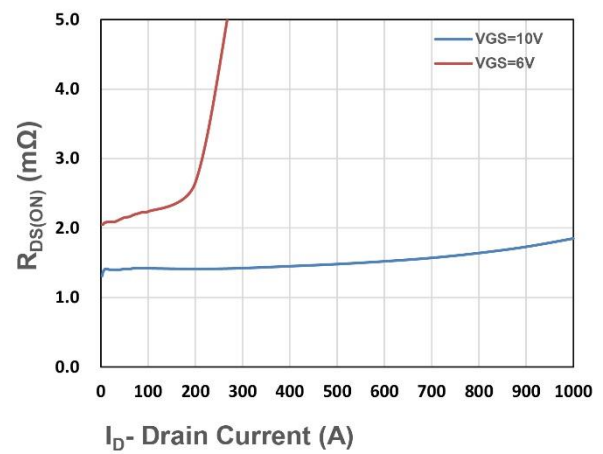


Figure 2. On-Resistance vs. I_D

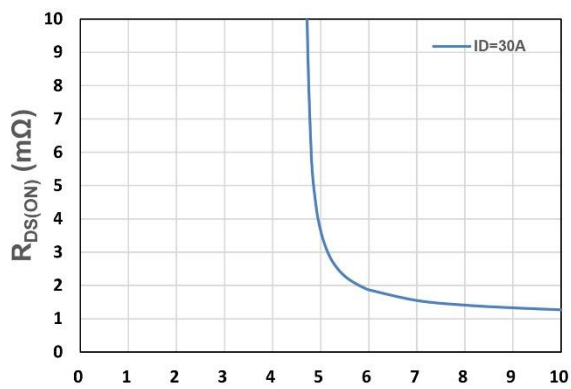


Figure 3. On-Resistance vs. V_{GS}

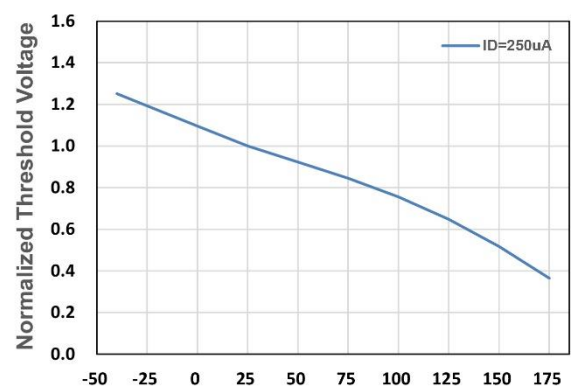


Figure 4. Gate Threshold Voltage

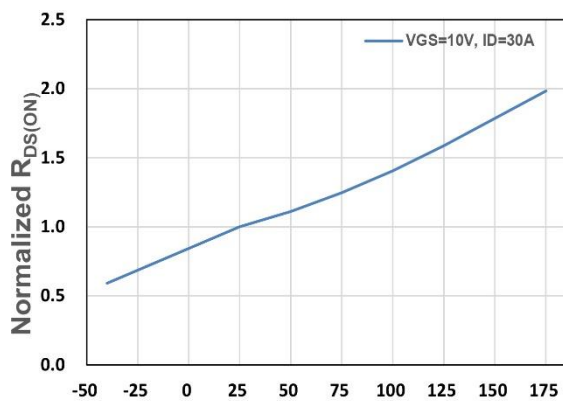


Figure 5. Drain-Source On Resistance

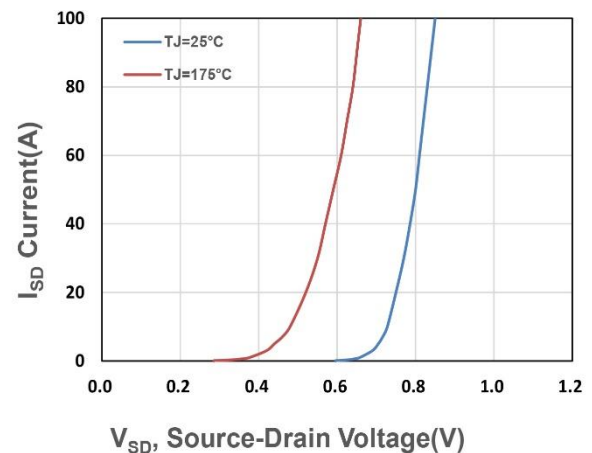
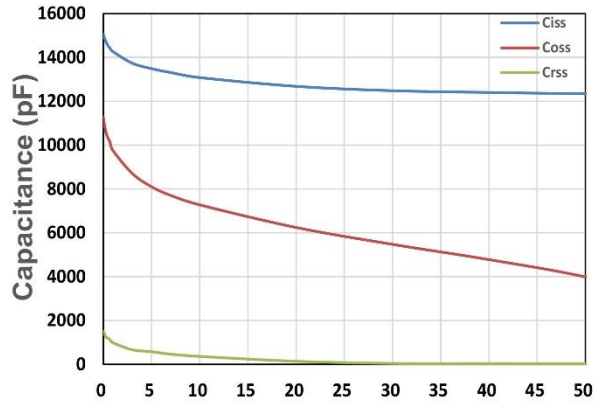
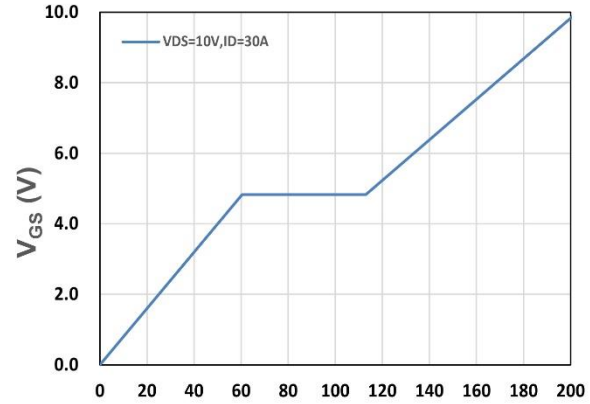


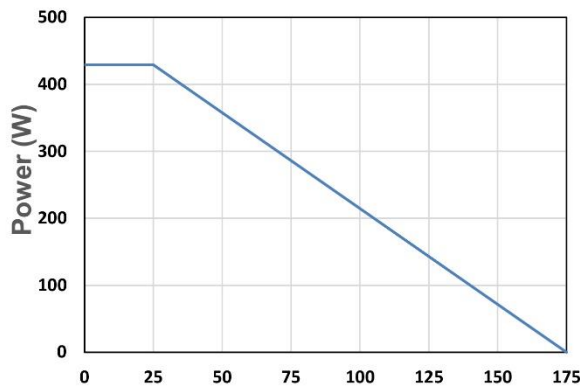
Figure 6. Source-Drain Diode Forward



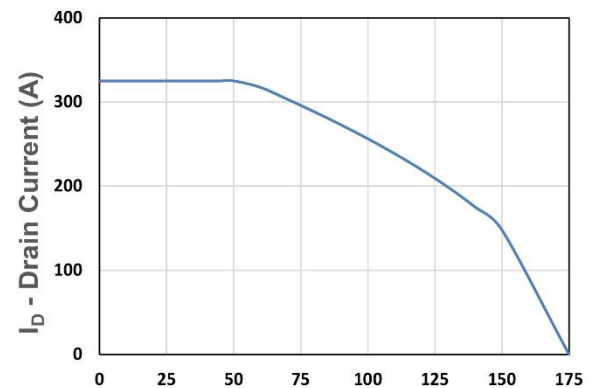
V_{DS} - Drain - Source Voltage (V)
Figure 7. Capacitance



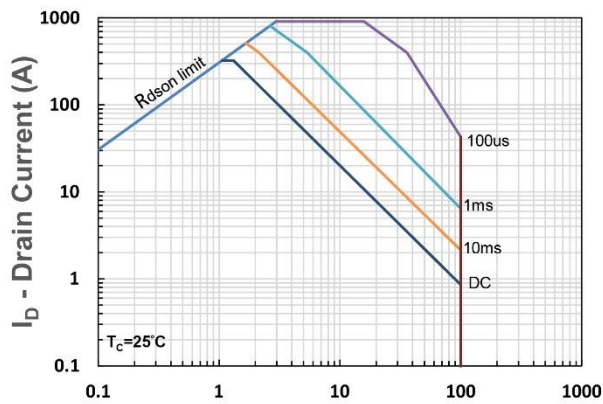
Qg , Total Gate Charge (nC)
Figure 8. Gate Charge Characteristics



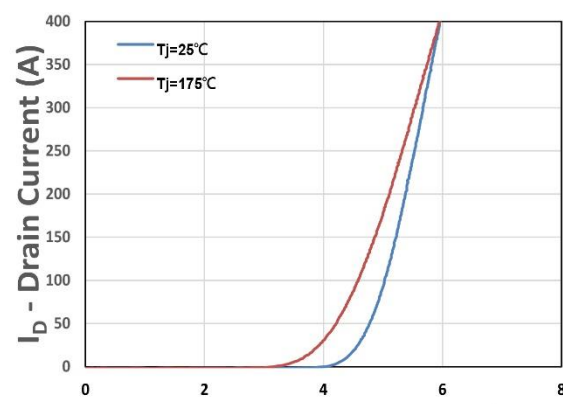
T_c - Case Temperature (°C)
Figure 9. Power Dissipation



T_c - Case Temperature (°C)
Figure 10. Drain Current



V_{DS} - Drain-Source Voltage (V)
Figure 11. Safe Operating Area



V_{GS} - Gate - Source Voltage (V)
Figure 12. Transfer Characteristics

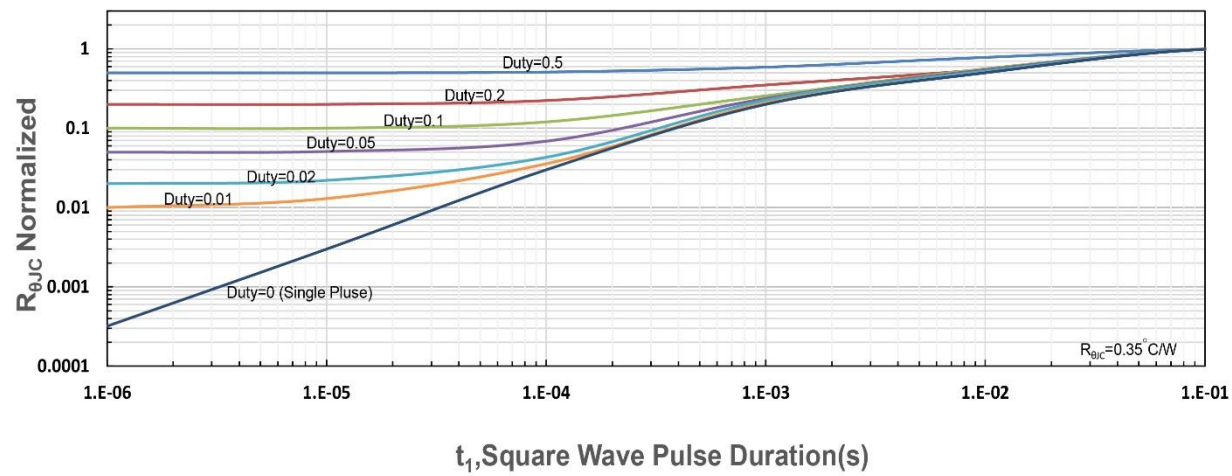


Figure 13. $R_{\theta JC}$ Transient Thermal Impedance