



Power MOSFETS

DATASHEET

LM1A092NAO2A

N-Channel
Enhancement Mode MOSFET

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Quality Management Systems
ISO 9001:2015 Certificate

LM1A092NAO2A

N-Channel Enhancement Mode MOSFET

Pin Description

Product Summary

TO252-2L(Top view)	Symbol	Symbol	N-Channel	Unit
			V _{DSS}	100
			R _{DS(ON)-Max}	9.7
			I _D	78

Feature

- Fast switching speed
- Reliable and Rugged
- ROHS Compliant & Halogen-Free
- 100% UIS and R_g Tested

Applications

- Power Management in DC/DC Converters
- USB Power Delivery (USB PD)

Ordering Information

Orderable Part Number	Package Type	Form	Shipping	Marking
LM1A092NAO2A	TO252-2L	Tape & Reel	3000 / Tape & Reel	1A092 <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>

Note : = Lot Code

Absolute Maximum Ratings (T_J=25°C Unless Otherwise Noted)

Symbol	Parameter		N-Channel	Unit
V _{DSS}	Drain-Source Voltage		100	V
V _{GSS}	Gate-Source Voltage		±20	
T _J	Maximum Junction Temperature		150	°C
T _{STG}	Storage Temperature Range		-55 to 150	°C
I _S	Diode Continuous Forward Current	T _c =25°C	54	A
I _{DM} ^①	Pulse Drain Current Tested	T _c =25°C	313	A
I _D	Continuous Drain Current	T _c =25°C	78	
		T _c =100°C	50	A
P _D	Maximum Power Dissipation	T _c =25°C	60	
		T _c =100°C	24	W
I _D	Continuous Drain Current	T _A =25°C	14.6	
		T _A =70°C	11.7	A
P _D	Maximum Power Dissipation	T _A =25°C	2.1	
		T _A =70°C	1.3	W
I _{AS} ^②	Avalanche Current, Single pulse	L=0.1mH	27	
		L=0.5mH	16	A
E _{AS} ^②	Avalanche Energy, Single pulse	L=0.1mH	36	
		L=0.5mH	64	mJ

Thermal Characteristics

Symbol	Parameter		Rating	Unit
R _{θJC}	Thermal Resistance-Junction to Case	Steady State	2.1	°C/W
R _{θJA} ^③	Thermal Resistance-Junction to Ambient	Steady State	60	°C/W

Note ① : Max. current is limited by bonding wire

Note ② : UIS tested and pulse width are limited by maximum junction temperature 150°C

Note ③ : Surface Mounted on 1in² FR-4 board with 1oz

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N-Channel Electrical Characteristics ($T_J=25^\circ\text{C}$ Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Static Electrical Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_{\text{DS}}=250\mu\text{A}$	100	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}}=80\text{V}$, $V_{\text{GS}}=0\text{V}$	-	-	1	μA
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}$, $I_{\text{DS}}=250\mu\text{A}$	1	2	3	V
I_{GSS}	Gate Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	-	-	± 100	nA
$R_{\text{DS(ON)}}^{\circledast}$	Drain-Source On-state Resistance	$V_{\text{GS}}=10\text{V}$, $I_{\text{DS}}=20\text{A}$	-	8.1	9.7	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}$, $I_{\text{DS}}=20\text{A}$		11	14	
g_{fs}	Forward Transconductance	$V_{\text{DS}}=5\text{V}$, $I_{\text{DS}}=10\text{A}$	-	23	-	S
Dynamic Characteristics [®]						
R_{G}	Gate Resistance	$V_{\text{GS}}=0\text{V}$, $V_{\text{DS}}=0\text{V}$, Freq.=1MHz	-	1	-	Ω
C_{iss}	Input Capacitance	$V_{\text{GS}}=0\text{V}$, $V_{\text{DS}}=50\text{V}$, Freq.=1MHz	-	1916	-	pF
C_{oss}	Output Capacitance		-	504	-	
C_{rss}	Reverse Transfer Capacitance		-	35	-	
$t_{\text{d(ON)}}$	Turn-on Delay Time	$V_{\text{GS}}=10\text{V}$, $V_{\text{DS}}=50\text{V}$, $I_{\text{D}}=1\text{A}$, $R_{\text{GEN}}=6\Omega$	-	9.1	-	nS
t_{r}	Turn-on Rise Time		-	17.5	-	
$t_{\text{d(OFF)}}$	Turn-off Delay Time		-	32.2	-	
t_{f}	Turn-off Fall Time		-	72	-	
Q_{g}	Total Gate Charge	$V_{\text{GS}}=4.5\text{V}$, $V_{\text{DS}}=50\text{V}$ $I_{\text{D}}=20\text{A}$	-	22	-	nC
Q_{g}	Total Gate Charge	$V_{\text{GS}}=10\text{V}$, $V_{\text{DS}}=50\text{V}$, $I_{\text{D}}=20\text{A}$	-	41	-	
Q_{gs}	Gate-Source Charge		-	9	-	
Q_{gd}	Gate-Drain Charge		-	10.5	-	
Source-Drain Characteristics						
$V_{\text{SD}}^{\circledast}$	Diode Forward Voltage	$I_{\text{SD}}=10\text{A}$, $V_{\text{GS}}=0\text{V}$	-	0.8	1.1	V
t_{rr}	Reverse Recovery Time	$I_{\text{F}}=10\text{A}$, $V_{\text{R}}=50\text{V}$	-	37.2	-	nS
Q_{rr}	Reverse Recovery Charge	$dI_{\text{F}}/dt=100\text{A}/\mu\text{s}$	-	35	-	nC

Note ④ : Pulse test (pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$).

Note ⑤ : Guaranteed by design, not subject to production testing.

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N-Channel Typical Characteristics

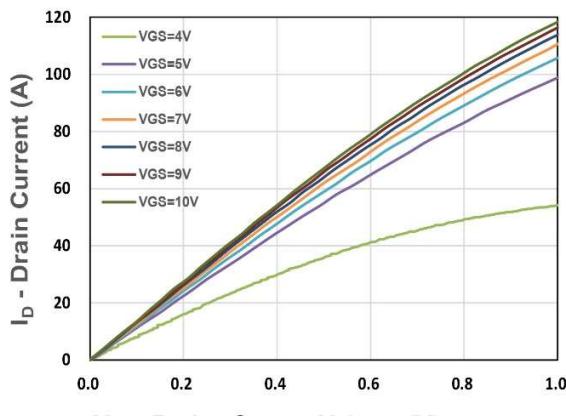


Figure 1. Output Characteristics

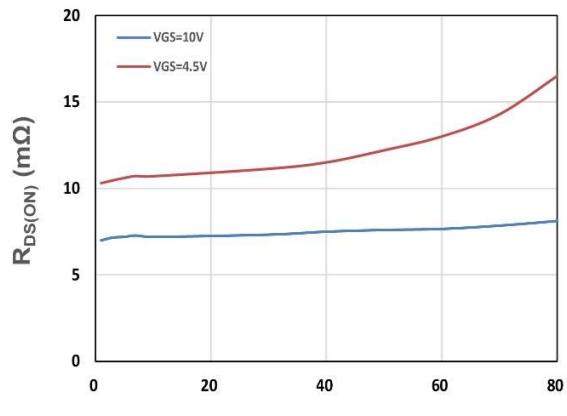


Figure 2. On-Resistance vs. ID

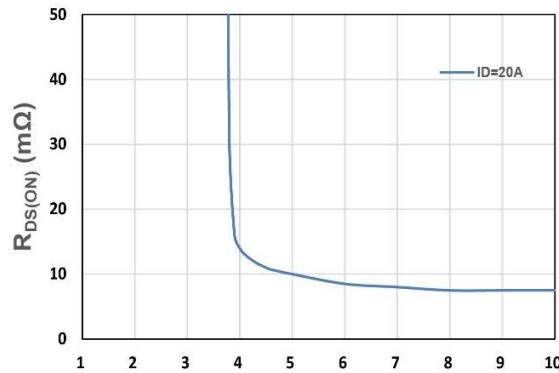


Figure 3. On-Resistance vs. VGS

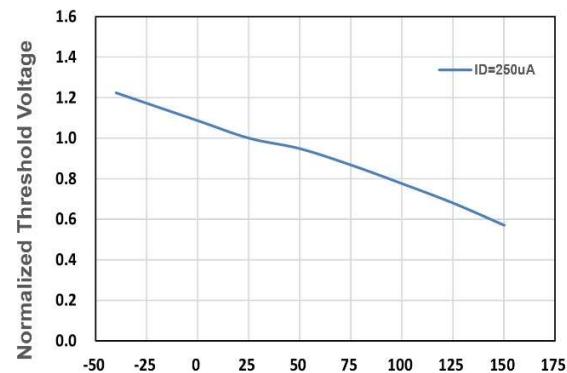


Figure 4. Gate Threshold Voltage

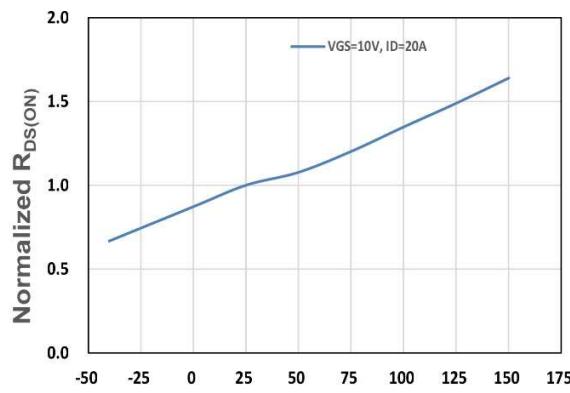


Figure 5. Drain-Source On Resistance

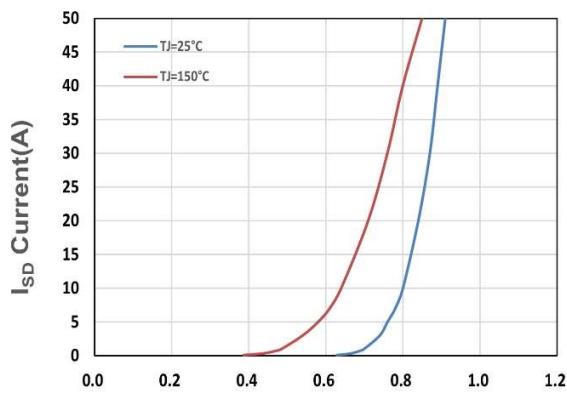
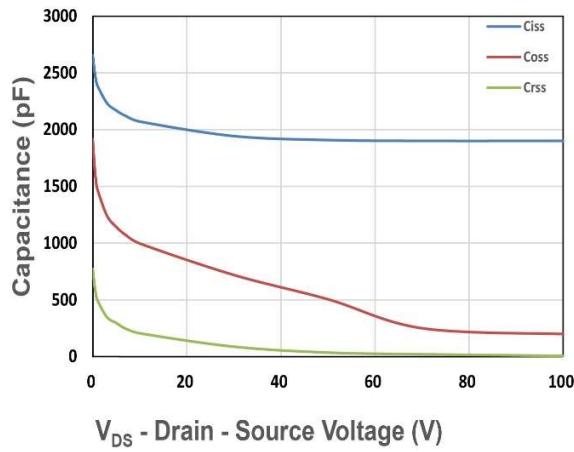


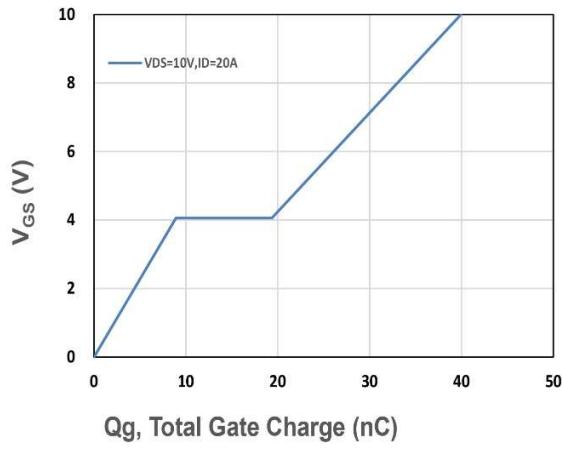
Figure 6. Source-Drain Diode Forward

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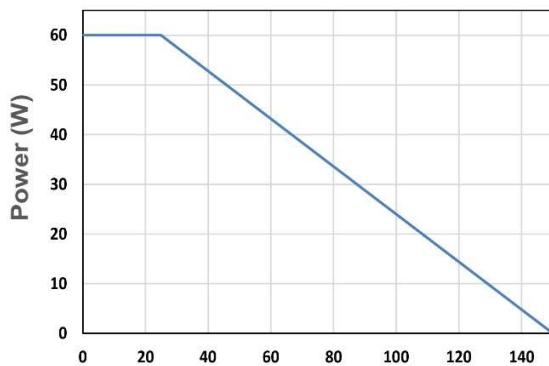
V_{DS} - Drain - Source Voltage (V)

Figure 7. Capacitance



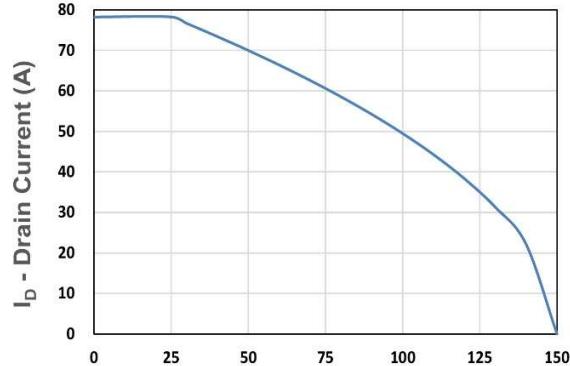
Q_g , Total Gate Charge (nC)

Figure 8. Gate Charge Characteristics



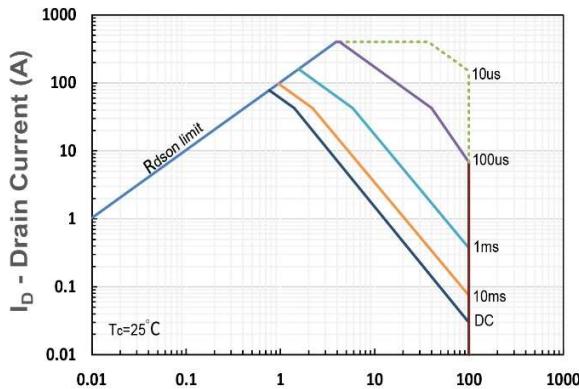
T_c - Junction Temperature (°C)

Figure 9. Power Dissipation



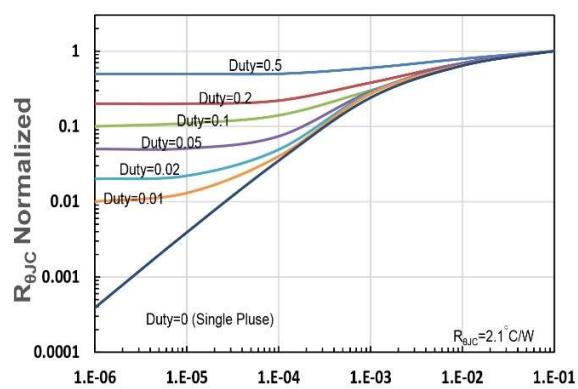
T_c - Junction Temperature (°C)

Figure 10. Drain Current



V_{DS} - Drain-Source Voltage (V)

Figure 11. Safe Operating Area



t_1 , Square Wave Pulse Duration(s)

Figure 12. $R_{\theta JC}$ Transient Thermal Impedance