



# Power MOSFETS

## DATASHEET

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**LM30C50NGA3A**

N-Channel  
Enhancement Mode MOSFET

- Leadpower-semiconductor Corp., Ltd
- sales@leadpower-semi.com
- (03) 6577339 FAX : (03) 6577229
- [www.leadpower-semi.com](http://www.leadpower-semi.com)

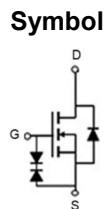
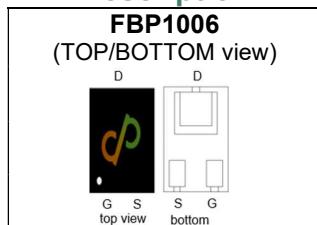


Quality Management Systems  
ISO 9001:2015 Certificate

# LM30C50NGA3A

## N-Channel Enhancement Mode MOSFET

### Pin Description



### Product Summary

Symbol	N-Channel	Unit
$V_{DSS}$	30	V
$R_{DS(ON)-Max}$	420	$m\Omega$
ID	1	A

### Feature

- Fast switching speed
- Reliable and Rugged
- ROHS Compliant & Halogen-Free
- ESD Protection

### Applications

- Portable Equipment
- Battery Powered System
- Load Switch

### Ordering Information

Orderable Part Number	Package Type	Form	Shipping	Marking
LM30C50NGA3A	FBP1006	Tape & Reel	10000/ Tape & Reel	□5

Note : □ = Lot Code

### Absolute Maximum Ratings ( $T_J=25^\circ C$ Unless Otherwise Noted)

Symbol	Parameter		N-Channel	Unit
$V_{DSS}$	Drain-Source Voltage	$T_A=25^\circ C$	30	V
$V_{GSS}$	Gate-Source Voltage		$\pm 12$	
$T_J$	Maximum Junction Temperature		150	$^\circ C$
$T_{STG}$	Storage Temperature Range		-55 to 150	$^\circ C$
$I_S$	Diode Continuous Forward Current	$T_A=25^\circ C$	0.6	A
$I_{DM}^{\circledR}$	Pulse Drain Current Tested	$T_A=25^\circ C$	2.5	A
$I_D$	Continuous Drain Current	$T_A=25^\circ C$	1	A
		$T_A=70^\circ C$	0.8	
$P_D$	Maximum Power Dissipation	$T_A=25^\circ C$	0.69	W
		$T_A=70^\circ C$	0.44	
$I_{AS}^{\circledR}$	Avalanche Current, Single pulse	$L=0.1mH$	1.8	A
$E_{AS}^{\circledR}$	Avalanche Energy, Single pulse	$L=0.1mH$	0.16	$mJ$

### Thermal Characteristics

Symbol	Parameter		Rating	Unit
$R_{QJA}^{\circledR}$	Thermal Resistance-Junction to Ambient	Steady State	180	$^\circ C/W$

Note ① : Max. current is limited by junction temperature.

Note ② : UIS tested and pulse width are limited by maximum junction temperature  $150^\circ C$

Note ③ : Surface Mounted on 1in<sup>2</sup> FR-4 board with 1oz.

### N-Channel Electrical Characteristics ( $T_J=25^\circ C$ Unless Otherwise Noted)

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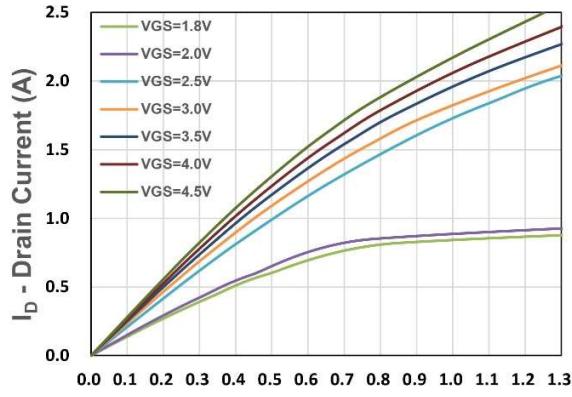
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
<b>Static Electrical Characteristics</b>						
<b>BV<sub>DSS</sub></b>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>DS</sub> =250uA	30	-	-	V
<b>I<sub>DSS</sub></b>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =24V, V <sub>GS</sub> =0V	-	-	1	uA
<b>V<sub>GS(th)</sub></b>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>DS</sub> =250uA	0.6	0.95	1.3	V
<b>I<sub>GSS</sub></b>	Gate Leakage Current	V <sub>GS</sub> =±12V, V <sub>DS</sub> =0V	-	-	±10	uA
<b>R<sub>DS(on)</sub><sup>④</sup></b>	Drain-Source On-state Resistance	V <sub>GS</sub> =4.5V, I <sub>DS</sub> =0.5A	-	350	420	mΩ
		V <sub>GS</sub> =2.5V, I <sub>DS</sub> =0.3A	-	435	565	
		V <sub>GS</sub> =1.8V, I <sub>DS</sub> =0.1A	-	740	850	
<b>g<sub>f</sub>s</b>	Forward Transconductance	V <sub>DS</sub> =5V, I <sub>DS</sub> =0.25A	-	1.2	-	S
<b>Dynamic Characteristics<sup>⑤</sup></b>						
<b>R<sub>G</sub></b>	Gate Resistance	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, Freq.=1MHz	-	194	-	Ω
<b>C<sub>iss</sub></b>	Input Capacitance	V <sub>GS</sub> =0V, V <sub>DS</sub> =15V, Freq.=1MHz	-	38.1	-	pF
<b>C<sub>oss</sub></b>	Output Capacitance		-	4.8	-	
<b>C<sub>rss</sub></b>	Reverse Transfer Capacitance		-	3.4	-	
<b>t<sub>d(on)</sub></b>	Turn-on Delay Time	V <sub>GS</sub> =4.5V, V <sub>DS</sub> =15V, I <sub>D</sub> =1A, R <sub>GEN</sub> =6Ω	-	5.3	-	nS
<b>t<sub>r</sub></b>	Turn-on Rise Time		-	20.2	-	
<b>t<sub>d(off)</sub></b>	Turn-off Delay Time		-	34.5	-	
<b>t<sub>f</sub></b>	Turn-off Fall Time		-	30.3	-	
<b>Q<sub>g</sub></b>	Total Gate Charge	V <sub>GS</sub> =4.5V, V <sub>DS</sub> =15V, I <sub>D</sub> =1A	-	0.85	-	nC
<b>Q<sub>gs</sub></b>	Gate-Source Charge		-	0.29	-	
<b>Q<sub>gd</sub></b>	Gate-Drain Charge		-	0.16	-	
<b>Source-Drain Characteristics</b>						
<b>V<sub>SD</sub><sup>④</sup></b>	Diode Forward Voltage	I <sub>SD</sub> =0.25A, V <sub>GS</sub> =0V	-	0.9	1.1	V
<b>t<sub>rr</sub></b>	Reverse Recovery Time	I <sub>F</sub> =0.25A, V <sub>R</sub> =20V	-	24.1	-	nS
<b>Q<sub>rr</sub></b>	Reverse Recovery Charge	dl <sub>F</sub> /dt=100A/μs	-	3.2	-	nC

Note ④ : Pulse test (pulse width≤300us, duty cycle≤2%).

Note ⑤ : Guaranteed by design, not subject to production testing.

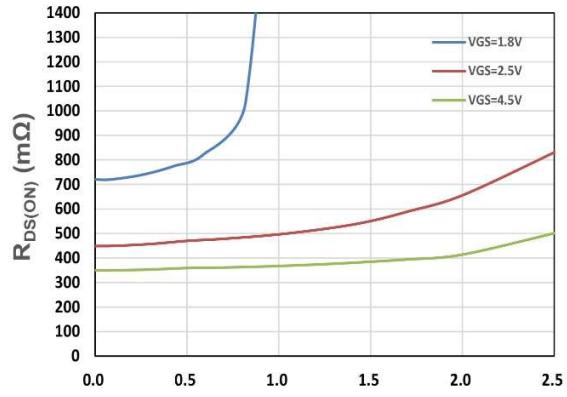
## N-Channel Typical Characteristics

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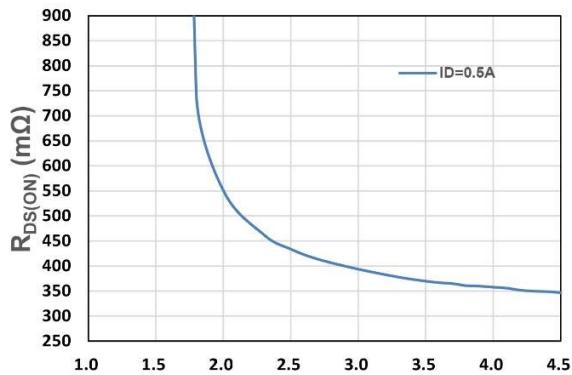
$V_{DS}$  - Drain - Source Voltage (V)

Figure 1. Output Characteristics



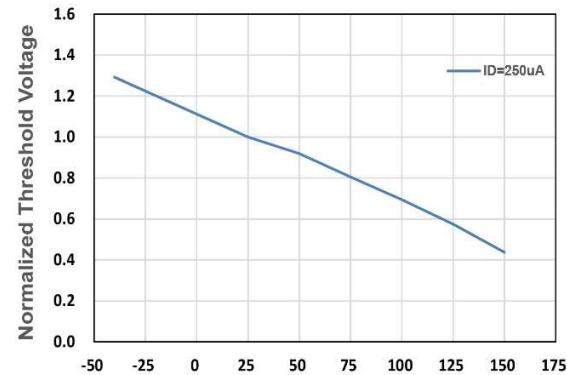
$I_D$ - Drain Current (A)

Figure 2. On-Resistance vs. ID



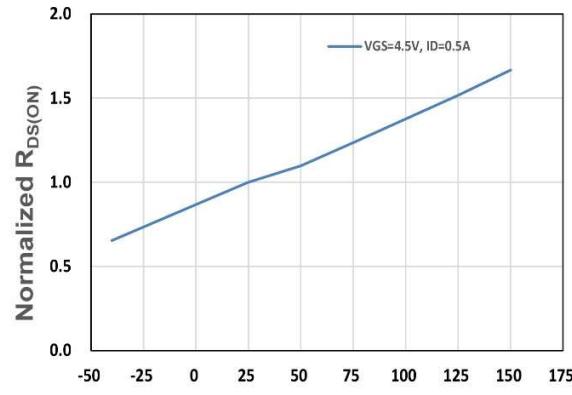
$V_{GS}$  - Gate - Source Voltage (V)

Figure 3. On-Resistance vs. VGS



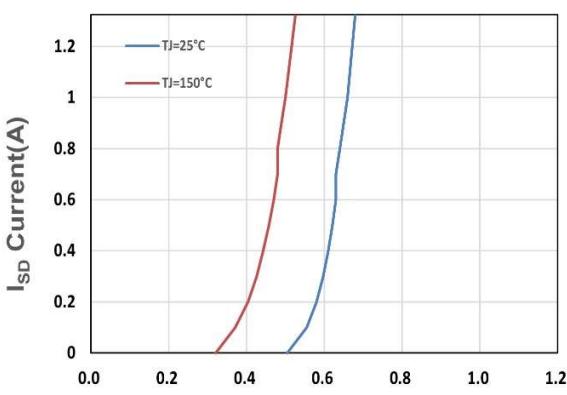
$T_j$ , Junction Temperature(°C)

Figure 4. Gate Threshold Voltage



$T_j$  , Junction Temperature(°C)

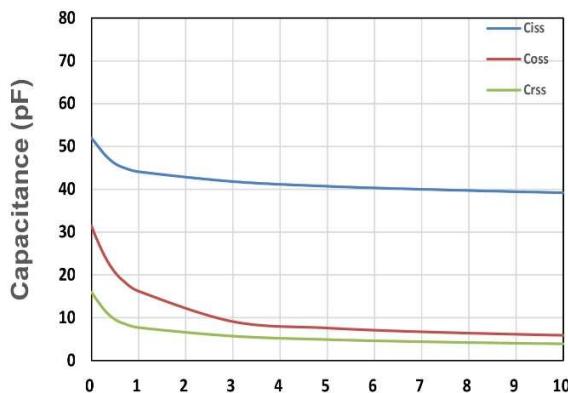
Figure 5. Drain-Source On Resistance



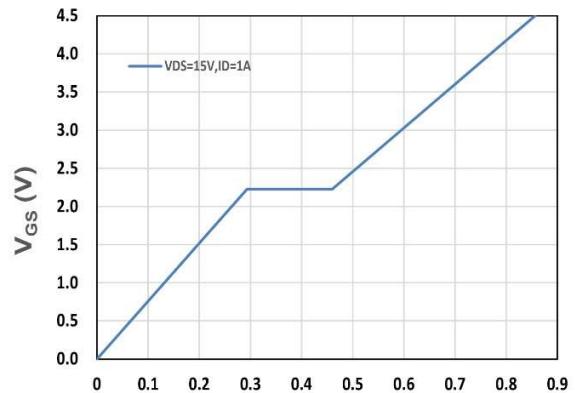
$V_{SD}$ , Source-Drain Voltage(V)

Figure 6. Source-Drain Diode Forward

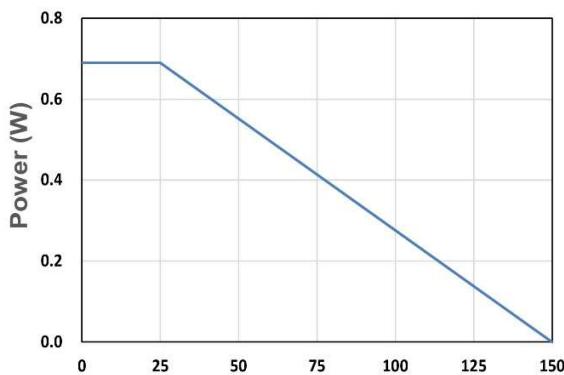
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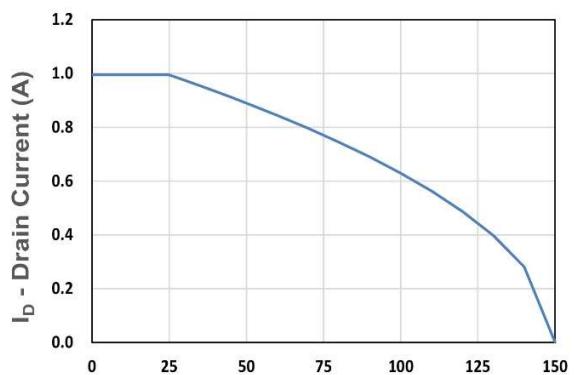
V<sub>DS</sub> - Drain - Source Voltage (V)  
Figure 7. Capacitance



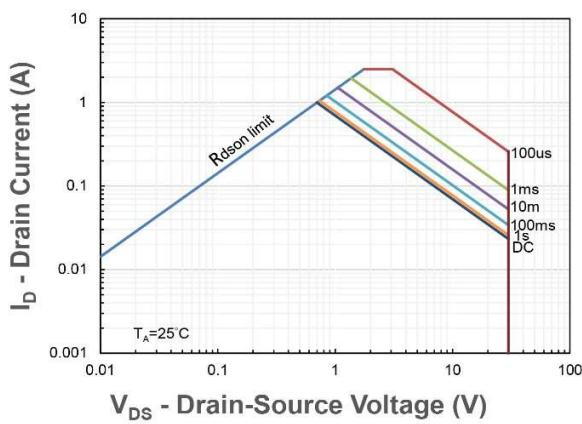
Q<sub>g</sub>, Total Gate Charge (nC)  
Figure 8. Gate Charge Characteristics



T<sub>A</sub> - Ambient Temperature (°C)  
Figure 9. Power Dissipation



T<sub>A</sub> - Ambient Temperature (°C)  
Figure 10. Drain Current



V<sub>DS</sub> - Drain-Source Voltage (V)

Figure 11. Safe Operating Area

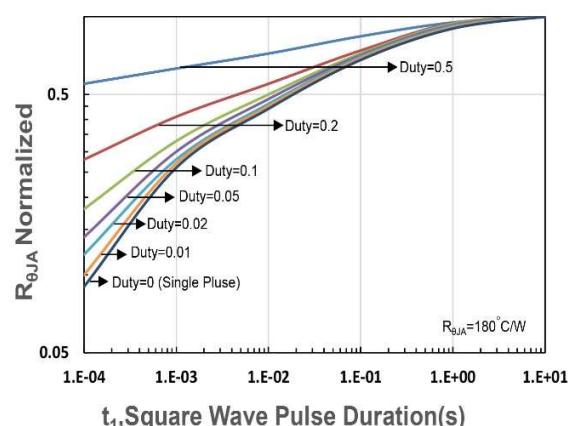


Figure 12. R<sub>θJA</sub> Transient Thermal Impedance