



Power MOSFETS

PRELIMINARY DATASHEET

LM40013NHM8A-Q

N-Channel
Enhancement Mode MOSFET



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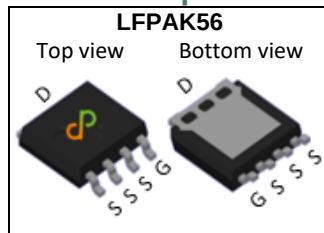


Quality Management Systems

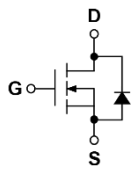
ISO 9001:2015 Certificate

N-Channel Enhancement Mode MOSFET

Pin Description



Symbol



Product Summary

Symbol	N-Channel	Unit
V_{DS}	40	V
$R_{DS(ON)-Typ.}$	0.8	m Ω
ID	259	A

Feature

- Fast switching speed
- Reliable and Rugged
- ROHS Compliant & Halogen-Free
- 100% UIS and Rg Tested
- AEC-Q101 qualified

Applications

- DC-to-DC converters
- Switch Mode Power Supply
- Brushless DC motor control

Ordering Information

Orderable Part Number	Package Type	Form	Shipping	Marking
LM40013NHM8A-Q	LFPK56	Tape & Reel	4000 / Tape & Reel	40013Q □□□□□□

Note : □□□□□□ = Lot Code

Absolute Maximum Ratings ($T_J=25^\circ\text{C}$ Unless Otherwise Noted)

Symbol	Parameter	N-Channel	Unit
V_{DS}	Drain-Source Voltage	40	V
V_{GS}	Gate-Source Voltage	± 20	V
T_J	Maximum Junction Temperature	175	$^\circ\text{C}$
T_{STG}	Storage Temperature Range	-55 to 175	$^\circ\text{C}$
I_S	Diode Continuous Forward Current	$T_C=25^\circ\text{C}$	81 A
I_{SP}	Diode Pulse Current	$T_C=25^\circ\text{C}$	400 ^① A
I_{DM}	Pulse Drain Current Tested	$T_C=25^\circ\text{C}$	646 ^① A
I_D	Continuous Drain Current	$T_C=25^\circ\text{C}$	259 A
		$T_C=100^\circ\text{C}$	183 A
P_D	Maximum Power Dissipation	$T_C=25^\circ\text{C}$	125 W
		$T_C=100^\circ\text{C}$	63 W
I_D	Continuous Drain Current	$T_A=25^\circ\text{C}$	42 A
		$T_A=70^\circ\text{C}$	35 A
P_D	Maximum Power Dissipation	$T_A=25^\circ\text{C}$	3.3 W
		$T_A=70^\circ\text{C}$	1.8 W
$I_{AS}^{②}$	Avalanche Current, Single pulse	L=0.1mH	60 A
		L=0.5mH	32 A
$E_{AS}^{②}$	Avalanche Energy, Single pulse	L=0.1mH	180 mJ
		L=0.5mH	256 mJ

Thermal Characteristics

Symbol	Parameter	Rating	Unit
$R_{\theta JC}$	Thermal Resistance-Junction to Case	Steady State	1.2 $^\circ\text{C/W}$
$R_{\theta JA}^{③}$	Thermal Resistance-Junction to Ambient	Steady State	45 $^\circ\text{C/W}$

Note ① : Max. current is limited by junction temperature

Note ② : UIS tested and pulse width are limited by maximum junction temperature 175 $^\circ\text{C}$

Note ③ : Surface Mounted on 1in² FR-4 board with 1oz

N-Channel Electrical Characteristics ($T_J=25^{\circ}\text{C}$ Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Static Electrical Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =250uA	40	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =32V, V _{GS} =0V	-	-	1	uA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250uA	-	3	-	V
I _{GSS}	Gate Leakage Current	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
R _{DS(ON)} ^④	Drain-Source On-state Resistance	V _{GS} =10V, I _{DS} =20A	-	0.8	-	mΩ
gfs	Forward Transconductance	V _{DS} =5V, I _{DS} =10A	-	46	-	S
Dynamic Characteristics ^⑤						
R _G	Gate Resistance	V _{GS} =0V,V _{DS} =0V, Freq.=1MHz	-	1	-	Ω
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =20V, Freq.=1MHz	-	4954	-	pF
C _{oss}	Output Capacitance		-	1543	-	
C _{rss}	Reverse Transfer Capacitance		-	98	-	
td(ON)	Turn-on Delay Time	V _{GS} =10V,V _{DS} =20V, Id=1A,RGEN=1Ω	-	20	-	nS
tr	Turn-on Rise Time		-	12	-	
td(OFF)	Turn-off Delay Time		-	40	-	
tf	Turn-off Fall Time		-	97	-	
Qg	Total Gate Charge	V _{GS} =10V,V _{DS} =20V, Id=20A	-	64	-	nC
Qgs	Gate-Source Charge		-	26	-	
Qgd	Gate-Drain Charge		-	9.9	-	
Source-Drain Characteristics						
VSD ^④	Diode Forward Voltage	ISD=10A, VGS=0V	-	0.75	-	V
trr	Reverse Recovery Time	IF=10A, VR=20V	-	61	-	nS
Qrr	Reverse Recovery Charge	dIF/dt=100A/μs	-	52	-	nC

Note ④ : Pulse test (pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$).

Note ⑤ : Guaranteed by design, not subject to production testing.

N-Channel Typical Characteristics

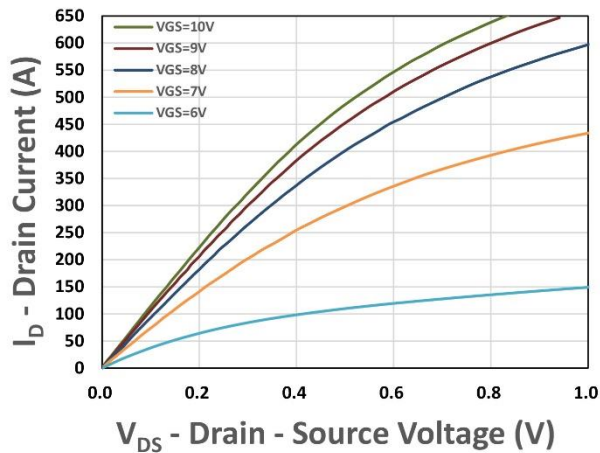


Figure 1. Output Characteristics

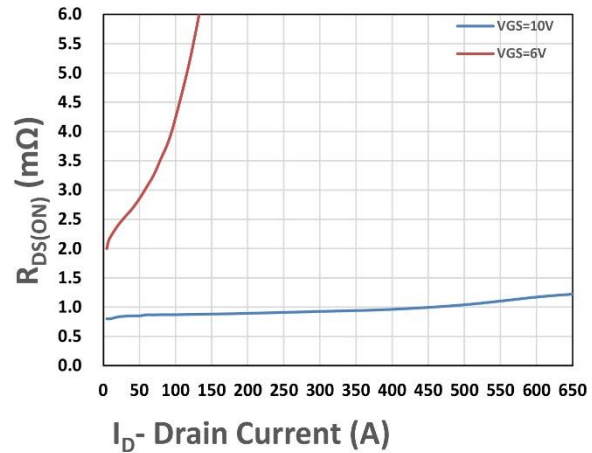


Figure 2. On-Resistance vs. ID

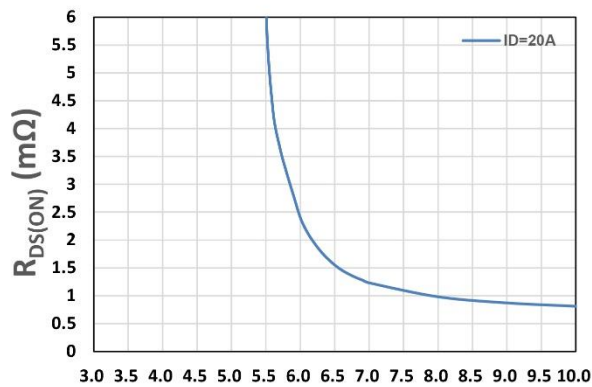


Figure 3. On-Resistance vs. V_{GS}

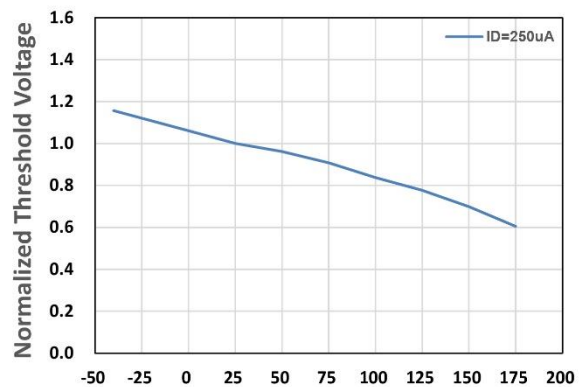


Figure 4. Gate Threshold Voltage

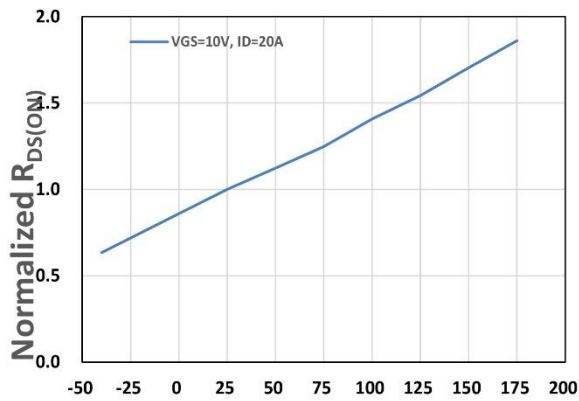


Figure 5. Drain-Source On Resistance

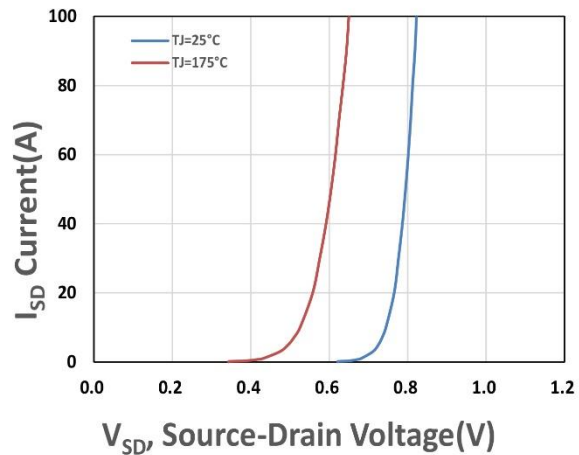
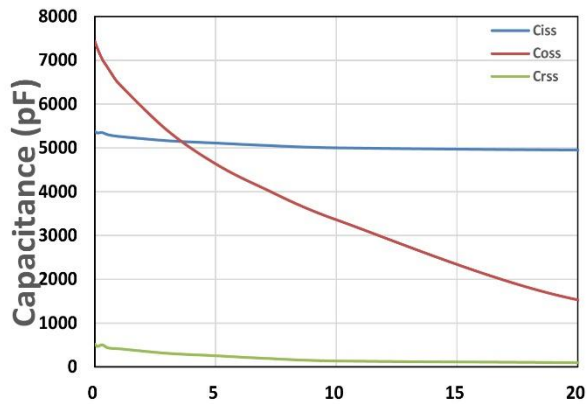
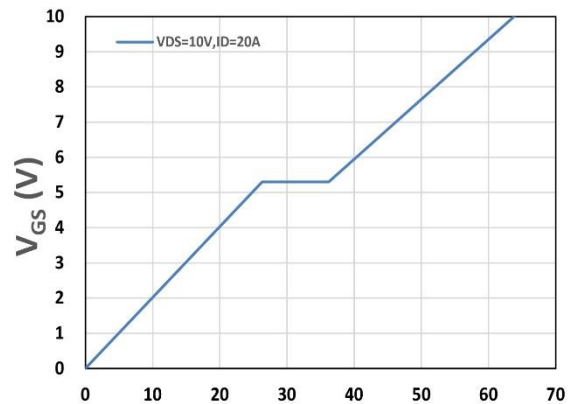


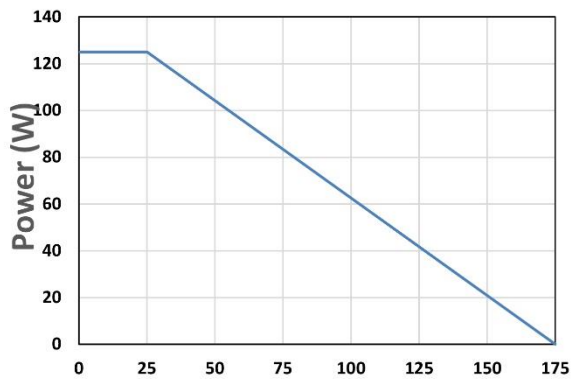
Figure 6. Source-Drain Diode Forward



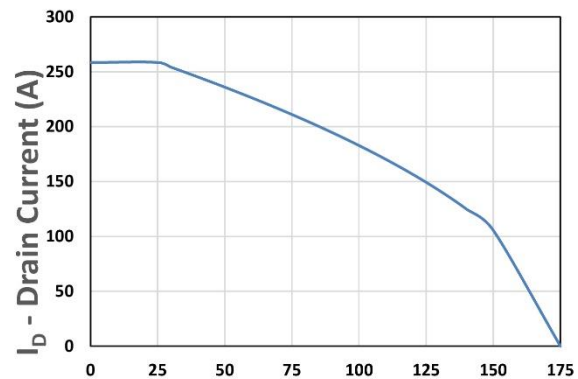
V_{DS} - Drain - Source Voltage (V)
Figure 7. Capacitance



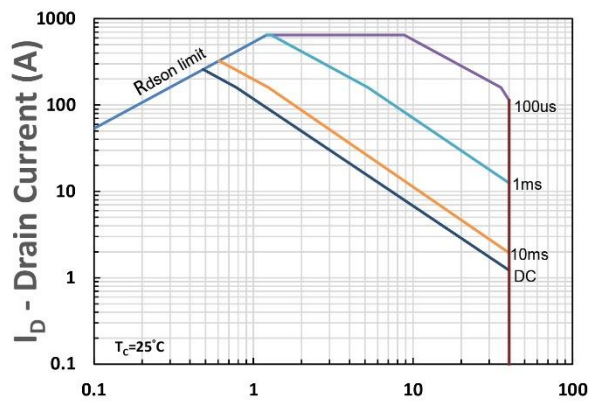
Q_g , Total Gate Charge (nC)
Figure 8. Gate Charge Characteristics



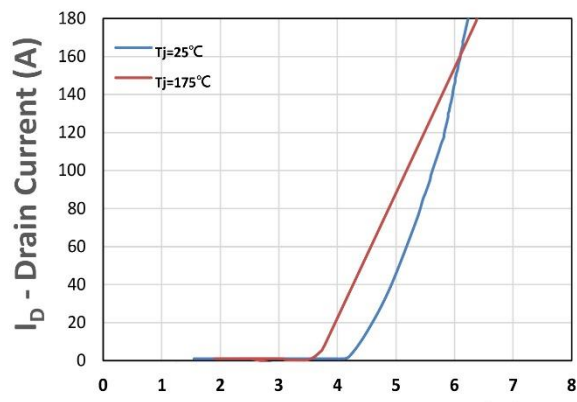
T_c - Case Temperature ($^{\circ}\text{C}$)
Figure 9. Power Dissipation



T_c - Case Temperature ($^{\circ}\text{C}$)
Figure 10. Drain Current



V_{DS} - Drain-Source Voltage (V)
Figure 11. Safe Operating Area



V_{GS} - Gate - Source Voltage (V)
Figure 12. Transfer Characteristics

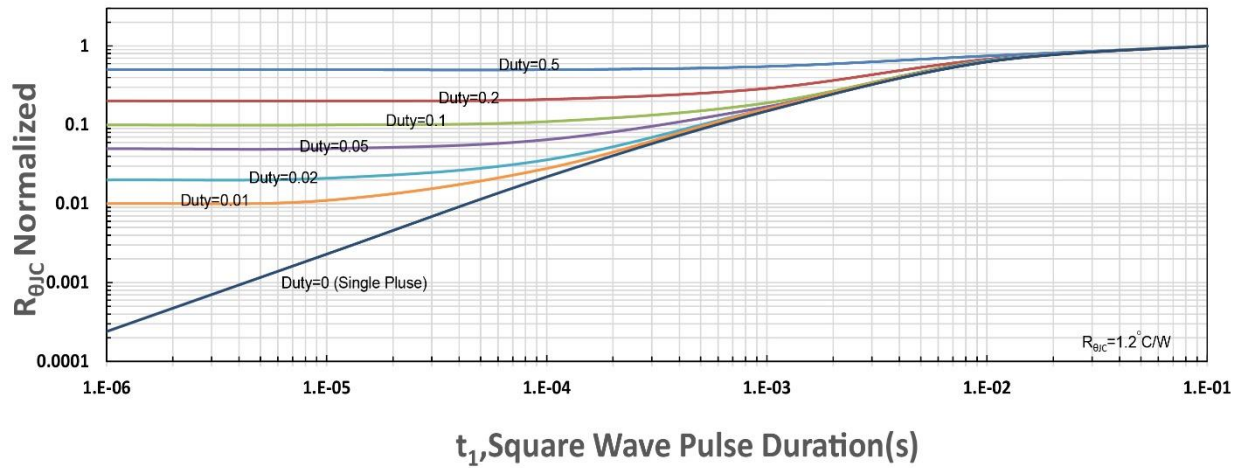


Figure 13. $R_{\theta JC}$ Transient Thermal Impedance